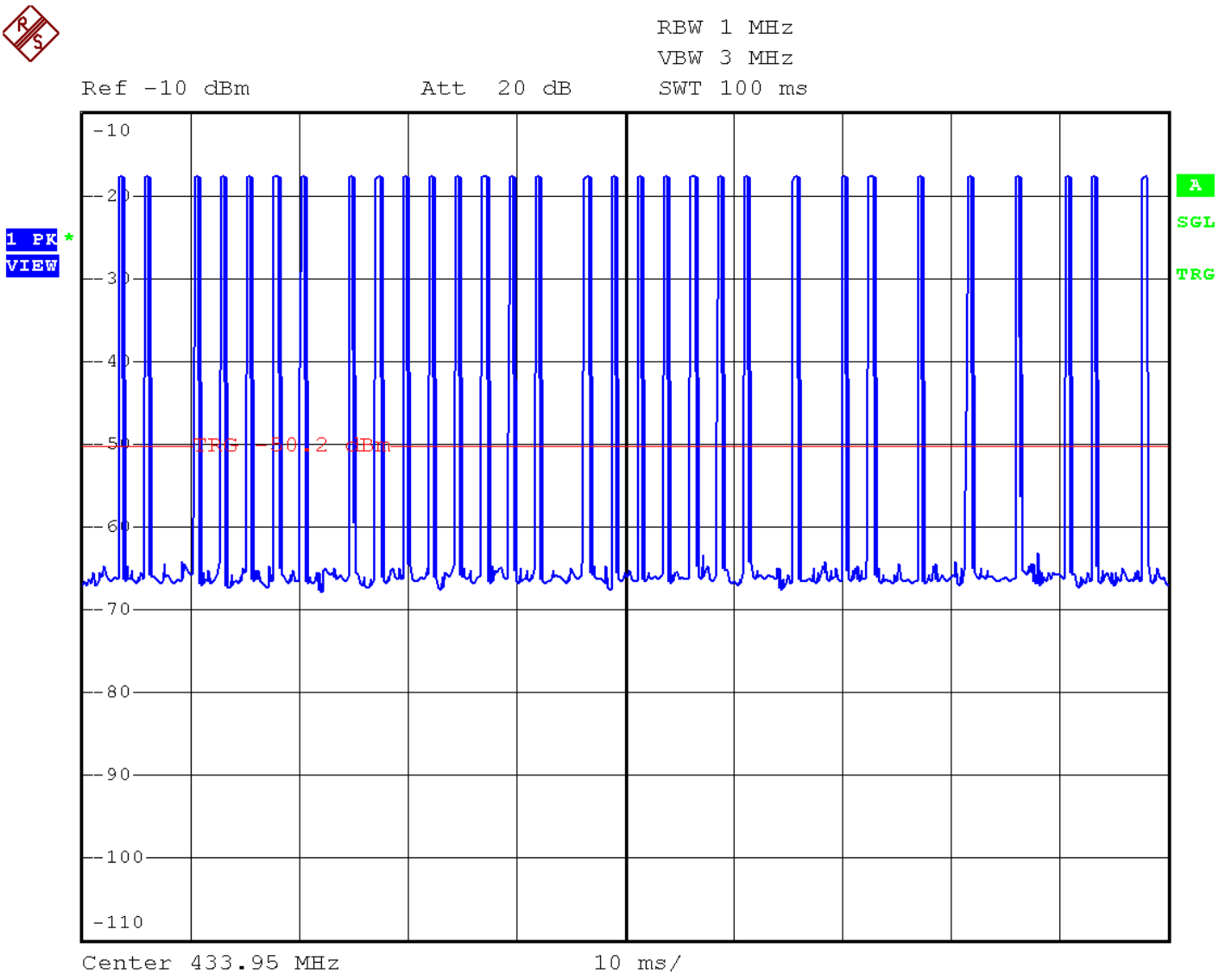
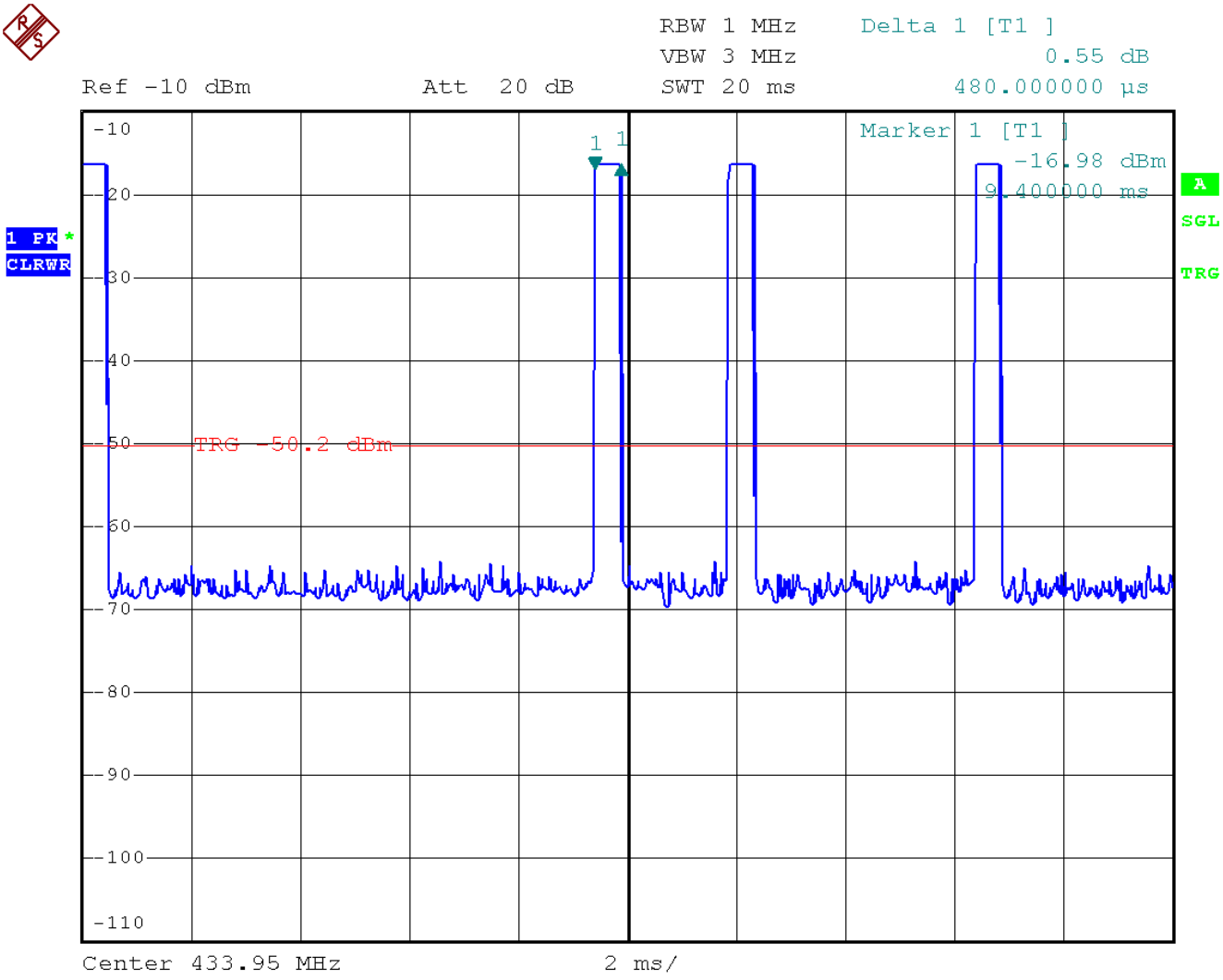


Timing Plot – Pursuant to FCC Part 15 Section 15. 231 Requirement



Duration of one cycle = 100ms

Effective Period of the cycle = 31 x 480us = 14880us = 14.88ms

Duty Cycle = Effective Period / Duration of one cycle

= 14.88ms / 100ms

= 0.1488

Average Factor = 20 x log (Duty Cycle)

= 20 x log (0.1488)

= -16.5479dB

Timing Plot – Pursuant to FCC Part 15 Section 15. 231 Requirement