

INTERTEK TESTING SERVICES

Report No. : HK11081225-1
Sample No. : 1/2

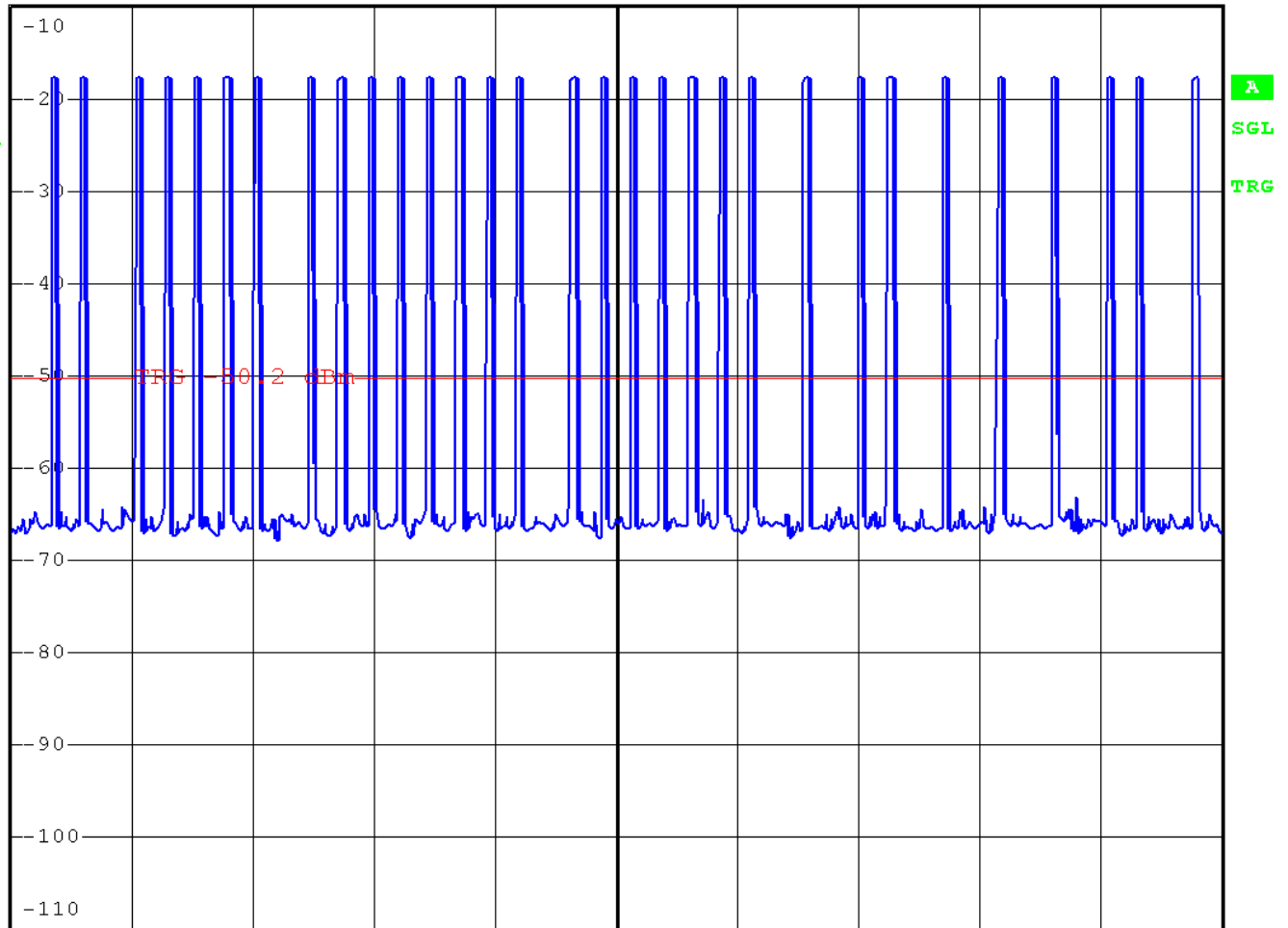


RBW 1 MHz
VBW 3 MHz
SWT 100 ms

Ref -10 dBm

Att 20 dB

1 PK*
VIEW



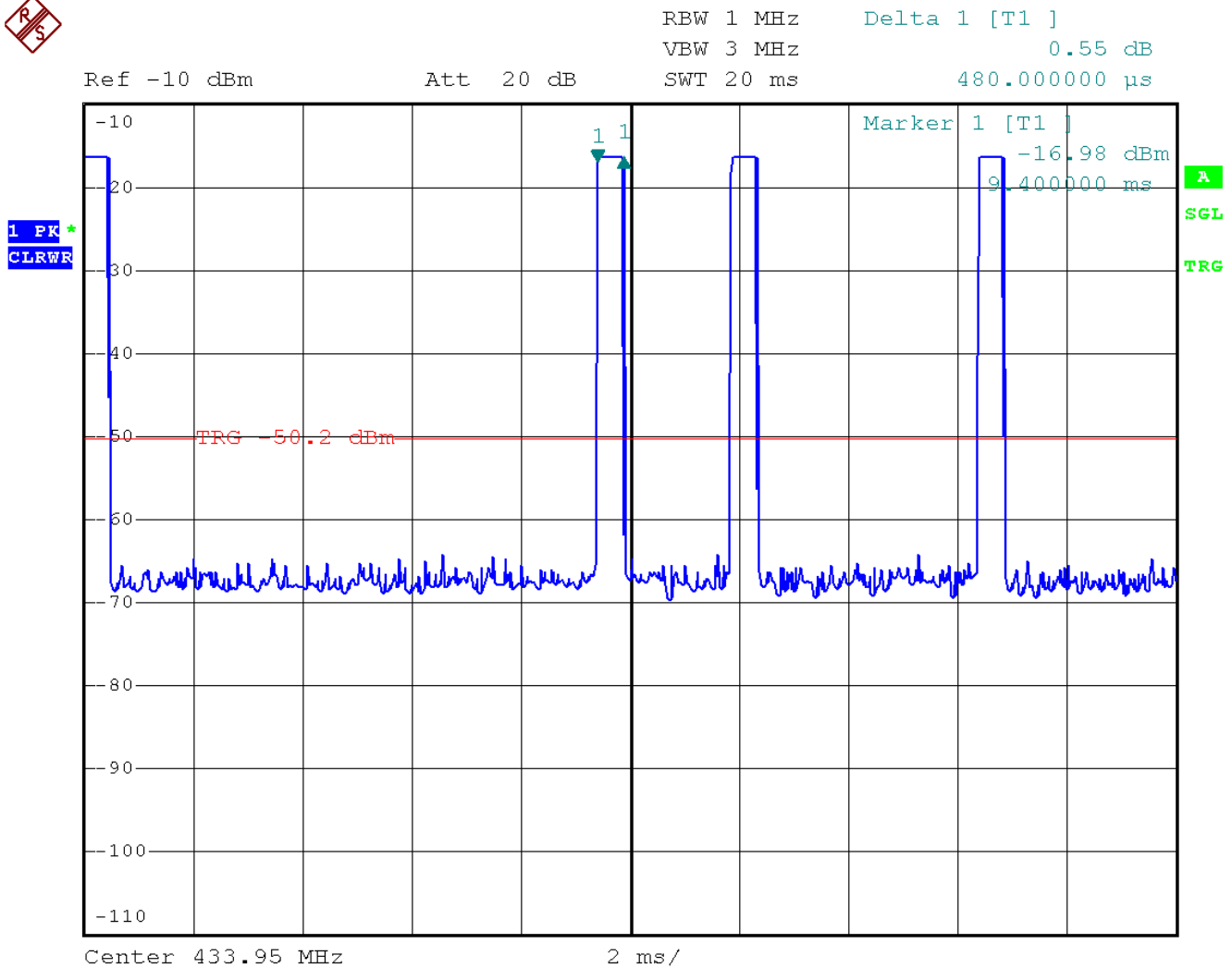
Center 433.95 MHz

10 ms/

Timing Plot – Pursuant to FCC Part 15 Section 15. 231 Requirement

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Duration of one cycle = 100ms

Effective Period of the cycle = $31 \times 480\mu s = 14880\mu s = 14.88ms$

Duty Cycle = Effective Period / Duration of one cycle

= $14.88ms / 100ms$

= 0.1488

Average Factor = $20 \times \log (\text{Duty Cycle})$

= $20 \times \log (0.1488)$

= -16.5479dB

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