

Circuit Description

1. The IMAP200 chip is master micro controller, the oscillator component X1 generated a frequency of 32.768kHz as base clock frequency, them IMAP200 is a center processing unit which the master frequency of running speed is 333MHz, with 128MHz DRAM and 2GB nand flash, and TF card, one USB port for USB disk and one mini USB port for connect to PC.
2. The WiFi module base on 802.11b/g/n WIFI wireless technolgy, the 40MHz clock frequency input to chip as base clock frequency, and use the high-frequency from 2412MHz to 2462MHz as carrier frequency for 802.11b/g/n(HT20), the high-frequency from 2422MHz to 2452MHz as carrier frequency for 802.11n(HT40), the CCK, BPSK, QPSK modulation type be used.
3. The USB disk and TF card all external signal and data input to MCU as program source, playing and display on LCD screen or output to speaker and external display.
4. The work voltage is DC 7.4V, supply by battery, with a DC 9V power adaptor.