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N41	30	15	DFT-256QAM	L	Edge_1RB_Left	25.92	27.44	33	PASS
N41	30	15	DFT-256QAM	L	Edge_1RB_Right	26.49	28.01	33	PASS
N41	30	15	DFT-256QAM	L	Outer_Full	26.21	27.73	33	PASS
N41	30	15	DFT-256QAM	L	Inner_Full	26.15	27.67	33	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Left	25.65	27.17	33	PASS
N41	30	15	CP-QPSK	L	Edge_1RB_Right	26.26	27.78	33	PASS
N41	30	15	CP-QPSK	L	Outer_Full	25.87	27.39	33	PASS
N41	30	15	CP-QPSK	L	Inner_Full	25.84	27.36	33	PASS
N41	30	15	CP-16QAM	L	Edge_1RB_Left	25.62	27.14	33	PASS
N41	30	15	CP-16QAM	L	Edge_1RB_Right	26.24	27.76	33	PASS
N41	30	15	CP-16QAM	L	Outer_Full	25.87	27.39	33	PASS
N41	30	15	CP-16QAM	L	Inner_Full	25.92	27.44	33	PASS
N41	30	15	CP-64QAM	L	Edge_1RB_Left	25.71	27.23	33	PASS
N41	30	15	CP-64QAM	L	Edge_1RB_Right	26.23	27.75	33	PASS
N41	30	15	CP-64QAM	L	Outer_Full	25.87	27.39	33	PASS
N41	30	15	CP-64QAM	L	Inner_Full	23.11	24.63	33	PASS
N41	30	15	CP-256QAM	L	Edge_1RB_Left	25.53	27.05	33	PASS
N41	30	15	CP-256QAM	L	Edge_1RB_Right	26.24	27.76	33	PASS
N41	30	15	CP-256QAM	L	Outer_Full	25.84	27.36	33	PASS
N41	30	15	CP-256QAM	L	Inner_Full	25.89	27.41	33	PASS
N41	30	15	DFT-PI2BPSK	M	Edge_1RB_Left	27.34	28.86	33	PASS
N41	30	15	DFT-PI2BPSK	M	Edge_1RB_Right	27.01	28.53	33	PASS
N41	30	15	DFT-PI2BPSK	M	Outer_Full	27.3	28.82	33	PASS
N41	30	15	DFT-PI2BPSK	M	Inner_Full	27.23	28.75	33	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Left	27.25	28.77	33	PASS
N41	30	15	DFT-QPSK	M	Edge_1RB_Right	26.92	28.44	33	PASS
N41	30	15	DFT-QPSK	M	Outer_Full	27.12	28.64	33	PASS
N41	30	15	DFT-QPSK	M	Inner_Full	27.06	28.58	33	PASS
N41	30	15	DFT-16QAM	M	Edge_1RB_Left	26.83	28.35	33	PASS
N41	30	15	DFT-16QAM	M	Edge_1RB_Right	26.4	27.92	33	PASS
N41	30	15	DFT-16QAM	M	Outer_Full	26.81	28.33	33	PASS
N41	30	15	DFT-16QAM	M	Inner_Full	26.76	28.28	33	PASS
N41	30	15	DFT-64QAM	M	Edge_1RB_Left	26.92	28.44	33	PASS
N41	30	15	DFT-64QAM	M	Edge_1RB_Right	26.59	28.11	33	PASS
N41	30	15	DFT-64QAM	M	Outer_Full	26.74	28.26	33	PASS
N41	30	15	DFT-64QAM	M	Inner_Full	26.69	28.21	33	PASS
N41	30	15	DFT-256QAM	M	Edge_1RB_Left	26.91	28.43	33	PASS



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N41	30	15	DFT-256QAM	M	Edge_1RB_Right	26.54	28.06	33	PASS
N41	30	15	DFT-256QAM	M	Outer_Full	26.75	28.27	33	PASS
N41	30	15	DFT-256QAM	M	Inner_Full	26.71	28.23	33	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Left	26.56	28.08	33	PASS
N41	30	15	CP-QPSK	M	Edge_1RB_Right	26.35	27.87	33	PASS
N41	30	15	CP-QPSK	M	Outer_Full	26.34	27.86	33	PASS
N41	30	15	CP-QPSK	M	Inner_Full	26.38	27.9	33	PASS
N41	30	15	CP-16QAM	M	Edge_1RB_Left	26.55	28.07	33	PASS
N41	30	15	CP-16QAM	M	Edge_1RB_Right	26.31	27.83	33	PASS
N41	30	15	CP-16QAM	M	Outer_Full	26.33	27.85	33	PASS
N41	30	15	CP-16QAM	M	Inner_Full	26.38	27.9	33	PASS
N41	30	15	CP-64QAM	M	Edge_1RB_Left	26.62	28.14	33	PASS
N41	30	15	CP-64QAM	M	Edge_1RB_Right	26.2	27.72	33	PASS
N41	30	15	CP-64QAM	M	Outer_Full	26.33	27.85	33	PASS
N41	30	15	CP-64QAM	M	Inner_Full	23.67	25.19	33	PASS
N41	30	15	CP-256QAM	M	Edge_1RB_Left	26.54	28.06	33	PASS
N41	30	15	CP-256QAM	M	Edge_1RB_Right	26.29	27.81	33	PASS
N41	30	15	CP-256QAM	M	Outer_Full	26.36	27.88	33	PASS
N41	30	15	CP-256QAM	M	Inner_Full	24.25	25.77	33	PASS
N41	30	15	DFT-PI2BPSK	H	Edge_1RB_Left	27.2	28.72	33	PASS
N41	30	15	DFT-PI2BPSK	H	Edge_1RB_Right	26.74	28.26	33	PASS
N41	30	15	DFT-PI2BPSK	H	Outer_Full	27.05	28.57	33	PASS
N41	30	15	DFT-PI2BPSK	H	Inner_Full	26.96	28.48	33	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Left	27.1	28.62	33	PASS
N41	30	15	DFT-QPSK	H	Edge_1RB_Right	26.64	28.16	33	PASS
N41	30	15	DFT-QPSK	H	Outer_Full	26.93	28.45	33	PASS
N41	30	15	DFT-QPSK	H	Inner_Full	26.88	28.4	33	PASS
N41	30	15	DFT-16QAM	H	Edge_1RB_Left	26.7	28.22	33	PASS
N41	30	15	DFT-16QAM	H	Edge_1RB_Right	26.23	27.75	33	PASS
N41	30	15	DFT-16QAM	H	Outer_Full	26.68	28.2	33	PASS
N41	30	15	DFT-16QAM	H	Inner_Full	26.62	28.14	33	PASS
N41	30	15	DFT-64QAM	H	Edge_1RB_Left	26.83	28.35	33	PASS
N41	30	15	DFT-64QAM	H	Edge_1RB_Right	26.34	27.86	33	PASS
N41	30	15	DFT-64QAM	H	Outer_Full	26.61	28.13	33	PASS
N41	30	15	DFT-64QAM	H	Inner_Full	26.55	28.07	33	PASS
N41	30	15	DFT-256QAM	H	Edge_1RB_Left	26.73	28.25	33	PASS
N41	30	15	DFT-256QAM	H	Edge_1RB_Right	26.28	27.8	33	PASS



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N41	30	15	DFT-256QAM	H	Outer_Full	26.62	28.14	33	PASS
N41	30	15	DFT-256QAM	H	Inner_Full	26.56	28.08	33	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Left	26.5	28.02	33	PASS
N41	30	15	CP-QPSK	H	Edge_1RB_Right	26.05	27.57	33	PASS
N41	30	15	CP-QPSK	H	Outer_Full	26.21	27.73	33	PASS
N41	30	15	CP-QPSK	H	Inner_Full	26.25	27.77	33	PASS
N41	30	15	CP-16QAM	H	Edge_1RB_Left	26.47	27.99	33	PASS
N41	30	15	CP-16QAM	H	Edge_1RB_Right	26.07	27.59	33	PASS
N41	30	15	CP-16QAM	H	Outer_Full	26.21	27.73	33	PASS
N41	30	15	CP-16QAM	H	Inner_Full	26.28	27.8	33	PASS
N41	30	15	CP-64QAM	H	Edge_1RB_Left	26.54	28.06	33	PASS
N41	30	15	CP-64QAM	H	Edge_1RB_Right	26.12	27.64	33	PASS
N41	30	15	CP-64QAM	H	Outer_Full	26.19	27.71	33	PASS
N41	30	15	CP-64QAM	H	Inner_Full	25.31	26.83	33	PASS
N41	30	15	CP-256QAM	H	Edge_1RB_Left	23.67	25.19	33	PASS
N41	30	15	CP-256QAM	H	Edge_1RB_Right	26.04	27.56	33	PASS
N41	30	15	CP-256QAM	H	Outer_Full	26.21	27.73	33	PASS
N41	30	15	CP-256QAM	H	Inner_Full	26.31	27.83	33	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Left	26.17	27.69	33	PASS
N41	30	20	DFT-PI2BPSK	L	Edge_1RB_Right	26.55	28.07	33	PASS
N41	30	20	DFT-PI2BPSK	L	Outer_Full	26.79	28.31	33	PASS
N41	30	20	DFT-PI2BPSK	L	Inner_Full	26.81	28.33	33	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Left	26.14	27.66	33	PASS
N41	30	20	DFT-QPSK	L	Edge_1RB_Right	26.54	28.06	33	PASS
N41	30	20	DFT-QPSK	L	Outer_Full	26.67	28.19	33	PASS
N41	30	20	DFT-QPSK	L	Inner_Full	26.71	28.23	33	PASS
N41	30	20	DFT-16QAM	L	Edge_1RB_Left	25.69	27.21	33	PASS
N41	30	20	DFT-16QAM	L	Edge_1RB_Right	26.19	27.71	33	PASS
N41	30	20	DFT-16QAM	L	Outer_Full	26.41	27.93	33	PASS
N41	30	20	DFT-16QAM	L	Inner_Full	26.44	27.96	33	PASS
N41	30	20	DFT-64QAM	L	Edge_1RB_Left	25.89	27.41	33	PASS
N41	30	20	DFT-64QAM	L	Edge_1RB_Right	26.39	27.91	33	PASS
N41	30	20	DFT-64QAM	L	Outer_Full	26.4	27.92	33	PASS
N41	30	20	DFT-64QAM	L	Inner_Full	26.43	27.95	33	PASS
N41	30	20	DFT-256QAM	L	Edge_1RB_Left	25.84	27.36	33	PASS
N41	30	20	DFT-256QAM	L	Edge_1RB_Right	26.33	27.85	33	PASS
N41	30	20	DFT-256QAM	L	Outer_Full	26.39	27.91	33	PASS



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N41	30	20	DFT-256QAM	L	Inner_Full	26.37	27.89	33	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Left	25.53	27.05	33	PASS
N41	30	20	CP-QPSK	L	Edge_1RB_Right	26.15	27.67	33	PASS
N41	30	20	CP-QPSK	L	Outer_Full	26	27.52	33	PASS
N41	30	20	CP-QPSK	L	Inner_Full	26.03	27.55	33	PASS
N41	30	20	CP-16QAM	L	Edge_1RB_Left	25.63	27.15	33	PASS
N41	30	20	CP-16QAM	L	Edge_1RB_Right	26.26	27.78	33	PASS
N41	30	20	CP-16QAM	L	Outer_Full	25.99	27.51	33	PASS
N41	30	20	CP-16QAM	L	Inner_Full	26.02	27.54	33	PASS
N41	30	20	CP-64QAM	L	Edge_1RB_Left	25.73	27.25	33	PASS
N41	30	20	CP-64QAM	L	Edge_1RB_Right	26.23	27.75	33	PASS
N41	30	20	CP-64QAM	L	Outer_Full	25.99	27.51	33	PASS
N41	30	20	CP-64QAM	L	Inner_Full	25.05	26.57	33	PASS
N41	30	20	CP-256QAM	L	Edge_1RB_Left	25.52	27.04	33	PASS
N41	30	20	CP-256QAM	L	Edge_1RB_Right	26.06	27.58	33	PASS
N41	30	20	CP-256QAM	L	Outer_Full	26.01	27.53	33	PASS
N41	30	20	CP-256QAM	L	Inner_Full	26.01	27.53	33	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Left	27.25	28.77	33	PASS
N41	30	20	DFT-PI2BPSK	M	Edge_1RB_Right	26.84	28.36	33	PASS
N41	30	20	DFT-PI2BPSK	M	Outer_Full	27.3	28.82	33	PASS
N41	30	20	DFT-PI2BPSK	M	Inner_Full	27.3	28.82	33	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Left	27.16	28.68	33	PASS
N41	30	20	DFT-QPSK	M	Edge_1RB_Right	26.72	28.24	33	PASS
N41	30	20	DFT-QPSK	M	Outer_Full	27.12	28.64	33	PASS
N41	30	20	DFT-QPSK	M	Inner_Full	27.13	28.65	33	PASS
N41	30	20	DFT-16QAM	M	Edge_1RB_Left	26.7	28.22	33	PASS
N41	30	20	DFT-16QAM	M	Edge_1RB_Right	26.46	27.98	33	PASS
N41	30	20	DFT-16QAM	M	Outer_Full	26.79	28.31	33	PASS
N41	30	20	DFT-16QAM	M	Inner_Full	26.84	28.36	33	PASS
N41	30	20	DFT-64QAM	M	Edge_1RB_Left	26.87	28.39	33	PASS
N41	30	20	DFT-64QAM	M	Edge_1RB_Right	26.45	27.97	33	PASS
N41	30	20	DFT-64QAM	M	Outer_Full	26.78	28.3	33	PASS
N41	30	20	DFT-64QAM	M	Inner_Full	26.81	28.33	33	PASS
N41	30	20	DFT-256QAM	M	Edge_1RB_Left	26.79	28.31	33	PASS
N41	30	20	DFT-256QAM	M	Edge_1RB_Right	26.36	27.88	33	PASS
N41	30	20	DFT-256QAM	M	Outer_Full	26.76	28.28	33	PASS
N41	30	20	DFT-256QAM	M	Inner_Full	26.77	28.29	33	PASS



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N41	30	20	CP-QPSK	M	Edge_1RB_Left	26.5	28.02	33	PASS
N41	30	20	CP-QPSK	M	Edge_1RB_Right	26.21	27.73	33	PASS
N41	30	20	CP-QPSK	M	Outer_Full	26.34	27.86	33	PASS
N41	30	20	CP-QPSK	M	Inner_Full	26.42	27.94	33	PASS
N41	30	20	CP-16QAM	M	Edge_1RB_Left	26.53	28.05	33	PASS
N41	30	20	CP-16QAM	M	Edge_1RB_Right	26.19	27.71	33	PASS
N41	30	20	CP-16QAM	M	Outer_Full	26.34	27.86	33	PASS
N41	30	20	CP-16QAM	M	Inner_Full	26.42	27.94	33	PASS
N41	30	20	CP-64QAM	M	Edge_1RB_Left	26.52	28.04	33	PASS
N41	30	20	CP-64QAM	M	Edge_1RB_Right	26.16	27.68	33	PASS
N41	30	20	CP-64QAM	M	Outer_Full	26.32	27.84	33	PASS
N41	30	20	CP-64QAM	M	Inner_Full	26.4	27.92	33	PASS
N41	30	20	CP-256QAM	M	Edge_1RB_Left	26.42	27.94	33	PASS
N41	30	20	CP-256QAM	M	Edge_1RB_Right	26.17	27.69	33	PASS
N41	30	20	CP-256QAM	M	Outer_Full	26.38	27.9	33	PASS
N41	30	20	CP-256QAM	M	Inner_Full	26.38	27.9	33	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Left	26.5	28.02	33	PASS
N41	30	20	DFT-PI2BPSK	H	Edge_1RB_Right	26.56	28.08	33	PASS
N41	30	20	DFT-PI2BPSK	H	Outer_Full	27.08	28.6	33	PASS
N41	30	20	DFT-PI2BPSK	H	Inner_Full	27.09	28.61	33	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Left	27.05	28.57	33	PASS
N41	30	20	DFT-QPSK	H	Edge_1RB_Right	26.46	27.98	33	PASS
N41	30	20	DFT-QPSK	H	Outer_Full	26.98	28.5	33	PASS
N41	30	20	DFT-QPSK	H	Inner_Full	26.98	28.5	33	PASS
N41	30	20	DFT-16QAM	H	Edge_1RB_Left	26.74	28.26	33	PASS
N41	30	20	DFT-16QAM	H	Edge_1RB_Right	26.12	27.64	33	PASS
N41	30	20	DFT-16QAM	H	Outer_Full	26.72	28.24	33	PASS
N41	30	20	DFT-16QAM	H	Inner_Full	26.72	28.24	33	PASS
N41	30	20	DFT-64QAM	H	Edge_1RB_Left	26.93	28.45	33	PASS
N41	30	20	DFT-64QAM	H	Edge_1RB_Right	26.29	27.81	33	PASS
N41	30	20	DFT-64QAM	H	Outer_Full	26.5	28.02	33	PASS
N41	30	20	DFT-64QAM	H	Inner_Full	26.54	28.06	33	PASS
N41	30	20	DFT-256QAM	H	Edge_1RB_Left	26.84	28.36	33	PASS
N41	30	20	DFT-256QAM	H	Edge_1RB_Right	26.14	27.66	33	PASS
N41	30	20	DFT-256QAM	H	Outer_Full	26.72	28.24	33	PASS
N41	30	20	DFT-256QAM	H	Inner_Full	26.56	28.08	33	PASS
N41	30	20	CP-QPSK	H	Edge_1RB_Left	26.54	28.06	33	PASS



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N41	30	20	CP-QPSK	H	Edge_1RB_Right	25.97	27.49	33	PASS
N41	30	20	CP-QPSK	H	Outer_Full	26.35	27.87	33	PASS
N41	30	20	CP-QPSK	H	Inner_Full	26.54	28.06	33	PASS
N41	30	20	CP-16QAM	H	Edge_1RB_Left	26.38	27.9	33	PASS
N41	30	20	CP-16QAM	H	Edge_1RB_Right	26.24	27.76	33	PASS
N41	30	20	CP-16QAM	H	Outer_Full	26.4	27.92	33	PASS
N41	30	20	CP-16QAM	H	Inner_Full	26.44	27.96	33	PASS
N41	30	20	CP-64QAM	H	Edge_1RB_Left	26.6	28.12	33	PASS
N41	30	20	CP-64QAM	H	Edge_1RB_Right	26.1	27.62	33	PASS
N41	30	20	CP-64QAM	H	Outer_Full	26.45	27.97	33	PASS
N41	30	20	CP-64QAM	H	Inner_Full	26.53	28.05	33	PASS
N41	30	20	CP-256QAM	H	Edge_1RB_Left	26.55	28.07	33	PASS
N41	30	20	CP-256QAM	H	Edge_1RB_Right	26.02	27.54	33	PASS
N41	30	20	CP-256QAM	H	Outer_Full	26.39	27.91	33	PASS
N41	30	20	CP-256QAM	H	Inner_Full	26.57	28.09	33	PASS
N41	30	30	DFT-PI2BPSK	L	Edge_1RB_Left	26.4	27.92	33	PASS
N41	30	30	DFT-PI2BPSK	L	Edge_1RB_Right	26.89	28.41	33	PASS
N41	30	30	DFT-PI2BPSK	L	Outer_Full	27.09	28.61	33	PASS
N41	30	30	DFT-PI2BPSK	L	Inner_Full	27.13	28.65	33	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Left	26.36	27.88	33	PASS
N41	30	30	DFT-QPSK	L	Edge_1RB_Right	26.88	28.4	33	PASS
N41	30	30	DFT-QPSK	L	Outer_Full	26.96	28.48	33	PASS
N41	30	30	DFT-QPSK	L	Inner_Full	26.99	28.51	33	PASS
N41	30	30	DFT-16QAM	L	Edge_1RB_Left	25.88	27.4	33	PASS
N41	30	30	DFT-16QAM	L	Edge_1RB_Right	26.66	28.18	33	PASS
N41	30	30	DFT-16QAM	L	Outer_Full	26.72	28.24	33	PASS
N41	30	30	DFT-16QAM	L	Inner_Full	26.77	28.29	33	PASS
N41	30	30	DFT-64QAM	L	Edge_1RB_Left	26.08	27.6	33	PASS
N41	30	30	DFT-64QAM	L	Edge_1RB_Right	26.69	28.21	33	PASS
N41	30	30	DFT-64QAM	L	Outer_Full	26.64	28.16	33	PASS
N41	30	30	DFT-64QAM	L	Inner_Full	25.34	26.86	33	PASS
N41	30	30	DFT-256QAM	L	Edge_1RB_Left	25.95	27.47	33	PASS
N41	30	30	DFT-256QAM	L	Edge_1RB_Right	26.66	28.18	33	PASS
N41	30	30	DFT-256QAM	L	Outer_Full	26.65	28.17	33	PASS
N41	30	30	DFT-256QAM	L	Inner_Full	26.7	28.22	33	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Left	25.78	27.3	33	PASS
N41	30	30	CP-QPSK	L	Edge_1RB_Right	26.5	28.02	33	PASS



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N41	30	30	CP-QPSK	L	Outer_Full	26.28	27.8	33	PASS
N41	30	30	CP-QPSK	L	Inner_Full	26.38	27.9	33	PASS
N41	30	30	CP-16QAM	L	Edge_1RB_Left	25.74	27.26	33	PASS
N41	30	30	CP-16QAM	L	Edge_1RB_Right	26.48	28	33	PASS
N41	30	30	CP-16QAM	L	Outer_Full	26.27	27.79	33	PASS
N41	30	30	CP-16QAM	L	Inner_Full	26.37	27.89	33	PASS
N41	30	30	CP-64QAM	L	Edge_1RB_Left	25.8	27.32	33	PASS
N41	30	30	CP-64QAM	L	Edge_1RB_Right	26.55	28.07	33	PASS
N41	30	30	CP-64QAM	L	Outer_Full	26.27	27.79	33	PASS
N41	30	30	CP-64QAM	L	Inner_Full	24.17	25.69	33	PASS
N41	30	30	CP-256QAM	L	Edge_1RB_Left	25.7	27.22	33	PASS
N41	30	30	CP-256QAM	L	Edge_1RB_Right	26.37	27.89	33	PASS
N41	30	30	CP-256QAM	L	Outer_Full	26.25	27.77	33	PASS
N41	30	30	CP-256QAM	L	Inner_Full	26.39	27.91	33	PASS
N41	30	30	DFT-PI2BPSK	M	Edge_1RB_Left	27.4	28.92	33	PASS
N41	30	30	DFT-PI2BPSK	M	Edge_1RB_Right	27.21	28.73	33	PASS
N41	30	30	DFT-PI2BPSK	M	Outer_Full	27.42	28.94	33	PASS
N41	30	30	DFT-PI2BPSK	M	Inner_Full	27.36	28.88	33	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Left	27.28	28.8	33	PASS
N41	30	30	DFT-QPSK	M	Edge_1RB_Right	27.06	28.58	33	PASS
N41	30	30	DFT-QPSK	M	Outer_Full	27.23	28.75	33	PASS
N41	30	30	DFT-QPSK	M	Inner_Full	27.19	28.71	33	PASS
N41	30	30	DFT-16QAM	M	Edge_1RB_Left	26.83	28.35	33	PASS
N41	30	30	DFT-16QAM	M	Edge_1RB_Right	26.6	28.12	33	PASS
N41	30	30	DFT-16QAM	M	Outer_Full	26.92	28.44	33	PASS
N41	30	30	DFT-16QAM	M	Inner_Full	26.91	28.43	33	PASS
N41	30	30	DFT-64QAM	M	Edge_1RB_Left	26.97	28.49	33	PASS
N41	30	30	DFT-64QAM	M	Edge_1RB_Right	26.72	28.24	33	PASS
N41	30	30	DFT-64QAM	M	Outer_Full	26.86	28.38	33	PASS
N41	30	30	DFT-64QAM	M	Inner_Full	26.84	28.36	33	PASS
N41	30	30	DFT-256QAM	M	Edge_1RB_Left	27	28.52	33	PASS
N41	30	30	DFT-256QAM	M	Edge_1RB_Right	26.73	28.25	33	PASS
N41	30	30	DFT-256QAM	M	Outer_Full	26.89	28.41	33	PASS
N41	30	30	DFT-256QAM	M	Inner_Full	26.85	28.37	33	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Left	26.65	28.17	33	PASS
N41	30	30	CP-QPSK	M	Edge_1RB_Right	26.47	27.99	33	PASS
N41	30	30	CP-QPSK	M	Outer_Full	26.41	27.93	33	PASS



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N41	30	30	CP-QPSK	M	Inner_Full	26.47	27.99	33	PASS
N41	30	30	CP-16QAM	M	Edge_1RB_Left	26.76	28.28	33	PASS
N41	30	30	CP-16QAM	M	Edge_1RB_Right	26.51	28.03	33	PASS
N41	30	30	CP-16QAM	M	Outer_Full	26.43	27.95	33	PASS
N41	30	30	CP-16QAM	M	Inner_Full	26.47	27.99	33	PASS
N41	30	30	CP-64QAM	M	Edge_1RB_Left	26.71	28.23	33	PASS
N41	30	30	CP-64QAM	M	Edge_1RB_Right	26.36	27.88	33	PASS
N41	30	30	CP-64QAM	M	Outer_Full	26.42	27.94	33	PASS
N41	30	30	CP-64QAM	M	Inner_Full	25.51	27.03	33	PASS
N41	30	30	CP-256QAM	M	Edge_1RB_Left	26.65	28.17	33	PASS
N41	30	30	CP-256QAM	M	Edge_1RB_Right	26.49	28.01	33	PASS
N41	30	30	CP-256QAM	M	Outer_Full	26.41	27.93	33	PASS
N41	30	30	CP-256QAM	M	Inner_Full	26.47	27.99	33	PASS
N41	30	30	DFT-PI2BPSK	H	Edge_1RB_Left	27.28	28.8	33	PASS
N41	30	30	DFT-PI2BPSK	H	Edge_1RB_Right	26.85	28.37	33	PASS
N41	30	30	DFT-PI2BPSK	H	Outer_Full	27.21	28.73	33	PASS
N41	30	30	DFT-PI2BPSK	H	Inner_Full	27.18	28.7	33	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Left	27.13	28.65	33	PASS
N41	30	30	DFT-QPSK	H	Edge_1RB_Right	26.78	28.3	33	PASS
N41	30	30	DFT-QPSK	H	Outer_Full	27.09	28.61	33	PASS
N41	30	30	DFT-QPSK	H	Inner_Full	27.07	28.59	33	PASS
N41	30	30	DFT-16QAM	H	Edge_1RB_Left	26.76	28.28	33	PASS
N41	30	30	DFT-16QAM	H	Edge_1RB_Right	26.37	27.89	33	PASS
N41	30	30	DFT-16QAM	H	Outer_Full	26.82	28.34	33	PASS
N41	30	30	DFT-16QAM	H	Inner_Full	26.82	28.34	33	PASS
N41	30	30	DFT-64QAM	H	Edge_1RB_Left	26.88	28.4	33	PASS
N41	30	30	DFT-64QAM	H	Edge_1RB_Right	26.45	27.97	33	PASS
N41	30	30	DFT-64QAM	H	Outer_Full	26.75	28.27	33	PASS
N41	30	30	DFT-64QAM	H	Inner_Full	26.77	28.29	33	PASS
N41	30	30	DFT-256QAM	H	Edge_1RB_Left	26.83	28.35	33	PASS
N41	30	30	DFT-256QAM	H	Edge_1RB_Right	26.32	27.84	33	PASS
N41	30	30	DFT-256QAM	H	Outer_Full	26.75	28.27	33	PASS
N41	30	30	DFT-256QAM	H	Inner_Full	26.77	28.29	33	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Left	26.57	28.09	33	PASS
N41	30	30	CP-QPSK	H	Edge_1RB_Right	26.22	27.74	33	PASS
N41	30	30	CP-QPSK	H	Outer_Full	26.38	27.9	33	PASS
N41	30	30	CP-QPSK	H	Inner_Full	26.51	28.03	33	PASS



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N41	30	30	CP-16QAM	H	Edge_1RB_Left	26.46	27.98	33	PASS
N41	30	30	CP-16QAM	H	Edge_1RB_Right	26.3	27.82	33	PASS
N41	30	30	CP-16QAM	H	Outer_Full	26.44	27.96	33	PASS
N41	30	30	CP-16QAM	H	Inner_Full	26.52	28.04	33	PASS
N41	30	30	CP-64QAM	H	Edge_1RB_Left	26.58	28.1	33	PASS
N41	30	30	CP-64QAM	H	Edge_1RB_Right	26.14	27.66	33	PASS
N41	30	30	CP-64QAM	H	Outer_Full	25.09	26.61	33	PASS
N41	30	30	CP-64QAM	H	Inner_Full	26.53	28.05	33	PASS
N41	30	30	CP-256QAM	H	Edge_1RB_Left	26.58	28.1	33	PASS
N41	30	30	CP-256QAM	H	Edge_1RB_Right	23.21	24.73	33	PASS
N41	30	30	CP-256QAM	H	Outer_Full	26.4	27.92	33	PASS
N41	30	30	CP-256QAM	H	Inner_Full	26.54	28.06	33	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Left	26.36	27.88	33	PASS
N41	30	40	DFT-PI2BPSK	L	Edge_1RB_Right	26.24	27.76	33	PASS
N41	30	40	DFT-PI2BPSK	L	Outer_Full	27.07	28.59	33	PASS
N41	30	40	DFT-PI2BPSK	L	Inner_Full	27.24	28.76	33	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Left	26.23	27.75	33	PASS
N41	30	40	DFT-QPSK	L	Edge_1RB_Right	26.25	27.77	33	PASS
N41	30	40	DFT-QPSK	L	Outer_Full	26.99	28.51	33	PASS
N41	30	40	DFT-QPSK	L	Inner_Full	27.16	28.68	33	PASS
N41	30	40	DFT-16QAM	L	Edge_1RB_Left	25.92	27.44	33	PASS
N41	30	40	DFT-16QAM	L	Edge_1RB_Right	25.94	27.46	33	PASS
N41	30	40	DFT-16QAM	L	Outer_Full	26.76	28.28	33	PASS
N41	30	40	DFT-16QAM	L	Inner_Full	26.91	28.43	33	PASS
N41	30	40	DFT-64QAM	L	Edge_1RB_Left	25.93	27.45	33	PASS
N41	30	40	DFT-64QAM	L	Edge_1RB_Right	26.1	27.62	33	PASS
N41	30	40	DFT-64QAM	L	Outer_Full	26.71	28.23	33	PASS
N41	30	40	DFT-64QAM	L	Inner_Full	26.28	27.8	33	PASS
N41	30	40	DFT-256QAM	L	Edge_1RB_Left	25.88	27.4	33	PASS
N41	30	40	DFT-256QAM	L	Edge_1RB_Right	26.02	27.54	33	PASS
N41	30	40	DFT-256QAM	L	Outer_Full	26.72	28.24	33	PASS
N41	30	40	DFT-256QAM	L	Inner_Full	26.89	28.41	33	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Left	25.69	27.21	33	PASS
N41	30	40	CP-QPSK	L	Edge_1RB_Right	26.01	27.53	33	PASS
N41	30	40	CP-QPSK	L	Outer_Full	26.36	27.88	33	PASS
N41	30	40	CP-QPSK	L	Inner_Full	26.58	28.1	33	PASS
N41	30	40	CP-16QAM	L	Edge_1RB_Left	25.68	27.2	33	PASS



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N41	30	40	CP-16QAM	L	Edge_1RB_Right	25.9	27.42	33	PASS
N41	30	40	CP-16QAM	L	Outer_Full	26.34	27.86	33	PASS
N41	30	40	CP-16QAM	L	Inner_Full	26.57	28.09	33	PASS
N41	30	40	CP-64QAM	L	Edge_1RB_Left	25.74	27.26	33	PASS
N41	30	40	CP-64QAM	L	Edge_1RB_Right	26.48	28	33	PASS
N41	30	40	CP-64QAM	L	Outer_Full	26.24	27.76	33	PASS
N41	30	40	CP-64QAM	L	Inner_Full	26.27	27.79	33	PASS
N41	30	40	CP-256QAM	L	Edge_1RB_Left	25.74	27.26	33	PASS
N41	30	40	CP-256QAM	L	Edge_1RB_Right	26.19	27.71	33	PASS
N41	30	40	CP-256QAM	L	Outer_Full	26.39	27.91	33	PASS
N41	30	40	CP-256QAM	L	Inner_Full	23.86	25.38	33	PASS
N41	30	40	DFT-PI2BPSK	M	Edge_1RB_Left	27.34	28.86	33	PASS
N41	30	40	DFT-PI2BPSK	M	Edge_1RB_Right	27.19	28.71	33	PASS
N41	30	40	DFT-PI2BPSK	M	Outer_Full	27.43	28.95	33	PASS
N41	30	40	DFT-PI2BPSK	M	Inner_Full	27.39	28.91	33	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Left	27.17	28.69	33	PASS
N41	30	40	DFT-QPSK	M	Edge_1RB_Right	27.03	28.55	33	PASS
N41	30	40	DFT-QPSK	M	Outer_Full	27.24	28.76	33	PASS
N41	30	40	DFT-QPSK	M	Inner_Full	27.22	28.74	33	PASS
N41	30	40	DFT-16QAM	M	Edge_1RB_Left	26.82	28.34	33	PASS
N41	30	40	DFT-16QAM	M	Edge_1RB_Right	26.53	28.05	33	PASS
N41	30	40	DFT-16QAM	M	Outer_Full	26.93	28.45	33	PASS
N41	30	40	DFT-16QAM	M	Inner_Full	26.91	28.43	33	PASS
N41	30	40	DFT-64QAM	M	Edge_1RB_Left	26.91	28.43	33	PASS
N41	30	40	DFT-64QAM	M	Edge_1RB_Right	26.7	28.22	33	PASS
N41	30	40	DFT-64QAM	M	Outer_Full	26.88	28.4	33	PASS
N41	30	40	DFT-64QAM	M	Inner_Full	25.54	27.06	33	PASS
N41	30	40	DFT-256QAM	M	Edge_1RB_Left	26.85	28.37	33	PASS
N41	30	40	DFT-256QAM	M	Edge_1RB_Right	26.58	28.1	33	PASS
N41	30	40	DFT-256QAM	M	Outer_Full	26.88	28.4	33	PASS
N41	30	40	DFT-256QAM	M	Inner_Full	26.88	28.4	33	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Left	26.51	28.03	33	PASS
N41	30	40	CP-QPSK	M	Edge_1RB_Right	26.36	27.88	33	PASS
N41	30	40	CP-QPSK	M	Outer_Full	26.45	27.97	33	PASS
N41	30	40	CP-QPSK	M	Inner_Full	26.49	28.01	33	PASS
N41	30	40	CP-16QAM	M	Edge_1RB_Left	26.51	28.03	33	PASS
N41	30	40	CP-16QAM	M	Edge_1RB_Right	26.38	27.9	33	PASS



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N41	30	40	CP-16QAM	M	Outer_Full	26.43	27.95	33	PASS
N41	30	40	CP-16QAM	M	Inner_Full	26.47	27.99	33	PASS
N41	30	40	CP-64QAM	M	Edge_1RB_Left	26.42	27.94	33	PASS
N41	30	40	CP-64QAM	M	Edge_1RB_Right	26.27	27.79	33	PASS
N41	30	40	CP-64QAM	M	Outer_Full	26.44	27.96	33	PASS
N41	30	40	CP-64QAM	M	Inner_Full	26.46	27.98	33	PASS
N41	30	40	CP-256QAM	M	Edge_1RB_Left	26.51	28.03	33	PASS
N41	30	40	CP-256QAM	M	Edge_1RB_Right	26.36	27.88	33	PASS
N41	30	40	CP-256QAM	M	Outer_Full	26.43	27.95	33	PASS
N41	30	40	CP-256QAM	M	Inner_Full	26.47	27.99	33	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Left	27.11	28.63	33	PASS
N41	30	40	DFT-PI2BPSK	H	Edge_1RB_Right	26.76	28.28	33	PASS
N41	30	40	DFT-PI2BPSK	H	Outer_Full	27.26	28.78	33	PASS
N41	30	40	DFT-PI2BPSK	H	Inner_Full	27.27	28.79	33	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Left	26.95	28.47	33	PASS
N41	30	40	DFT-QPSK	H	Edge_1RB_Right	26.64	28.16	33	PASS
N41	30	40	DFT-QPSK	H	Outer_Full	27.12	28.64	33	PASS
N41	30	40	DFT-QPSK	H	Inner_Full	27.15	28.67	33	PASS
N41	30	40	DFT-16QAM	H	Edge_1RB_Left	26.56	28.08	33	PASS
N41	30	40	DFT-16QAM	H	Edge_1RB_Right	26.3	27.82	33	PASS
N41	30	40	DFT-16QAM	H	Outer_Full	26.84	28.36	33	PASS
N41	30	40	DFT-16QAM	H	Inner_Full	26.88	28.4	33	PASS
N41	30	40	DFT-64QAM	H	Edge_1RB_Left	26.62	28.14	33	PASS
N41	30	40	DFT-64QAM	H	Edge_1RB_Right	26.39	27.91	33	PASS
N41	30	40	DFT-64QAM	H	Outer_Full	26.78	28.3	33	PASS
N41	30	40	DFT-64QAM	H	Inner_Full	26.84	28.36	33	PASS
N41	30	40	DFT-256QAM	H	Edge_1RB_Left	26.65	28.17	33	PASS
N41	30	40	DFT-256QAM	H	Edge_1RB_Right	26.32	27.84	33	PASS
N41	30	40	DFT-256QAM	H	Outer_Full	26.8	28.32	33	PASS
N41	30	40	DFT-256QAM	H	Inner_Full	26.85	28.37	33	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Left	26.18	27.7	33	PASS
N41	30	40	CP-QPSK	H	Edge_1RB_Right	25.9	27.42	33	PASS
N41	30	40	CP-QPSK	H	Outer_Full	26.38	27.9	33	PASS
N41	30	40	CP-QPSK	H	Inner_Full	23.19	24.71	33	PASS
N41	30	40	CP-16QAM	H	Edge_1RB_Left	26.39	27.91	33	PASS
N41	30	40	CP-16QAM	H	Edge_1RB_Right	26.18	27.7	33	PASS
N41	30	40	CP-16QAM	H	Outer_Full	26.36	27.88	33	PASS



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N41	30	40	CP-16QAM	H	Inner_Full	26.49	28.01	33	PASS
N41	30	40	CP-64QAM	H	Edge_1RB_Left	26.23	27.75	33	PASS
N41	30	40	CP-64QAM	H	Edge_1RB_Right	26.13	27.65	33	PASS
N41	30	40	CP-64QAM	H	Outer_Full	26.37	27.89	33	PASS
N41	30	40	CP-64QAM	H	Inner_Full	25.14	26.66	33	PASS
N41	30	40	CP-256QAM	H	Edge_1RB_Left	26.36	27.88	33	PASS
N41	30	40	CP-256QAM	H	Edge_1RB_Right	26.09	27.61	33	PASS
N41	30	40	CP-256QAM	H	Outer_Full	26.35	27.87	33	PASS
N41	30	40	CP-256QAM	H	Inner_Full	26.5	28.02	33	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Left	26.34	27.86	33	PASS
N41	30	50	DFT-PI2BPSK	L	Edge_1RB_Right	26.53	28.05	33	PASS
N41	30	50	DFT-PI2BPSK	L	Outer_Full	26.83	28.35	33	PASS
N41	30	50	DFT-PI2BPSK	L	Inner_Full	27.09	28.61	33	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Left	26.26	27.78	33	PASS
N41	30	50	DFT-QPSK	L	Edge_1RB_Right	26.52	28.04	33	PASS
N41	30	50	DFT-QPSK	L	Outer_Full	26.8	28.32	33	PASS
N41	30	50	DFT-QPSK	L	Inner_Full	27.05	28.57	33	PASS
N41	30	50	DFT-16QAM	L	Edge_1RB_Left	25.84	27.36	33	PASS
N41	30	50	DFT-16QAM	L	Edge_1RB_Right	26.29	27.81	33	PASS
N41	30	50	DFT-16QAM	L	Outer_Full	26.6	28.12	33	PASS
N41	30	50	DFT-16QAM	L	Inner_Full	26.86	28.38	33	PASS
N41	30	50	DFT-64QAM	L	Edge_1RB_Left	26.03	27.55	33	PASS
N41	30	50	DFT-64QAM	L	Edge_1RB_Right	26.42	27.94	33	PASS
N41	30	50	DFT-64QAM	L	Outer_Full	26.57	28.09	33	PASS
N41	30	50	DFT-64QAM	L	Inner_Full	26.82	28.34	33	PASS
N41	30	50	DFT-256QAM	L	Edge_1RB_Left	25.92	27.44	33	PASS
N41	30	50	DFT-256QAM	L	Edge_1RB_Right	26.29	27.81	33	PASS
N41	30	50	DFT-256QAM	L	Outer_Full	26.55	28.07	33	PASS
N41	30	50	DFT-256QAM	L	Inner_Full	26.81	28.33	33	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Left	25.66	27.18	33	PASS
N41	30	50	CP-QPSK	L	Edge_1RB_Right	26.22	27.74	33	PASS
N41	30	50	CP-QPSK	L	Outer_Full	26.3	27.82	33	PASS
N41	30	50	CP-QPSK	L	Inner_Full	26.51	28.03	33	PASS
N41	30	50	CP-16QAM	L	Edge_1RB_Left	25.69	27.21	33	PASS
N41	30	50	CP-16QAM	L	Edge_1RB_Right	26.19	27.71	33	PASS
N41	30	50	CP-16QAM	L	Outer_Full	26.32	27.84	33	PASS
N41	30	50	CP-16QAM	L	Inner_Full	26.53	28.05	33	PASS



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N41	30	50	CP-64QAM	L	Edge_1RB_Left	25.58	27.1	33	PASS
N41	30	50	CP-64QAM	L	Edge_1RB_Right	26.27	27.79	33	PASS
N41	30	50	CP-64QAM	L	Outer_Full	26.32	27.84	33	PASS
N41	30	50	CP-64QAM	L	Inner_Full	24.11	25.63	33	PASS
N41	30	50	CP-256QAM	L	Edge_1RB_Left	25.69	27.21	33	PASS
N41	30	50	CP-256QAM	L	Edge_1RB_Right	26.3	27.82	33	PASS
N41	30	50	CP-256QAM	L	Outer_Full	26.36	27.88	33	PASS
N41	30	50	CP-256QAM	L	Inner_Full	26.34	27.86	33	PASS
N41	30	50	DFT-PI2BPSK	M	Edge_1RB_Left	27.25	28.77	33	PASS
N41	30	50	DFT-PI2BPSK	M	Edge_1RB_Right	27.28	28.8	33	PASS
N41	30	50	DFT-PI2BPSK	M	Outer_Full	27.32	28.84	33	PASS
N41	30	50	DFT-PI2BPSK	M	Inner_Full	27.36	28.88	33	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Left	27.23	28.75	33	PASS
N41	30	50	DFT-QPSK	M	Edge_1RB_Right	27.11	28.63	33	PASS
N41	30	50	DFT-QPSK	M	Outer_Full	27.16	28.68	33	PASS
N41	30	50	DFT-QPSK	M	Inner_Full	27.23	28.75	33	PASS
N41	30	50	DFT-16QAM	M	Edge_1RB_Left	26.76	28.28	33	PASS
N41	30	50	DFT-16QAM	M	Edge_1RB_Right	26.63	28.15	33	PASS
N41	30	50	DFT-16QAM	M	Outer_Full	26.85	28.37	33	PASS
N41	30	50	DFT-16QAM	M	Inner_Full	26.93	28.45	33	PASS
N41	30	50	DFT-64QAM	M	Edge_1RB_Left	26.93	28.45	33	PASS
N41	30	50	DFT-64QAM	M	Edge_1RB_Right	26.79	28.31	33	PASS
N41	30	50	DFT-64QAM	M	Outer_Full	26.79	28.31	33	PASS
N41	30	50	DFT-64QAM	M	Inner_Full	24.69	26.21	33	PASS
N41	30	50	DFT-256QAM	M	Edge_1RB_Left	26.93	28.45	33	PASS
N41	30	50	DFT-256QAM	M	Edge_1RB_Right	26.73	28.25	33	PASS
N41	30	50	DFT-256QAM	M	Outer_Full	26.82	28.34	33	PASS
N41	30	50	DFT-256QAM	M	Inner_Full	26.92	28.44	33	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Left	26.61	28.13	33	PASS
N41	30	50	CP-QPSK	M	Edge_1RB_Right	26.5	28.02	33	PASS
N41	30	50	CP-QPSK	M	Outer_Full	26.5	28.02	33	PASS
N41	30	50	CP-QPSK	M	Inner_Full	26.52	28.04	33	PASS
N41	30	50	CP-16QAM	M	Edge_1RB_Left	26.68	28.2	33	PASS
N41	30	50	CP-16QAM	M	Edge_1RB_Right	26.57	28.09	33	PASS
N41	30	50	CP-16QAM	M	Outer_Full	26.48	28	33	PASS
N41	30	50	CP-16QAM	M	Inner_Full	26.5	28.02	33	PASS
N41	30	50	CP-64QAM	M	Edge_1RB_Left	26.75	28.27	33	PASS



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N41	30	50	CP-64QAM	M	Edge_1RB_Right	26.6	28.12	33	PASS
N41	30	50	CP-64QAM	M	Outer_Full	26.45	27.97	33	PASS
N41	30	50	CP-64QAM	M	Inner_Full	26.9	28.42	33	PASS
N41	30	50	CP-256QAM	M	Edge_1RB_Left	26.65	28.17	33	PASS
N41	30	50	CP-256QAM	M	Edge_1RB_Right	26.51	28.03	33	PASS
N41	30	50	CP-256QAM	M	Outer_Full	26.48	28	33	PASS
N41	30	50	CP-256QAM	M	Inner_Full	26.86	28.38	33	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Left	27.22	28.74	33	PASS
N41	30	50	DFT-PI2BPSK	H	Edge_1RB_Right	26.96	28.48	33	PASS
N41	30	50	DFT-PI2BPSK	H	Outer_Full	27.18	28.7	33	PASS
N41	30	50	DFT-PI2BPSK	H	Inner_Full	27.29	28.81	33	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Left	26.99	28.51	33	PASS
N41	30	50	DFT-QPSK	H	Edge_1RB_Right	26.77	28.29	33	PASS
N41	30	50	DFT-QPSK	H	Outer_Full	27.03	28.55	33	PASS
N41	30	50	DFT-QPSK	H	Inner_Full	27.16	28.68	33	PASS
N41	30	50	DFT-16QAM	H	Edge_1RB_Left	26.54	28.06	33	PASS
N41	30	50	DFT-16QAM	H	Edge_1RB_Right	26.39	27.91	33	PASS
N41	30	50	DFT-16QAM	H	Outer_Full	26.73	28.25	33	PASS
N41	30	50	DFT-16QAM	H	Inner_Full	26.89	28.41	33	PASS
N41	30	50	DFT-64QAM	H	Edge_1RB_Left	26.68	28.2	33	PASS
N41	30	50	DFT-64QAM	H	Edge_1RB_Right	26.52	28.04	33	PASS
N41	30	50	DFT-64QAM	H	Outer_Full	26.68	28.2	33	PASS
N41	30	50	DFT-64QAM	H	Inner_Full	24.61	26.13	33	PASS
N41	30	50	DFT-256QAM	H	Edge_1RB_Left	26.58	28.1	33	PASS
N41	30	50	DFT-256QAM	H	Edge_1RB_Right	26.36	27.88	33	PASS
N41	30	50	DFT-256QAM	H	Outer_Full	26.66	28.18	33	PASS
N41	30	50	DFT-256QAM	H	Inner_Full	26.83	28.35	33	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Left	26.33	27.85	33	PASS
N41	30	50	CP-QPSK	H	Edge_1RB_Right	26.12	27.64	33	PASS
N41	30	50	CP-QPSK	H	Outer_Full	26.33	27.85	33	PASS
N41	30	50	CP-QPSK	H	Inner_Full	26.45	27.97	33	PASS
N41	30	50	CP-16QAM	H	Edge_1RB_Left	26.29	27.81	33	PASS
N41	30	50	CP-16QAM	H	Edge_1RB_Right	26.46	27.98	33	PASS
N41	30	50	CP-16QAM	H	Outer_Full	26.47	27.99	33	PASS
N41	30	50	CP-16QAM	H	Inner_Full	26.5	28.02	33	PASS
N41	30	50	CP-64QAM	H	Edge_1RB_Left	26.52	28.04	33	PASS
N41	30	50	CP-64QAM	H	Edge_1RB_Right	23.51	25.03	33	PASS



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N41	30	50	CP-64QAM	H	Outer_Full	26.31	27.83	33	PASS
N41	30	50	CP-64QAM	H	Inner_Full	26.59	28.11	33	PASS
N41	30	50	CP-256QAM	H	Edge_1RB_Left	26.41	27.93	33	PASS
N41	30	50	CP-256QAM	H	Edge_1RB_Right	26.2	27.72	33	PASS
N41	30	50	CP-256QAM	H	Outer_Full	26.08	27.6	33	PASS
N41	30	50	CP-256QAM	H	Inner_Full	26.6	28.12	33	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Left	25.63	27.15	33	PASS
N41	30	60	DFT-PI2BPSK	L	Edge_1RB_Right	26.59	28.11	33	PASS
N41	30	60	DFT-PI2BPSK	L	Outer_Full	26.89	28.41	33	PASS
N41	30	60	DFT-PI2BPSK	L	Inner_Full	27.01	28.53	33	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Left	25.98	27.5	33	PASS
N41	30	60	DFT-QPSK	L	Edge_1RB_Right	26.55	28.07	33	PASS
N41	30	60	DFT-QPSK	L	Outer_Full	26.86	28.38	33	PASS
N41	30	60	DFT-QPSK	L	Inner_Full	26.99	28.51	33	PASS
N41	30	60	DFT-16QAM	L	Edge_1RB_Left	25.58	27.1	33	PASS
N41	30	60	DFT-16QAM	L	Edge_1RB_Right	26.25	27.77	33	PASS
N41	30	60	DFT-16QAM	L	Outer_Full	26.66	28.18	33	PASS
N41	30	60	DFT-16QAM	L	Inner_Full	26.8	28.32	33	PASS
N41	30	60	DFT-64QAM	L	Edge_1RB_Left	25.81	27.33	33	PASS
N41	30	60	DFT-64QAM	L	Edge_1RB_Right	26.44	27.96	33	PASS
N41	30	60	DFT-64QAM	L	Outer_Full	26.74	28.26	33	PASS
N41	30	60	DFT-64QAM	L	Inner_Full	26.91	28.43	33	PASS
N41	30	60	DFT-256QAM	L	Edge_1RB_Left	25.7	27.22	33	PASS
N41	30	60	DFT-256QAM	L	Edge_1RB_Right	26.48	28	33	PASS
N41	30	60	DFT-256QAM	L	Outer_Full	26.66	28.18	33	PASS
N41	30	60	DFT-256QAM	L	Inner_Full	26.9	28.42	33	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Left	25.55	27.07	33	PASS
N41	30	60	CP-QPSK	L	Edge_1RB_Right	26.41	27.93	33	PASS
N41	30	60	CP-QPSK	L	Outer_Full	26.38	27.9	33	PASS
N41	30	60	CP-QPSK	L	Inner_Full	26.6	28.12	33	PASS
N41	30	60	CP-16QAM	L	Edge_1RB_Left	25.58	27.1	33	PASS
N41	30	60	CP-16QAM	L	Edge_1RB_Right	26.45	27.97	33	PASS
N41	30	60	CP-16QAM	L	Outer_Full	26.37	27.89	33	PASS
N41	30	60	CP-16QAM	L	Inner_Full	26.55	28.07	33	PASS
N41	30	60	CP-64QAM	L	Edge_1RB_Left	25.66	27.18	33	PASS
N41	30	60	CP-64QAM	L	Edge_1RB_Right	26.54	28.06	33	PASS
N41	30	60	CP-64QAM	L	Outer_Full	26.34	27.86	33	PASS



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N41	30	60	CP-64QAM	L	Inner_Full	26.63	28.15	33	PASS
N41	30	60	CP-256QAM	L	Edge_1RB_Left	25.42	26.94	33	PASS
N41	30	60	CP-256QAM	L	Edge_1RB_Right	26.27	27.79	33	PASS
N41	30	60	CP-256QAM	L	Outer_Full	26.39	27.91	33	PASS
N41	30	60	CP-256QAM	L	Inner_Full	26.65	28.17	33	PASS
N41	30	60	DFT-PI2BPSK	M	Edge_1RB_Left	27	28.52	33	PASS
N41	30	60	DFT-PI2BPSK	M	Edge_1RB_Right	27.04	28.56	33	PASS
N41	30	60	DFT-PI2BPSK	M	Outer_Full	27.4	28.92	33	PASS
N41	30	60	DFT-PI2BPSK	M	Inner_Full	27.42	28.94	33	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Left	27	28.52	33	PASS
N41	30	60	DFT-QPSK	M	Edge_1RB_Right	26.9	28.42	33	PASS
N41	30	60	DFT-QPSK	M	Outer_Full	27.21	28.73	33	PASS
N41	30	60	DFT-QPSK	M	Inner_Full	27.25	28.77	33	PASS
N41	30	60	DFT-16QAM	M	Edge_1RB_Left	26.56	28.08	33	PASS
N41	30	60	DFT-16QAM	M	Edge_1RB_Right	26.42	27.94	33	PASS
N41	30	60	DFT-16QAM	M	Outer_Full	26.89	28.41	33	PASS
N41	30	60	DFT-16QAM	M	Inner_Full	26.96	28.48	33	PASS
N41	30	60	DFT-64QAM	M	Edge_1RB_Left	26.67	28.19	33	PASS
N41	30	60	DFT-64QAM	M	Edge_1RB_Right	26.56	28.08	33	PASS
N41	30	60	DFT-64QAM	M	Outer_Full	26.84	28.36	33	PASS
N41	30	60	DFT-64QAM	M	Inner_Full	26.9	28.42	33	PASS
N41	30	60	DFT-256QAM	M	Edge_1RB_Left	26.63	28.15	33	PASS
N41	30	60	DFT-256QAM	M	Edge_1RB_Right	26.52	28.04	33	PASS
N41	30	60	DFT-256QAM	M	Outer_Full	26.82	28.34	33	PASS
N41	30	60	DFT-256QAM	M	Inner_Full	26.88	28.4	33	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Left	26.4	27.92	33	PASS
N41	30	60	CP-QPSK	M	Edge_1RB_Right	26.38	27.9	33	PASS
N41	30	60	CP-QPSK	M	Outer_Full	26.42	27.94	33	PASS
N41	30	60	CP-QPSK	M	Inner_Full	26.5	28.02	33	PASS
N41	30	60	CP-16QAM	M	Edge_1RB_Left	26.3	27.82	33	PASS
N41	30	60	CP-16QAM	M	Edge_1RB_Right	26.33	27.85	33	PASS
N41	30	60	CP-16QAM	M	Outer_Full	26.4	27.92	33	PASS
N41	30	60	CP-16QAM	M	Inner_Full	26.47	27.99	33	PASS
N41	30	60	CP-64QAM	M	Edge_1RB_Left	26.45	27.97	33	PASS
N41	30	60	CP-64QAM	M	Edge_1RB_Right	26.44	27.96	33	PASS
N41	30	60	CP-64QAM	M	Outer_Full	26.39	27.91	33	PASS
N41	30	60	CP-64QAM	M	Inner_Full	23.96	25.48	33	PASS



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N41	30	60	CP-256QAM	M	Edge_1RB_Left	26.34	27.86	33	PASS
N41	30	60	CP-256QAM	M	Edge_1RB_Right	26.36	27.88	33	PASS
N41	30	60	CP-256QAM	M	Outer_Full	26.4	27.92	33	PASS
N41	30	60	CP-256QAM	M	Inner_Full	26.48	28	33	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Left	27.07	28.59	33	PASS
N41	30	60	DFT-PI2BPSK	H	Edge_1RB_Right	26.62	28.14	33	PASS
N41	30	60	DFT-PI2BPSK	H	Outer_Full	27.21	28.73	33	PASS
N41	30	60	DFT-PI2BPSK	H	Inner_Full	27.31	28.83	33	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Left	26.95	28.47	33	PASS
N41	30	60	DFT-QPSK	H	Edge_1RB_Right	26.56	28.08	33	PASS
N41	30	60	DFT-QPSK	H	Outer_Full	27.08	28.6	33	PASS
N41	30	60	DFT-QPSK	H	Inner_Full	27.17	28.69	33	PASS
N41	30	60	DFT-16QAM	H	Edge_1RB_Left	26.5	28.02	33	PASS
N41	30	60	DFT-16QAM	H	Edge_1RB_Right	26.19	27.71	33	PASS
N41	30	60	DFT-16QAM	H	Outer_Full	26.78	28.3	33	PASS
N41	30	60	DFT-16QAM	H	Inner_Full	26.88	28.4	33	PASS
N41	30	60	DFT-64QAM	H	Edge_1RB_Left	26.6	28.12	33	PASS
N41	30	60	DFT-64QAM	H	Edge_1RB_Right	26.29	27.81	33	PASS
N41	30	60	DFT-64QAM	H	Outer_Full	26.72	28.24	33	PASS
N41	30	60	DFT-64QAM	H	Inner_Full	26.84	28.36	33	PASS
N41	30	60	DFT-256QAM	H	Edge_1RB_Left	26.52	28.04	33	PASS
N41	30	60	DFT-256QAM	H	Edge_1RB_Right	26.17	27.69	33	PASS
N41	30	60	DFT-256QAM	H	Outer_Full	26.72	28.24	33	PASS
N41	30	60	DFT-256QAM	H	Inner_Full	26.83	28.35	33	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Left	26.26	27.78	33	PASS
N41	30	60	CP-QPSK	H	Edge_1RB_Right	26.04	27.56	33	PASS
N41	30	60	CP-QPSK	H	Outer_Full	26.32	27.84	33	PASS
N41	30	60	CP-QPSK	H	Inner_Full	26.44	27.96	33	PASS
N41	30	60	CP-16QAM	H	Edge_1RB_Left	26.3	27.82	33	PASS
N41	30	60	CP-16QAM	H	Edge_1RB_Right	26.04	27.56	33	PASS
N41	30	60	CP-16QAM	H	Outer_Full	26.28	27.8	33	PASS
N41	30	60	CP-16QAM	H	Inner_Full	26.44	27.96	33	PASS
N41	30	60	CP-64QAM	H	Edge_1RB_Left	26.16	27.68	33	PASS
N41	30	60	CP-64QAM	H	Edge_1RB_Right	25.91	27.43	33	PASS
N41	30	60	CP-64QAM	H	Outer_Full	26.3	27.82	33	PASS
N41	30	60	CP-64QAM	H	Inner_Full	26.46	27.98	33	PASS
N41	30	60	CP-256QAM	H	Edge_1RB_Left	26.27	27.79	33	PASS



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N41	30	60	CP-256QAM	H	Edge_1RB_Right	25.94	27.46	33	PASS
N41	30	60	CP-256QAM	H	Outer_Full	26.34	27.86	33	PASS
N41	30	60	CP-256QAM	H	Inner_Full	26.45	27.97	33	PASS
N41	30	70	DFT-PI2BPSK	L	Edge_1RB_Left	26.21	27.73	33	PASS
N41	30	70	DFT-PI2BPSK	L	Edge_1RB_Right	26.67	28.19	33	PASS
N41	30	70	DFT-PI2BPSK	L	Outer_Full	26.74	28.26	33	PASS
N41	30	70	DFT-PI2BPSK	L	Inner_Full	26.61	28.13	33	PASS
N41	30	70	DFT-QPSK	L	Edge_1RB_Left	26.05	27.57	33	PASS
N41	30	70	DFT-QPSK	L	Edge_1RB_Right	26.66	28.18	33	PASS
N41	30	70	DFT-QPSK	L	Outer_Full	26.69	28.21	33	PASS
N41	30	70	DFT-QPSK	L	Inner_Full	26.61	28.13	33	PASS
N41	30	70	DFT-16QAM	L	Edge_1RB_Left	25.82	27.34	33	PASS
N41	30	70	DFT-16QAM	L	Edge_1RB_Right	26.28	27.8	33	PASS
N41	30	70	DFT-16QAM	L	Outer_Full	26.55	28.07	33	PASS
N41	30	70	DFT-16QAM	L	Inner_Full	26.49	28.01	33	PASS
N41	30	70	DFT-64QAM	L	Edge_1RB_Left	25.94	27.46	33	PASS
N41	30	70	DFT-64QAM	L	Edge_1RB_Right	26.37	27.89	33	PASS
N41	30	70	DFT-64QAM	L	Outer_Full	26.49	28.01	33	PASS
N41	30	70	DFT-64QAM	L	Inner_Full	26.46	27.98	33	PASS
N41	30	70	DFT-256QAM	L	Edge_1RB_Left	25.78	27.3	33	PASS
N41	30	70	DFT-256QAM	L	Edge_1RB_Right	26.33	27.85	33	PASS
N41	30	70	DFT-256QAM	L	Outer_Full	26.48	28	33	PASS
N41	30	70	DFT-256QAM	L	Inner_Full	26.15	27.67	33	PASS
N41	30	70	CP-QPSK	L	Edge_1RB_Left	25.6	27.12	33	PASS
N41	30	70	CP-QPSK	L	Edge_1RB_Right	26.13	27.65	33	PASS
N41	30	70	CP-QPSK	L	Outer_Full	26.21	27.73	33	PASS
N41	30	70	CP-QPSK	L	Inner_Full	26.32	27.84	33	PASS
N41	30	70	CP-16QAM	L	Edge_1RB_Left	25.66	27.18	33	PASS
N41	30	70	CP-16QAM	L	Edge_1RB_Right	26.19	27.71	33	PASS
N41	30	70	CP-16QAM	L	Outer_Full	26.2	27.72	33	PASS
N41	30	70	CP-16QAM	L	Inner_Full	26.42	27.94	33	PASS
N41	30	70	CP-64QAM	L	Edge_1RB_Left	25.6	27.12	33	PASS
N41	30	70	CP-64QAM	L	Edge_1RB_Right	26.09	27.61	33	PASS
N41	30	70	CP-64QAM	L	Outer_Full	24.03	25.55	33	PASS
N41	30	70	CP-64QAM	L	Inner_Full	26.39	27.91	33	PASS
N41	30	70	CP-256QAM	L	Edge_1RB_Left	25.6	27.12	33	PASS
N41	30	70	CP-256QAM	L	Edge_1RB_Right	26.05	27.57	33	PASS



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N41	30	70	CP-256QAM	L	Outer_Full	23.23	24.75	33	PASS
N41	30	70	CP-256QAM	L	Inner_Full	26.34	27.86	33	PASS
N41	30	70	DFT-PI2BPSK	M	Edge_1RB_Left	26.79	28.31	33	PASS
N41	30	70	DFT-PI2BPSK	M	Edge_1RB_Right	26.95	28.47	33	PASS
N41	30	70	DFT-PI2BPSK	M	Outer_Full	27.1	28.62	33	PASS
N41	30	70	DFT-PI2BPSK	M	Inner_Full	27.12	28.64	33	PASS
N41	30	70	DFT-QPSK	M	Edge_1RB_Left	26.7	28.22	33	PASS
N41	30	70	DFT-QPSK	M	Edge_1RB_Right	26.78	28.3	33	PASS
N41	30	70	DFT-QPSK	M	Outer_Full	26.93	28.45	33	PASS
N41	30	70	DFT-QPSK	M	Inner_Full	26.98	28.5	33	PASS
N41	30	70	DFT-16QAM	M	Edge_1RB_Left	26.55	28.07	33	PASS
N41	30	70	DFT-16QAM	M	Edge_1RB_Right	26.54	28.06	33	PASS
N41	30	70	DFT-16QAM	M	Outer_Full	26.64	28.16	33	PASS
N41	30	70	DFT-16QAM	M	Inner_Full	26.68	28.2	33	PASS
N41	30	70	DFT-64QAM	M	Edge_1RB_Left	26.37	27.89	33	PASS
N41	30	70	DFT-64QAM	M	Edge_1RB_Right	26.36	27.88	33	PASS
N41	30	70	DFT-64QAM	M	Outer_Full	26.58	28.1	33	PASS
N41	30	70	DFT-64QAM	M	Inner_Full	26.63	28.15	33	PASS
N41	30	70	DFT-256QAM	M	Edge_1RB_Left	26.58	28.1	33	PASS
N41	30	70	DFT-256QAM	M	Edge_1RB_Right	26.52	28.04	33	PASS
N41	30	70	DFT-256QAM	M	Outer_Full	26.57	28.09	33	PASS
N41	30	70	DFT-256QAM	M	Inner_Full	26.62	28.14	33	PASS
N41	30	70	CP-QPSK	M	Edge_1RB_Left	26.36	27.88	33	PASS
N41	30	70	CP-QPSK	M	Edge_1RB_Right	26.23	27.75	33	PASS
N41	30	70	CP-QPSK	M	Outer_Full	26.18	27.7	33	PASS
N41	30	70	CP-QPSK	M	Inner_Full	26.28	27.8	33	PASS
N41	30	70	CP-16QAM	M	Edge_1RB_Left	26.17	27.69	33	PASS
N41	30	70	CP-16QAM	M	Edge_1RB_Right	26.05	27.57	33	PASS
N41	30	70	CP-16QAM	M	Outer_Full	26.17	27.69	33	PASS
N41	30	70	CP-16QAM	M	Inner_Full	26.26	27.78	33	PASS
N41	30	70	CP-64QAM	M	Edge_1RB_Left	26.04	27.56	33	PASS
N41	30	70	CP-64QAM	M	Edge_1RB_Right	26.06	27.58	33	PASS
N41	30	70	CP-64QAM	M	Outer_Full	26.16	27.68	33	PASS
N41	30	70	CP-64QAM	M	Inner_Full	26.28	27.8	33	PASS
N41	30	70	CP-256QAM	M	Edge_1RB_Left	26.31	27.83	33	PASS
N41	30	70	CP-256QAM	M	Edge_1RB_Right	26.22	27.74	33	PASS
N41	30	70	CP-256QAM	M	Outer_Full	26.2	27.72	33	PASS



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N41	30	70	CP-256QAM	M	Inner_Full	26.3	27.82	33	PASS
N41	30	70	DFT-PI2BPSK	H	Edge_1RB_Left	26.95	28.47	33	PASS
N41	30	70	DFT-PI2BPSK	H	Edge_1RB_Right	26.67	28.19	33	PASS
N41	30	70	DFT-PI2BPSK	H	Outer_Full	27.09	28.61	33	PASS
N41	30	70	DFT-PI2BPSK	H	Inner_Full	27.13	28.65	33	PASS
N41	30	70	DFT-QPSK	H	Edge_1RB_Left	26.79	28.31	33	PASS
N41	30	70	DFT-QPSK	H	Edge_1RB_Right	26.51	28.03	33	PASS
N41	30	70	DFT-QPSK	H	Outer_Full	26.93	28.45	33	PASS
N41	30	70	DFT-QPSK	H	Inner_Full	26.97	28.49	33	PASS
N41	30	70	DFT-16QAM	H	Edge_1RB_Left	26.48	28	33	PASS
N41	30	70	DFT-16QAM	H	Edge_1RB_Right	26.43	27.95	33	PASS
N41	30	70	DFT-16QAM	H	Outer_Full	26.62	28.14	33	PASS
N41	30	70	DFT-16QAM	H	Inner_Full	26.65	28.17	33	PASS
N41	30	70	DFT-64QAM	H	Edge_1RB_Left	26.35	27.87	33	PASS
N41	30	70	DFT-64QAM	H	Edge_1RB_Right	26.28	27.8	33	PASS
N41	30	70	DFT-64QAM	H	Outer_Full	26.56	28.08	33	PASS
N41	30	70	DFT-64QAM	H	Inner_Full	26.6	28.12	33	PASS
N41	30	70	DFT-256QAM	H	Edge_1RB_Left	26.4	27.92	33	PASS
N41	30	70	DFT-256QAM	H	Edge_1RB_Right	26.42	27.94	33	PASS
N41	30	70	DFT-256QAM	H	Outer_Full	26.58	28.1	33	PASS
N41	30	70	DFT-256QAM	H	Inner_Full	26.61	28.13	33	PASS
N41	30	70	CP-QPSK	H	Edge_1RB_Left	26.25	27.77	33	PASS
N41	30	70	CP-QPSK	H	Edge_1RB_Right	26.09	27.61	33	PASS
N41	30	70	CP-QPSK	H	Outer_Full	26.17	27.69	33	PASS
N41	30	70	CP-QPSK	H	Inner_Full	26.27	27.79	33	PASS
N41	30	70	CP-16QAM	H	Edge_1RB_Left	26.08	27.6	33	PASS
N41	30	70	CP-16QAM	H	Edge_1RB_Right	25.95	27.47	33	PASS
N41	30	70	CP-16QAM	H	Outer_Full	26.17	27.69	33	PASS
N41	30	70	CP-16QAM	H	Inner_Full	26.25	27.77	33	PASS
N41	30	70	CP-64QAM	H	Edge_1RB_Left	26.19	27.71	33	PASS
N41	30	70	CP-64QAM	H	Edge_1RB_Right	25.99	27.51	33	PASS
N41	30	70	CP-64QAM	H	Outer_Full	26.17	27.69	33	PASS
N41	30	70	CP-64QAM	H	Inner_Full	26.26	27.78	33	PASS
N41	30	70	CP-256QAM	H	Edge_1RB_Left	26.15	27.67	33	PASS
N41	30	70	CP-256QAM	H	Edge_1RB_Right	26.06	27.58	33	PASS
N41	30	70	CP-256QAM	H	Outer_Full	26.17	27.69	33	PASS
N41	30	70	CP-256QAM	H	Inner_Full	26.27	27.79	33	PASS



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N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Left	25.64	27.16	33	PASS
N41	30	80	DFT-PI2BPSK	L	Edge_1RB_Right	26.54	28.06	33	PASS
N41	30	80	DFT-PI2BPSK	L	Outer_Full	26.57	28.09	33	PASS
N41	30	80	DFT-PI2BPSK	L	Inner_Full	26.63	28.15	33	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Left	25.46	26.98	33	PASS
N41	30	80	DFT-QPSK	L	Edge_1RB_Right	26.48	28	33	PASS
N41	30	80	DFT-QPSK	L	Outer_Full	26.51	28.03	33	PASS
N41	30	80	DFT-QPSK	L	Inner_Full	26.63	28.15	33	PASS
N41	30	80	DFT-16QAM	L	Edge_1RB_Left	25.41	26.93	33	PASS
N41	30	80	DFT-16QAM	L	Edge_1RB_Right	26.3	27.82	33	PASS
N41	30	80	DFT-16QAM	L	Outer_Full	26.34	27.86	33	PASS
N41	30	80	DFT-16QAM	L	Inner_Full	26.48	28	33	PASS
N41	30	80	DFT-64QAM	L	Edge_1RB_Left	25.34	26.86	33	PASS
N41	30	80	DFT-64QAM	L	Edge_1RB_Right	26.19	27.71	33	PASS
N41	30	80	DFT-64QAM	L	Outer_Full	26.3	27.82	33	PASS
N41	30	80	DFT-64QAM	L	Inner_Full	26.46	27.98	33	PASS
N41	30	80	DFT-256QAM	L	Edge_1RB_Left	25.43	26.95	33	PASS
N41	30	80	DFT-256QAM	L	Edge_1RB_Right	26.34	27.86	33	PASS
N41	30	80	DFT-256QAM	L	Outer_Full	26.3	27.82	33	PASS
N41	30	80	DFT-256QAM	L	Inner_Full	26.47	27.99	33	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Left	25.25	26.77	33	PASS
N41	30	80	CP-QPSK	L	Edge_1RB_Right	26.1	27.62	33	PASS
N41	30	80	CP-QPSK	L	Outer_Full	26.08	27.6	33	PASS
N41	30	80	CP-QPSK	L	Inner_Full	26.27	27.79	33	PASS
N41	30	80	CP-16QAM	L	Edge_1RB_Left	25.06	26.58	33	PASS
N41	30	80	CP-16QAM	L	Edge_1RB_Right	25.95	27.47	33	PASS
N41	30	80	CP-16QAM	L	Outer_Full	26.08	27.6	33	PASS
N41	30	80	CP-16QAM	L	Inner_Full	26.26	27.78	33	PASS
N41	30	80	CP-64QAM	L	Edge_1RB_Left	25.09	26.61	33	PASS
N41	30	80	CP-64QAM	L	Edge_1RB_Right	26.01	27.53	33	PASS
N41	30	80	CP-64QAM	L	Outer_Full	26.05	27.57	33	PASS
N41	30	80	CP-64QAM	L	Inner_Full	26.35	27.87	33	PASS
N41	30	80	CP-256QAM	L	Edge_1RB_Left	25.34	26.86	33	PASS
N41	30	80	CP-256QAM	L	Edge_1RB_Right	26.13	27.65	33	PASS
N41	30	80	CP-256QAM	L	Outer_Full	25.83	27.35	33	PASS
N41	30	80	CP-256QAM	L	Inner_Full	26.35	27.87	33	PASS
N41	30	80	DFT-PI2BPSK	M	Edge_1RB_Left	26.58	28.1	33	PASS



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N41	30	80	DFT-PI2BPSK	M	Edge_1RB_Right	26.86	28.38	33	PASS
N41	30	80	DFT-PI2BPSK	M	Outer_Full	27.02	28.54	33	PASS
N41	30	80	DFT-PI2BPSK	M	Inner_Full	27.14	28.66	33	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Left	26.47	27.99	33	PASS
N41	30	80	DFT-QPSK	M	Edge_1RB_Right	26.73	28.25	33	PASS
N41	30	80	DFT-QPSK	M	Outer_Full	26.85	28.37	33	PASS
N41	30	80	DFT-QPSK	M	Inner_Full	26.99	28.51	33	PASS
N41	30	80	DFT-16QAM	M	Edge_1RB_Left	26.41	27.93	33	PASS
N41	30	80	DFT-16QAM	M	Edge_1RB_Right	26.43	27.95	33	PASS
N41	30	80	DFT-16QAM	M	Outer_Full	26.56	28.08	33	PASS
N41	30	80	DFT-16QAM	M	Inner_Full	26.7	28.22	33	PASS
N41	30	80	DFT-64QAM	M	Edge_1RB_Left	26.21	27.73	33	PASS
N41	30	80	DFT-64QAM	M	Edge_1RB_Right	26.3	27.82	33	PASS
N41	30	80	DFT-64QAM	M	Outer_Full	26.51	28.03	33	PASS
N41	30	80	DFT-64QAM	M	Inner_Full	26.66	28.18	33	PASS
N41	30	80	DFT-256QAM	M	Edge_1RB_Left	26.43	27.95	33	PASS
N41	30	80	DFT-256QAM	M	Edge_1RB_Right	26.46	27.98	33	PASS
N41	30	80	DFT-256QAM	M	Outer_Full	26.49	28.01	33	PASS
N41	30	80	DFT-256QAM	M	Inner_Full	26.39	27.91	33	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Left	26.29	27.81	33	PASS
N41	30	80	CP-QPSK	M	Edge_1RB_Right	26.3	27.82	33	PASS
N41	30	80	CP-QPSK	M	Outer_Full	26.22	27.74	33	PASS
N41	30	80	CP-QPSK	M	Inner_Full	26.34	27.86	33	PASS
N41	30	80	CP-16QAM	M	Edge_1RB_Left	26.06	27.58	33	PASS
N41	30	80	CP-16QAM	M	Edge_1RB_Right	26.08	27.6	33	PASS
N41	30	80	CP-16QAM	M	Outer_Full	26.22	27.74	33	PASS
N41	30	80	CP-16QAM	M	Inner_Full	26.32	27.84	33	PASS
N41	30	80	CP-64QAM	M	Edge_1RB_Left	25.97	27.49	33	PASS
N41	30	80	CP-64QAM	M	Edge_1RB_Right	26.12	27.64	33	PASS
N41	30	80	CP-64QAM	M	Outer_Full	26.2	27.72	33	PASS
N41	30	80	CP-64QAM	M	Inner_Full	26.38	27.9	33	PASS
N41	30	80	CP-256QAM	M	Edge_1RB_Left	26.27	27.79	33	PASS
N41	30	80	CP-256QAM	M	Edge_1RB_Right	26.34	27.86	33	PASS
N41	30	80	CP-256QAM	M	Outer_Full	26.29	27.81	33	PASS
N41	30	80	CP-256QAM	M	Inner_Full	26.38	27.9	33	PASS
N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Left	26.9	28.42	33	PASS
N41	30	80	DFT-PI2BPSK	H	Edge_1RB_Right	26.64	28.16	33	PASS



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