

WAP123N Circuit Description

Power supply optional 12V DC power supply input and the PD input two. Then the onboard switching power supply output DC3.3V, for the main chip and its peripheral circuit. The onboard DC3.3V switching power supply output DC1.2V, to the main chip power supply.

The main chip storage circuit part, by a 32M BYTE, 16 SDRAM provides linewidth operation data storage. By a 4M byte, 16 bit width parallel FLASH software store.

Ethernet part, by chip VSC8601 provides 1 1G Ethernet PHY layer part of the work, and the RT45 port. Ethernet MAC layer are integrated in the main chip RT2880. Between the two through the MII interface communication. VSC8601 offers Ethernet port connected to LED lamp indicating signal. VSC8601 clock signal is generated by a 25M oscillator with.

Wireless radio frequency RF signal receiving part, accomplished in RT2820, transmitting the signal output from the Transceiver to PA magnification, to the front end after processing by the antenna radiation. Receiving input from the antenna to the front-end processing, by LNA amplification, and then input to the transceiver. The transceiver and the main chip via a baseband IQ signal communication. The main chip and transceiver clock signals are composed of 1 pieces of 40M crystal with.