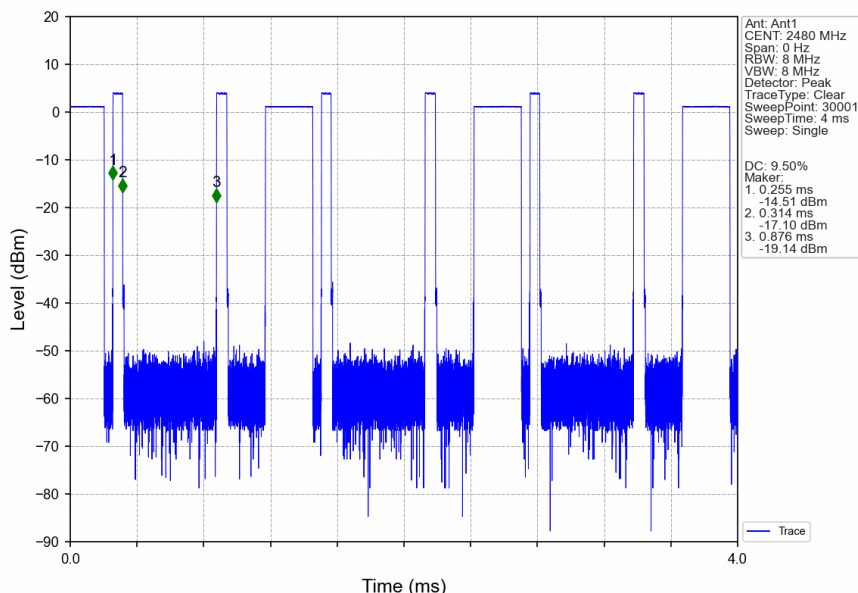
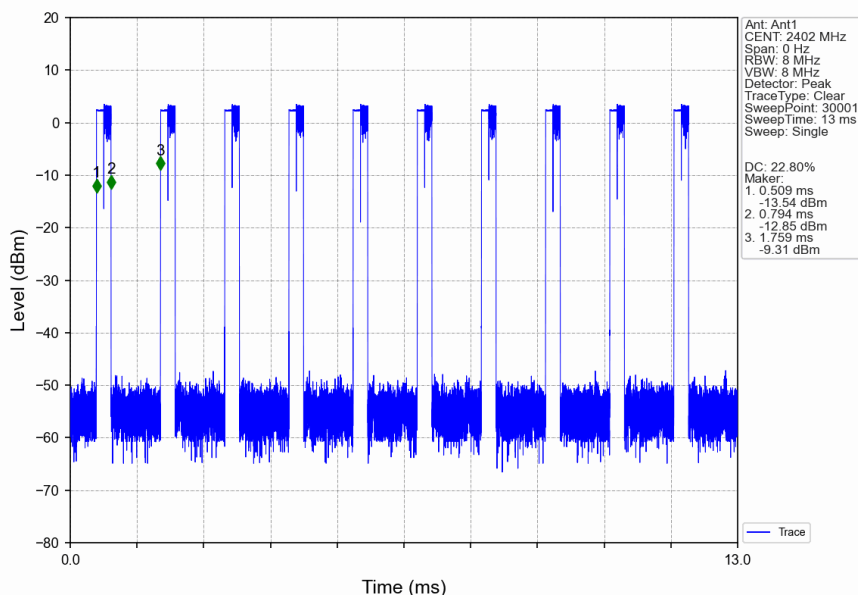


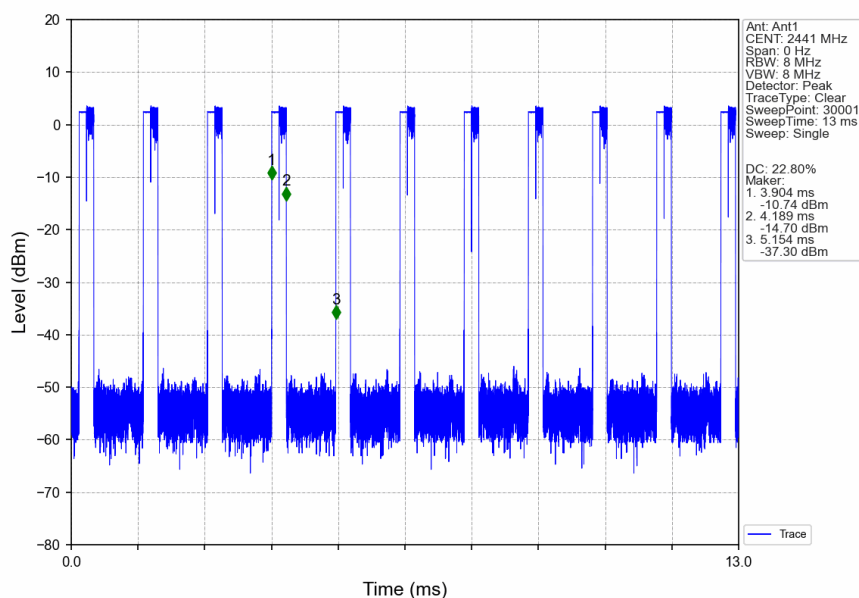
GFSK_DH5_HCH_2480MHz_Ant1_NTNV



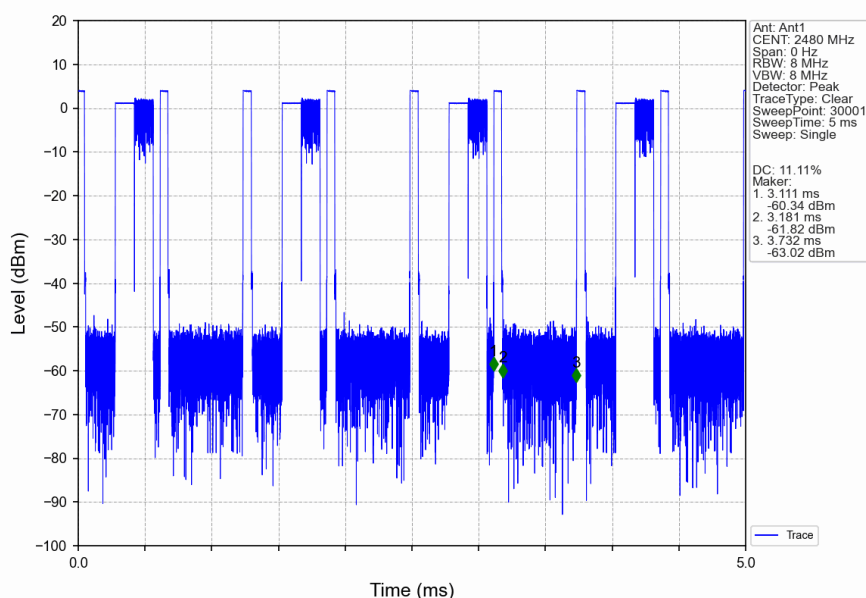
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV



2. Bandwidth

2.1 Test Result

2.1.1 OBW

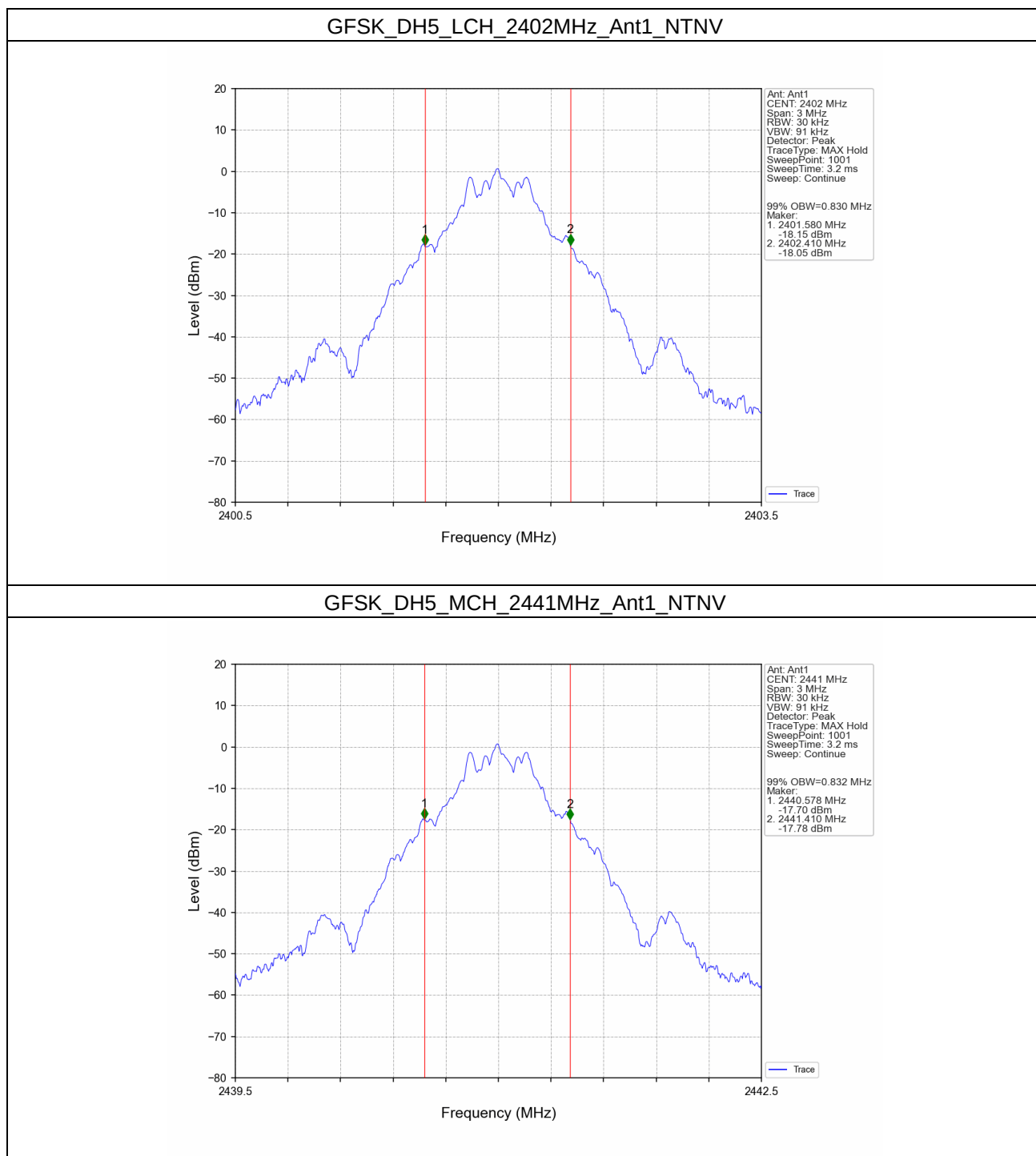
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	99% Occupied Bandwidth (MHz)		Verdict
					Result	Limit	
GFSK	SISO	2402	DH5	1	0.830	/	Pass
		2441	DH5	1	0.832	/	Pass
		2480	DH5	1	0.855	/	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	1.166	/	Pass
		2441	2DH5	1	1.165	/	Pass
		2480	2DH5	1	1.172	/	Pass

2.1.2 20dB BW

Mode	TX Type	Frequency (MHz)	Packet Type	ANT	20dB Bandwidth (MHz)		Verdict
					Result	Limit	
GFSK	SISO	2402	DH5	1	0.878	/	Pass
		2441	DH5	1	0.879	/	Pass
		2480	DH5	1	0.948	/	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	1.275	/	Pass
		2441	2DH5	1	1.271	/	Pass
		2480	2DH5	1	1.283	/	Pass

2.2 Test Graph

2.2.1 OBW



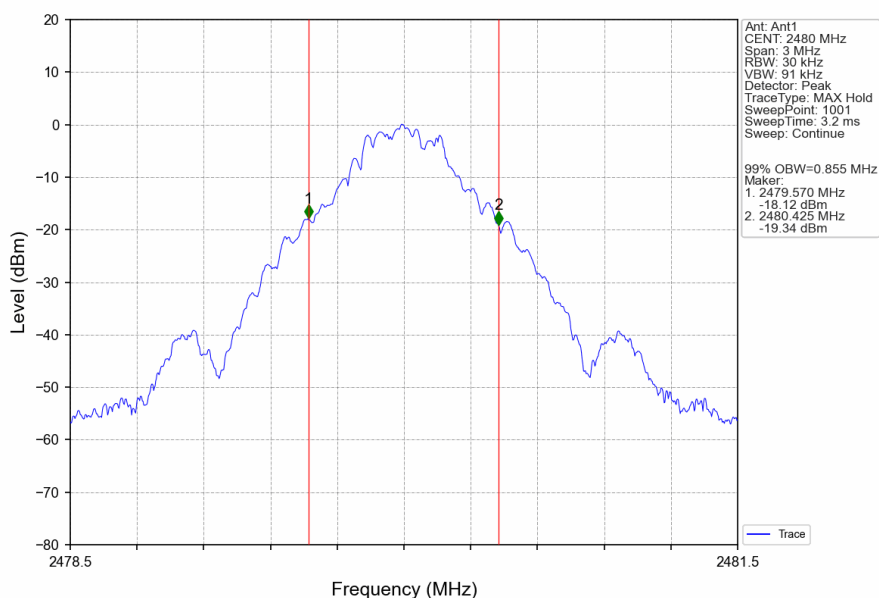
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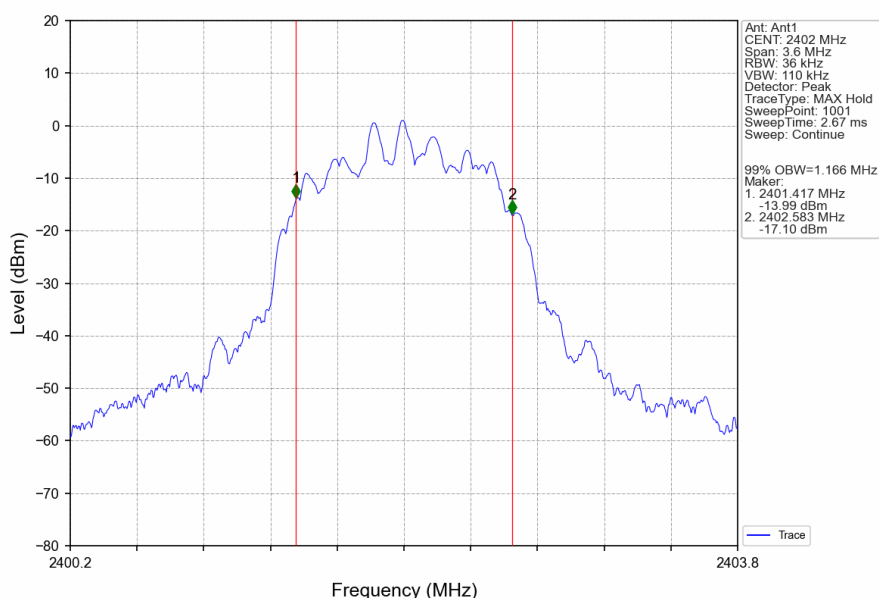
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GFSK_DH5_HCH_2480MHz_Ant1_NTNV



Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



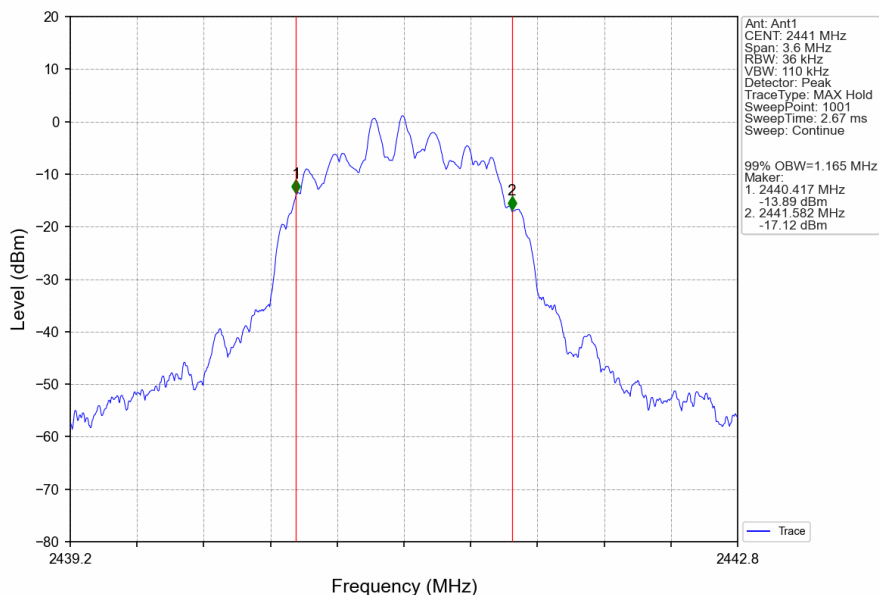
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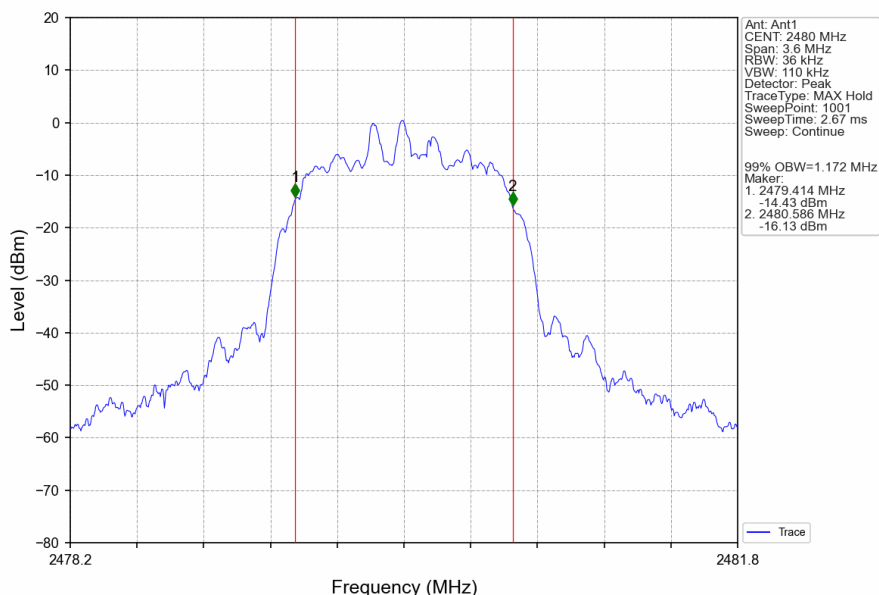
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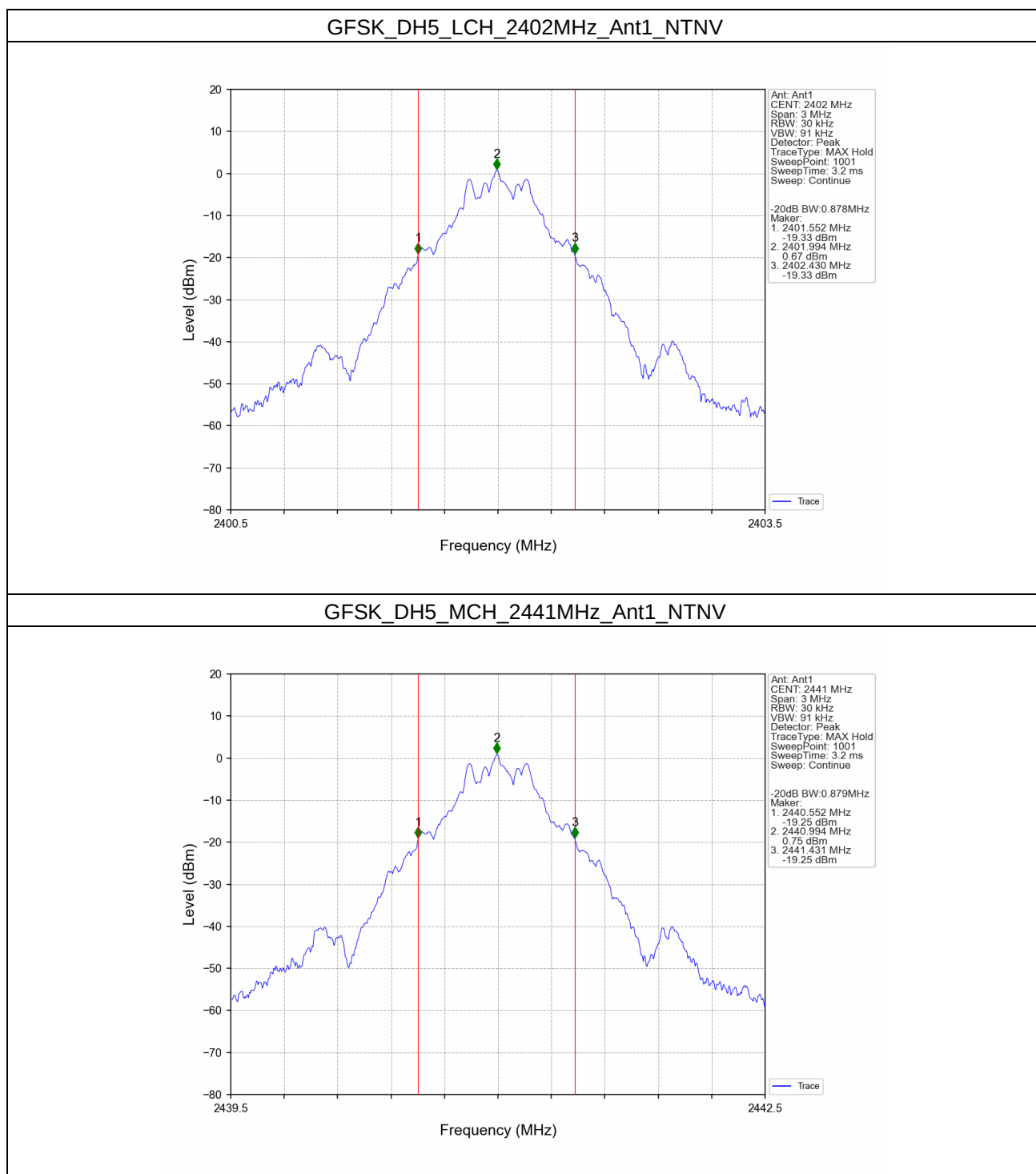
Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV



2.2.2 20dB BW



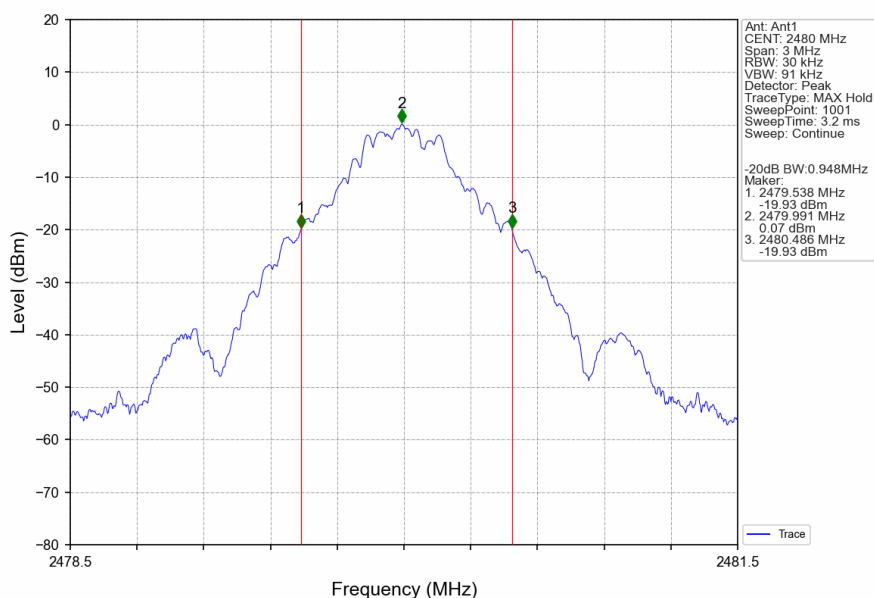
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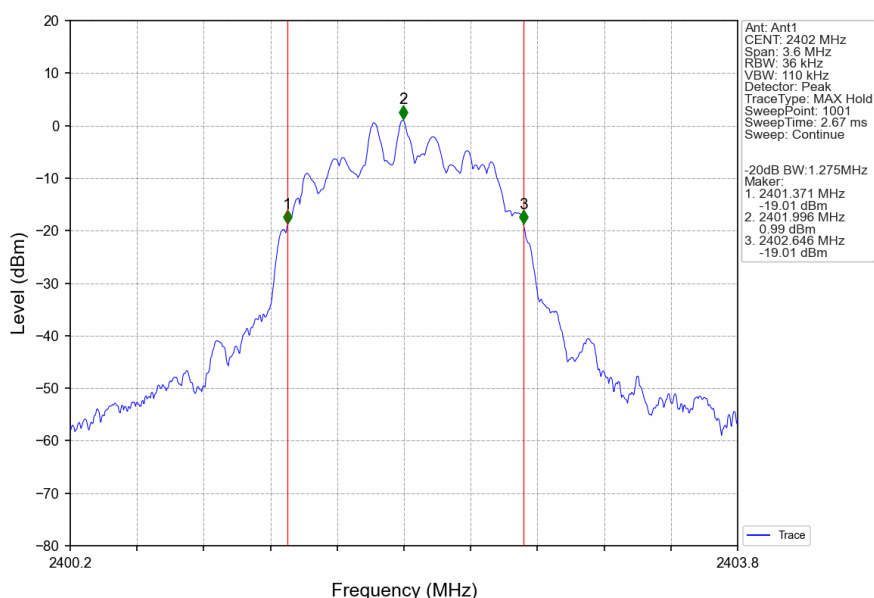
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GFSK_DH5_HCH_2480MHz_Ant1_NTNV



Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



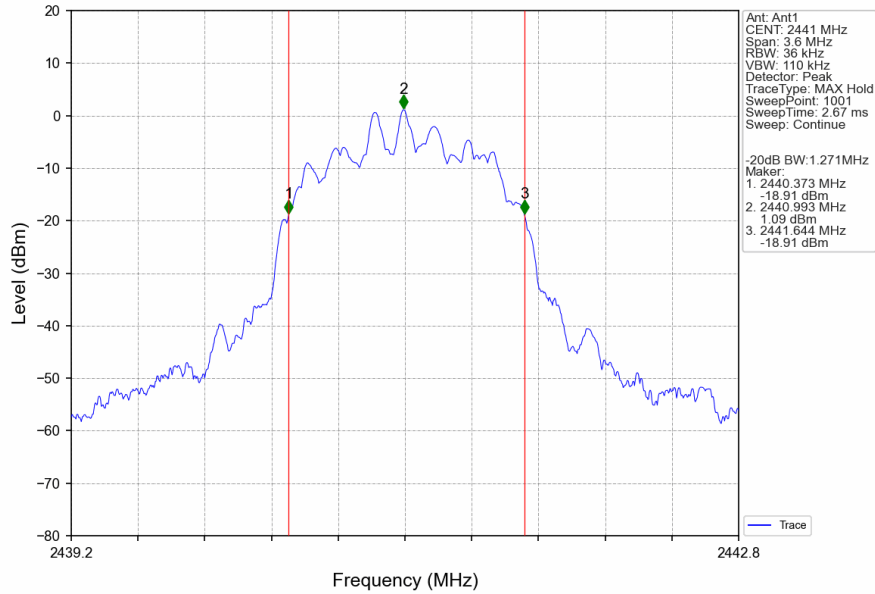
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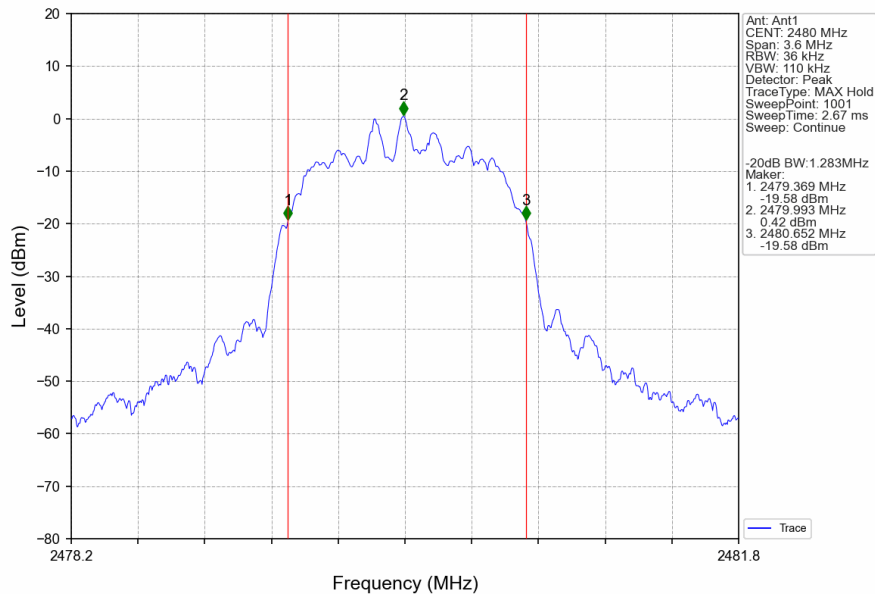
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Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV



3. Maximum Conducted Output Power

3.1 Test Result

3.1.1 Power

Mode	TX Type	Frequency (MHz)	Packet Type	Maximum Peak Conducted Output Power (dBm)		Verdict
				ANT1	Limit	
GFSK	SISO	2402	DH5	2.37	<=30	Pass
		2441	DH5	2.46	<=30	Pass
		2480	DH5	4.02	<=30	Pass
Pi/4DQPSK	SISO	2402	2DH5	3.23	<=20.97	Pass
		2441	2DH5	3.29	<=20.97	Pass
		2480	2DH5	4.02	<=20.97	Pass



4. Carrier Frequency Separation

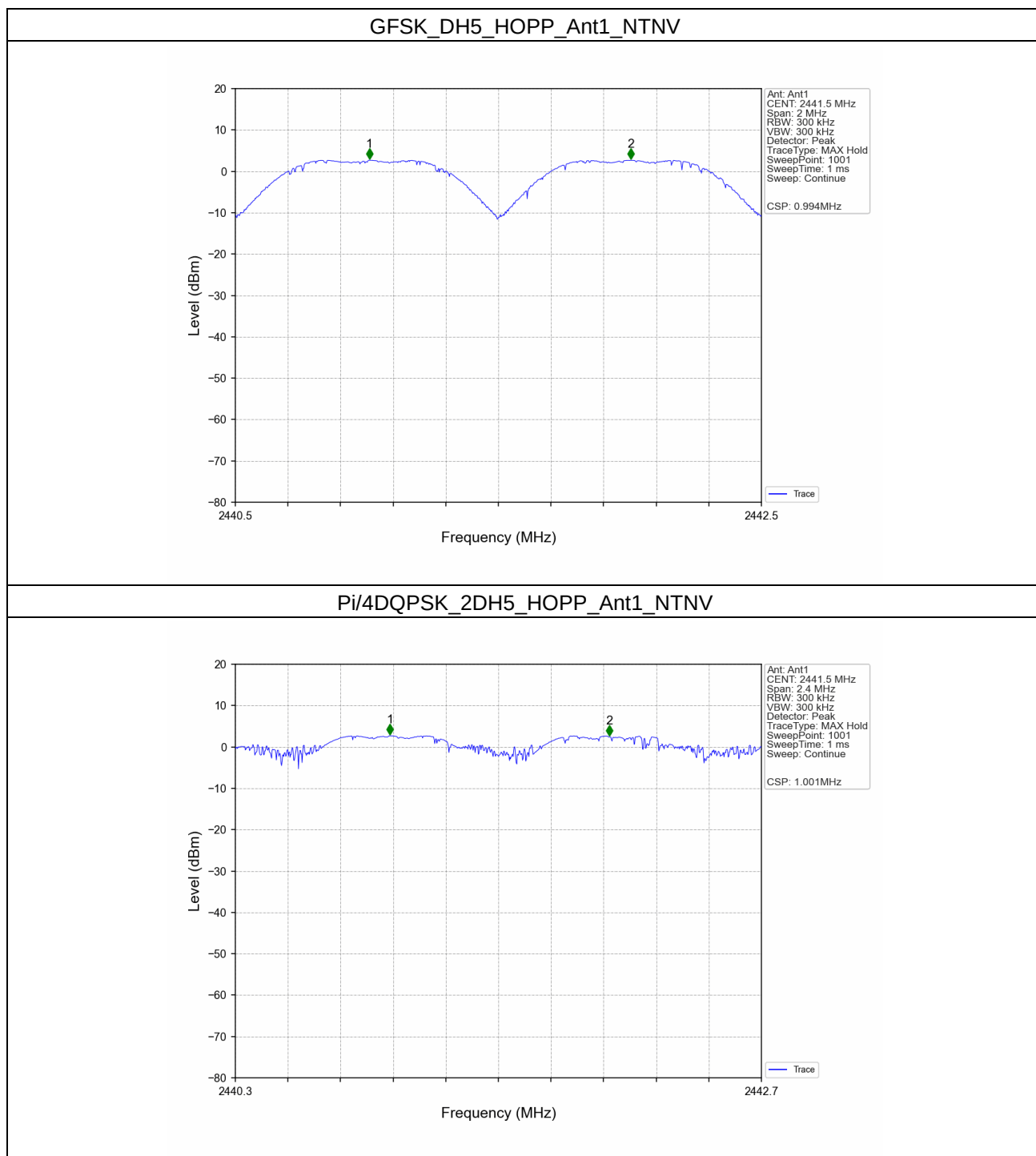
4.1 Test Result

4.1.1 Ant1

Ant1							
Mode	TX Type	Frequency (MHz)	Packet Type	Channel Separation (MHz)	20dB Bandwidth (MHz)	Limit (MHz)	Verdict
GFSK	SISO	HOPP	DH5	0.994	0.948	≥ 0.948	Pass
PI/4DQPSK	SISO	HOPP	2DH5	1.001	1.283	≥ 0.855	Pass

4.2 Test Graph

4.2.1 Ant1



5. Number of Hopping Frequencies

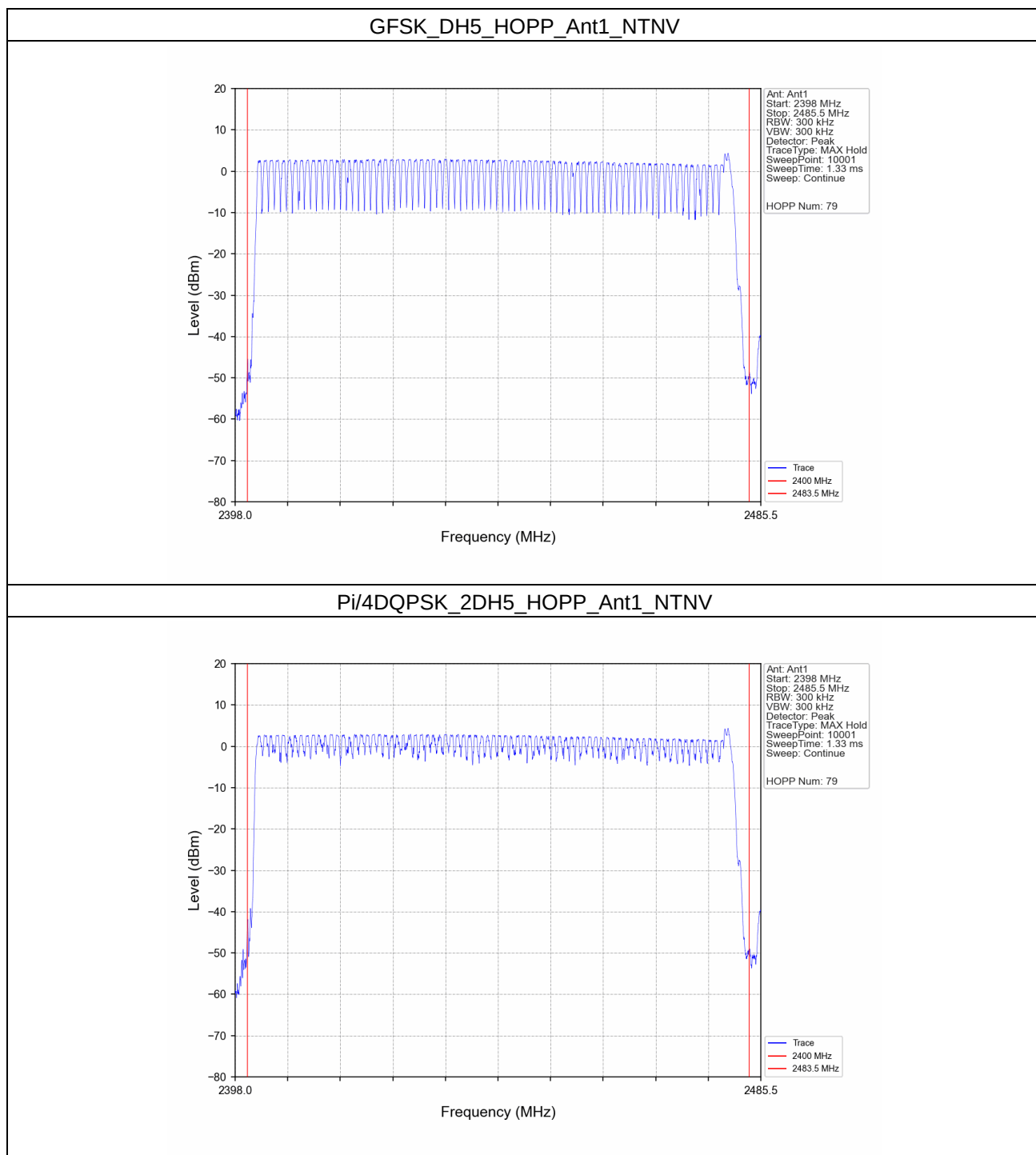
5.1 Test Result

5.1.1 HoppNum

Mode	TX Type	Frequency (MHz)	Packet Type	Num of Hopping Frequencies		Verdict
				ANT1	Limit	
GFSK	SISO	HOPP	DH5	79	>=15	Pass
PI/4DQPSK	SISO	HOPP	2DH5	79	>=15	Pass

5.2 Test Graph

5.2.1 HoppNum



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6. Time of Occupancy (Dwell Time)

6.1 Test Result

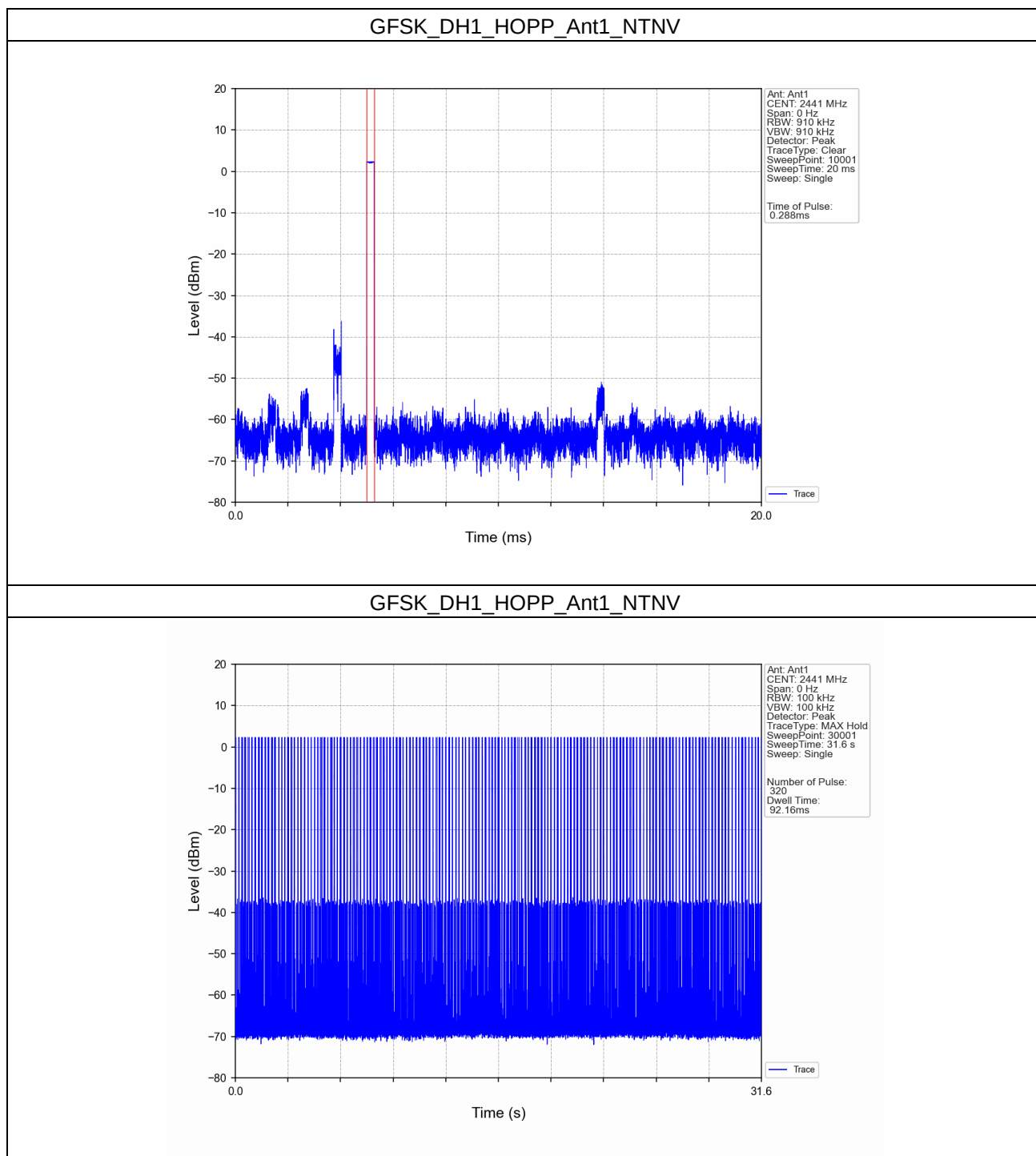
6.1.1 Ant1

Ant1									
Mode	TX Type	Frequency (MHz)	Packet Type	Duration of Single Pulse (ms)	Observation Period (s)	Num of Pulse in Observation Period	Dwell Time (ms)	Limit (ms)	Verdict
GFSK	SISO	HOPP	DH1	0.288	31.600	320	92.160	<=400	Pass
			DH3	0.288	31.600	320	92.160	<=400	Pass
			DH5	0.288	31.600	320	92.160	<=400	Pass
Pi/4DQPSK	SISO	HOPP	2DH1	0.288	31.600	320	92.160	<=400	Pass
			2DH3	0.286	31.600	319	91.234	<=400	Pass
			2DH5	0.288	31.600	319	91.872	<=400	Pass



6.2 Test Graph

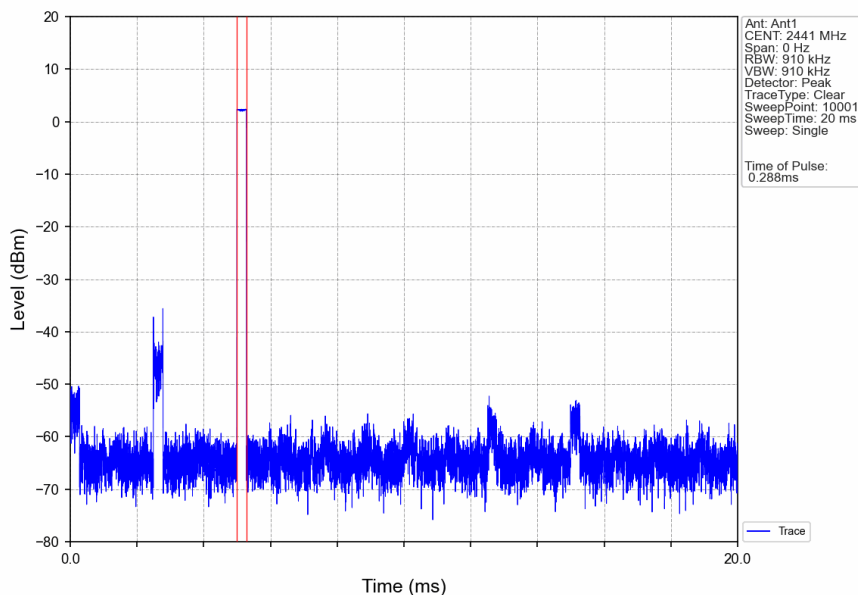
6.2.1 Ant1



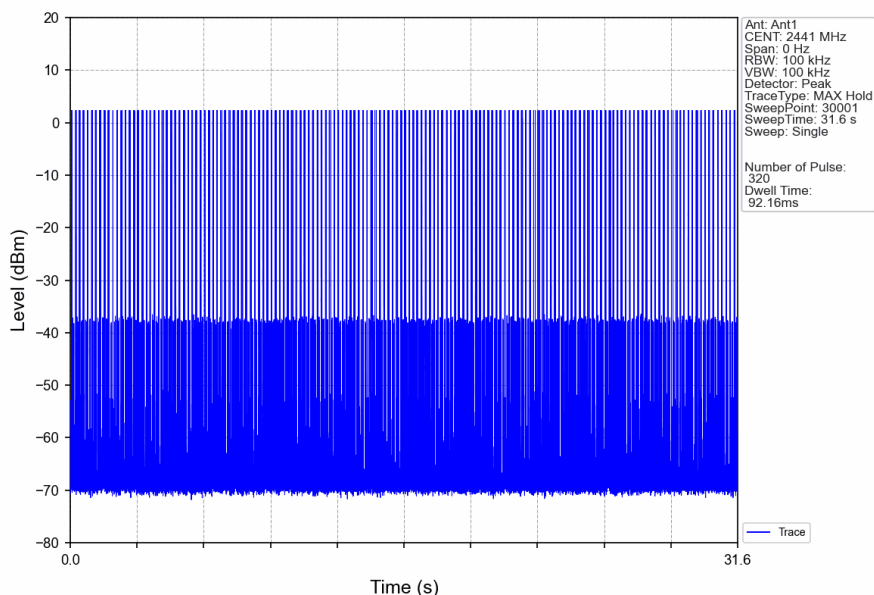
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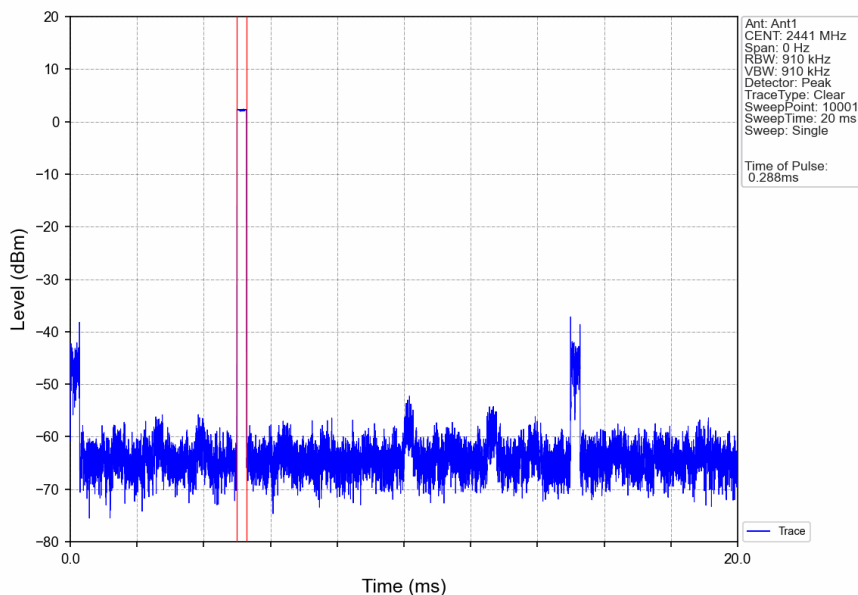
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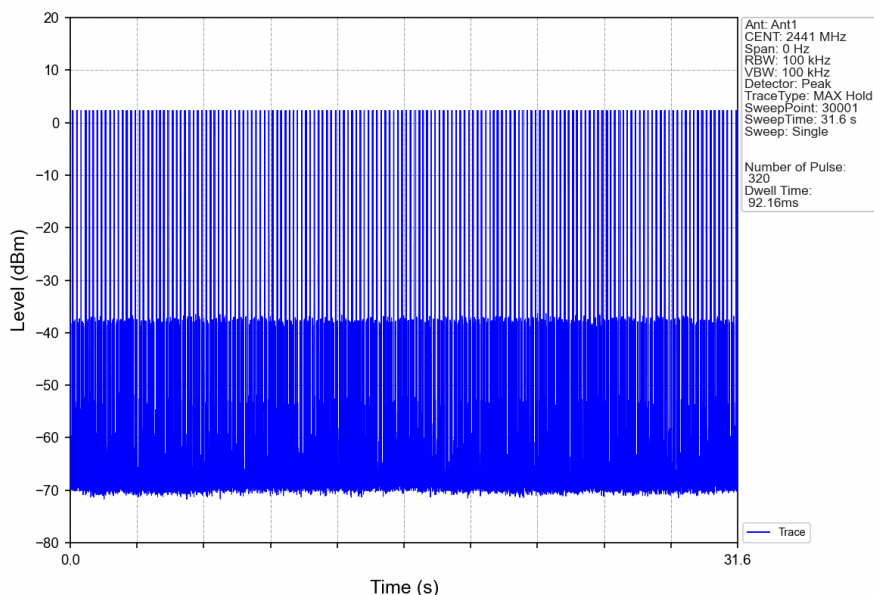
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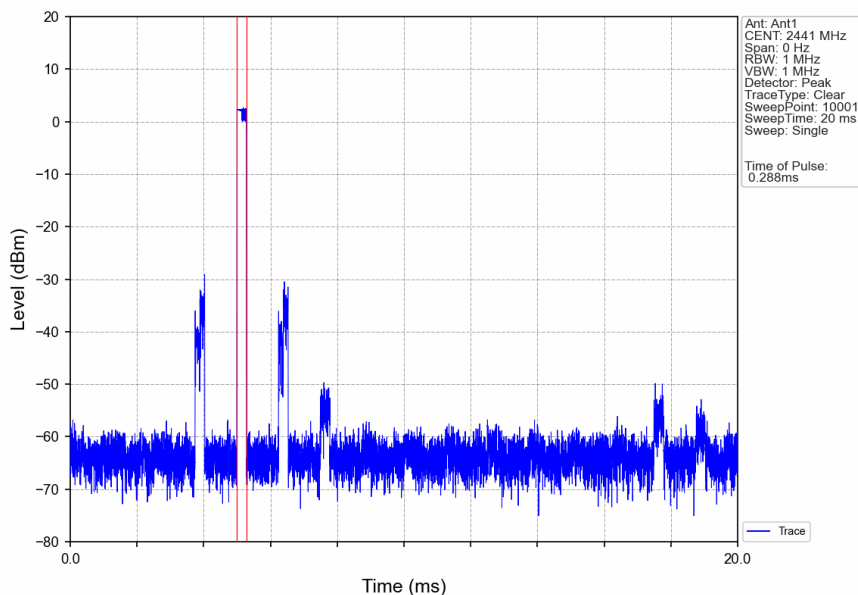
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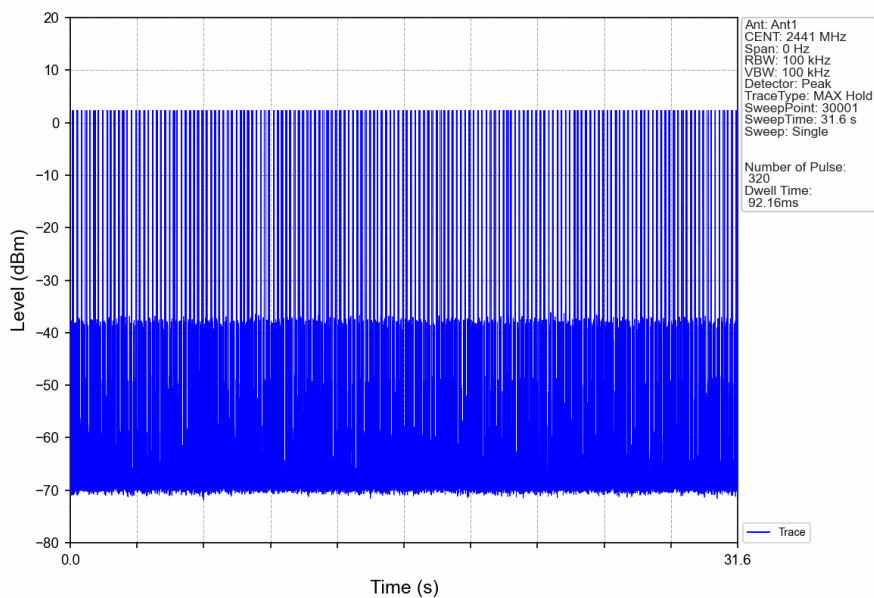
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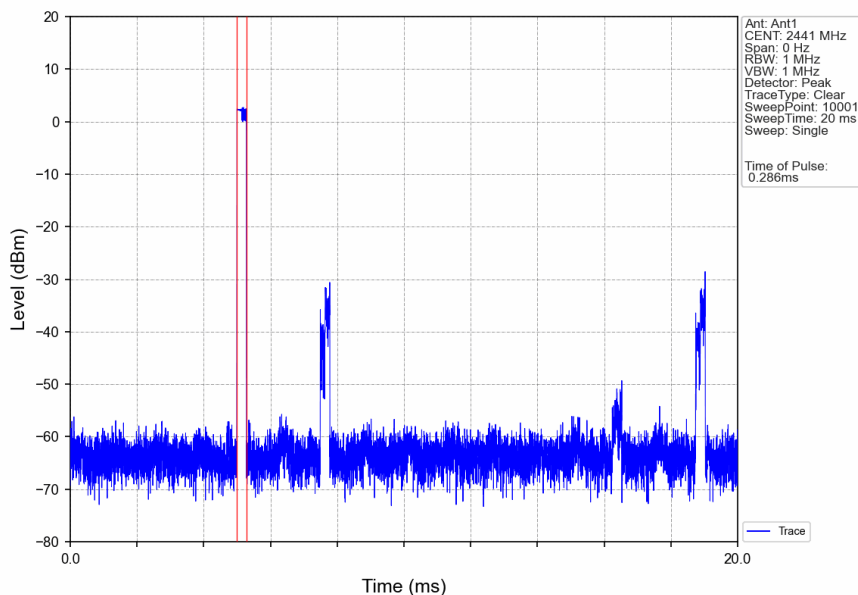
Pi/4DQPSK_2DH1_HOPP_Ant1_NTNV



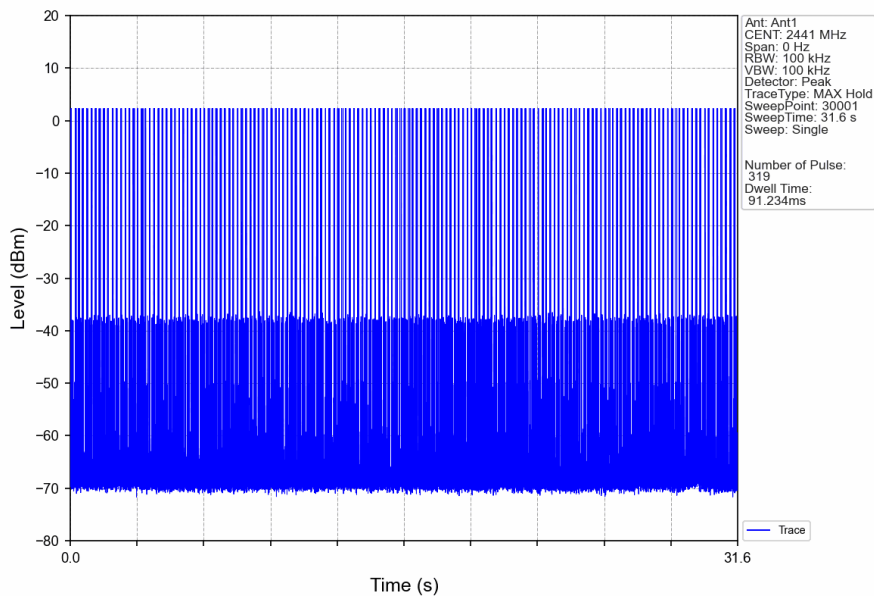
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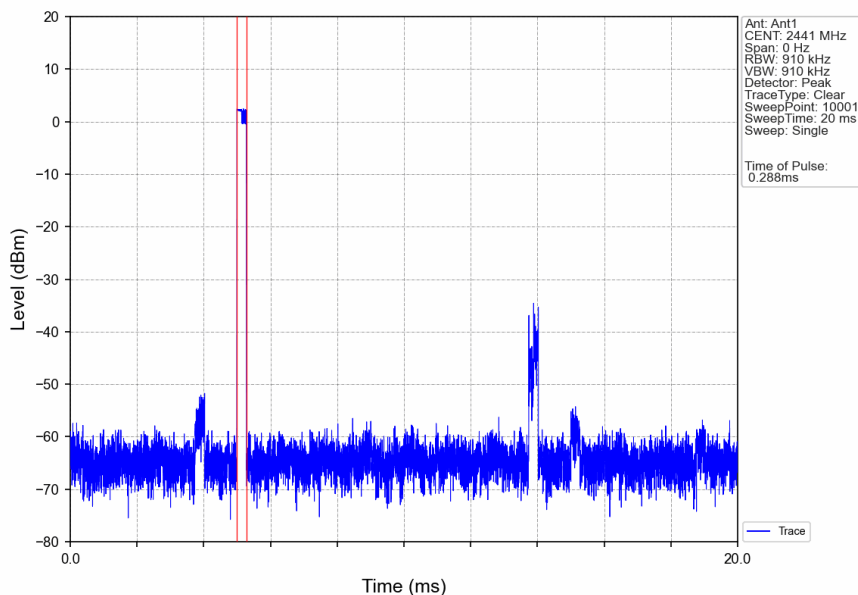
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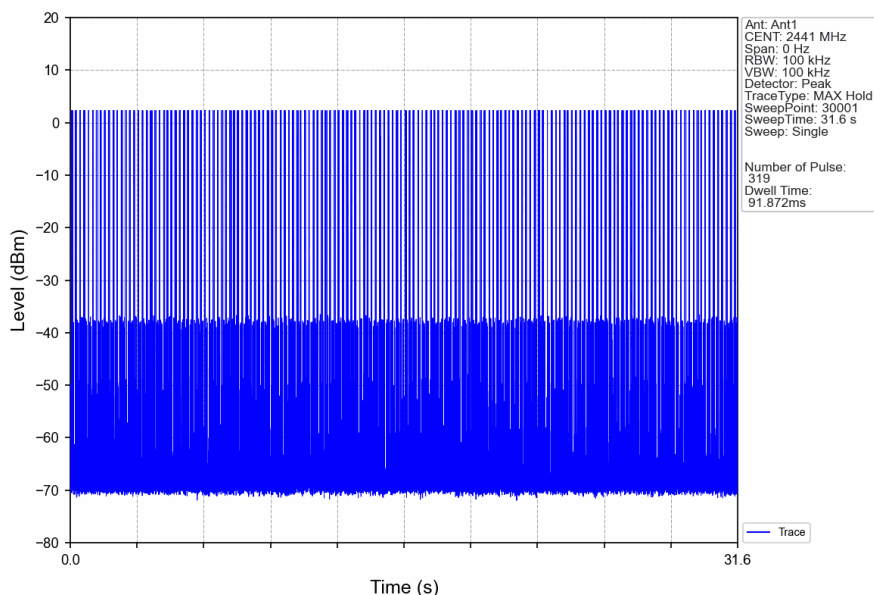
Pi/4DQPSK_2DH3_HOPP_Ant1_NTNV



Pi/4DQPSK_2DH5_HOPP_Ant1_NTNV



Pi/4DQPSK_2DH5_HOPP_Ant1_NTNV



7. Unwanted Emissions In Non-restricted Frequency Bands

7.1 Test Result

7.1.1 Ref

Mode	TX Type	Frequency (MHz)	Packet Type	ANT	Level of Reference (dBm)
GFSK	SISO	2402	DH5	1	2.31
		2441	DH5	1	2.36
		2480	DH5	1	3.94
Pi/4DQPSK	SISO	2402	2DH5	1	2.28
		2441	2DH5	1	2.37
		2480	2DH5	1	3.87

Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2013, the channel contains the maximum PSD level was used to establish the reference level.



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SZEMC-TRF-01 Rev. A/1

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7.1.2 CSE

Mode	TX Type	Frequency (MHz)	Packet Type	ANT	Level of Reference (dBm)	Limit (dBm)	Verdict
GFSK	SISO	2402	DH5	1	3.94	-16.06	Pass
		2441	DH5	1	3.94	-16.06	Pass
		2480	DH5	1	3.94	-16.06	Pass
		HOPP	DH5	1	3.94	-16.06	Pass
					3.94	-16.06	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	3.87	-16.13	Pass
		2441	2DH5	1	3.87	-16.13	Pass
		2480	2DH5	1	3.87	-16.13	Pass
		HOPP	2DH5	1	3.87	-16.13	Pass
					3.87	-16.13	Pass

Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2013, the channel contains the maximum PSD level was used to establish the reference level.



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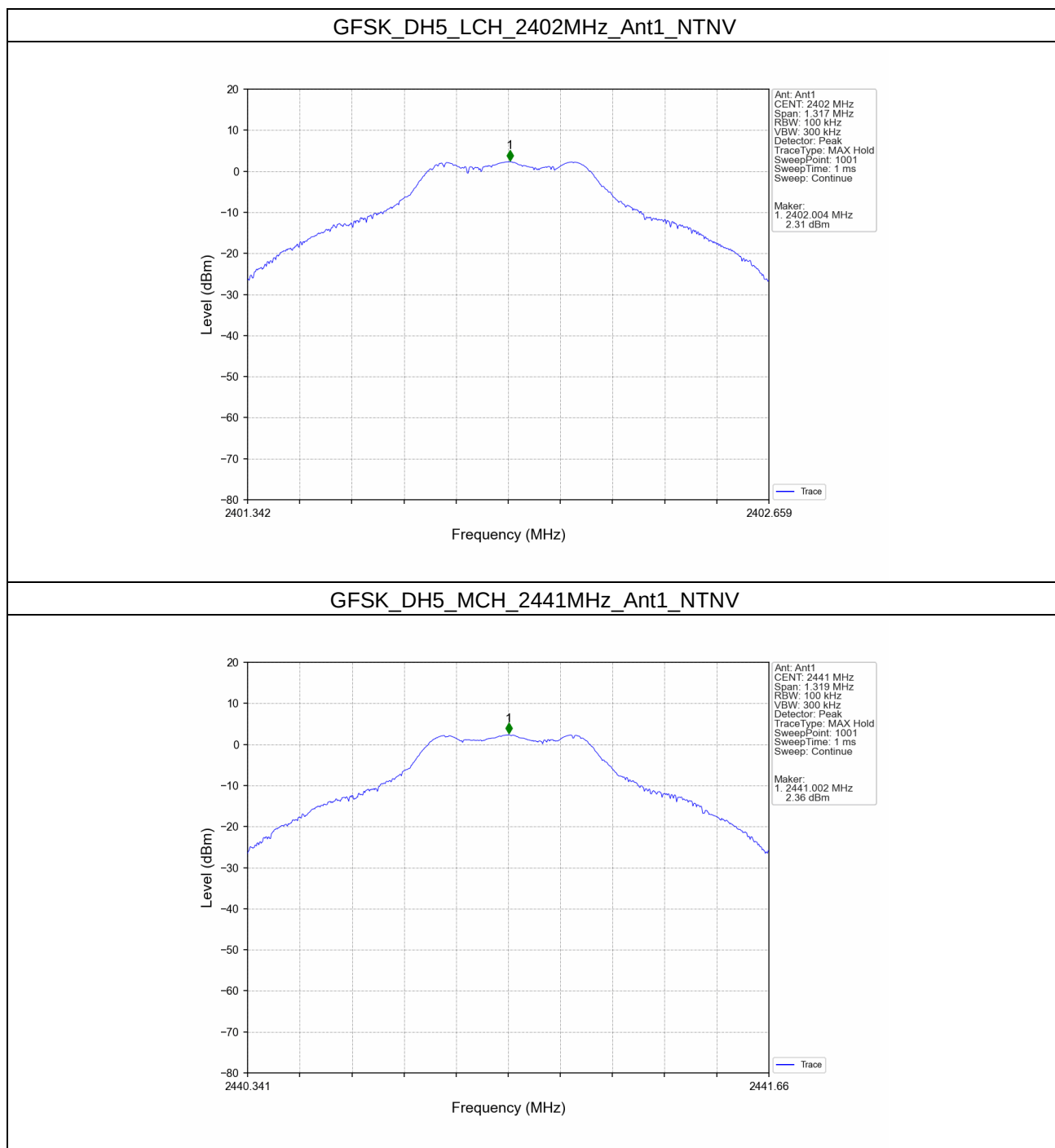
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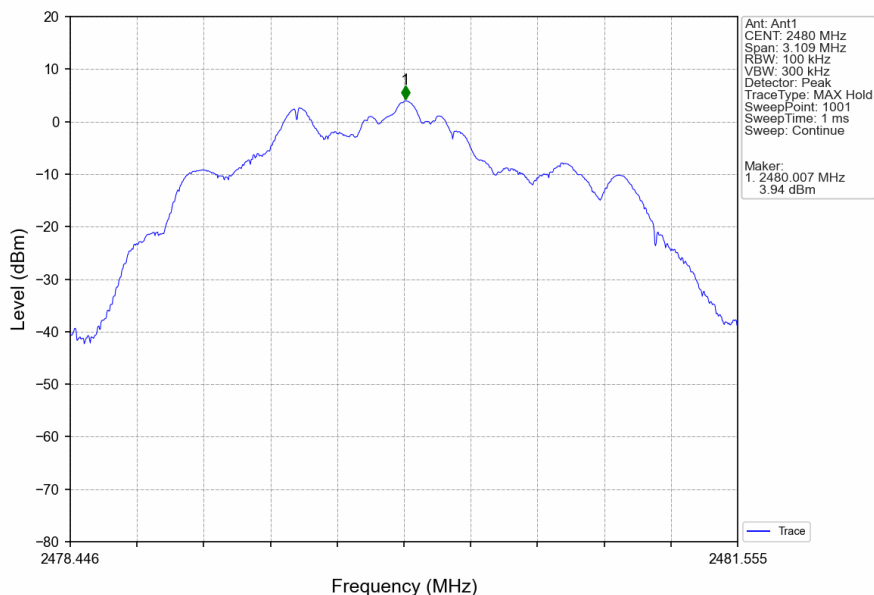
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7.2 Test Graph

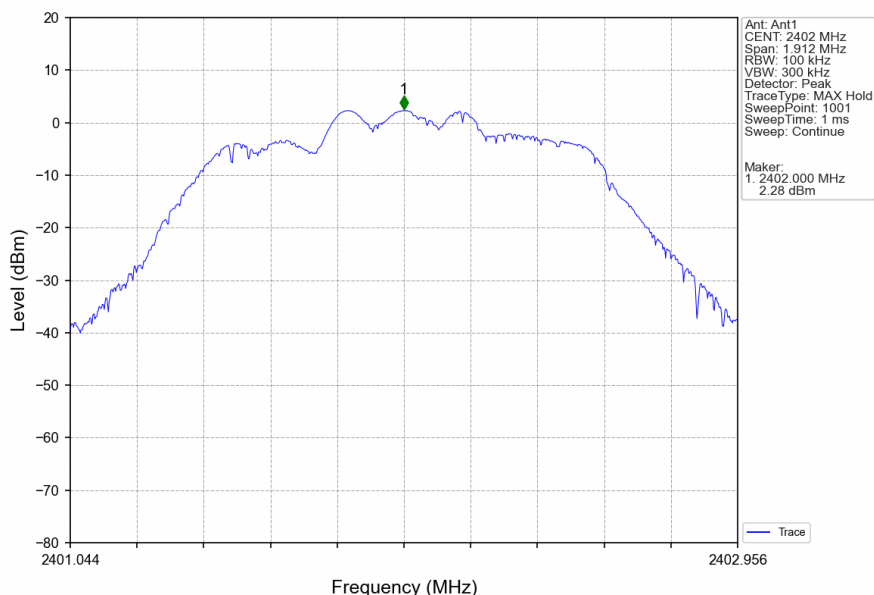
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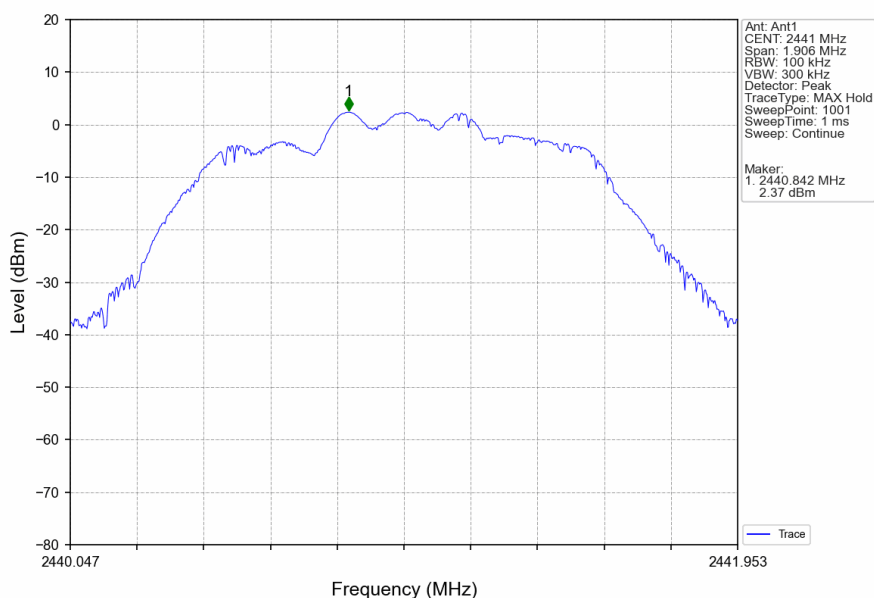
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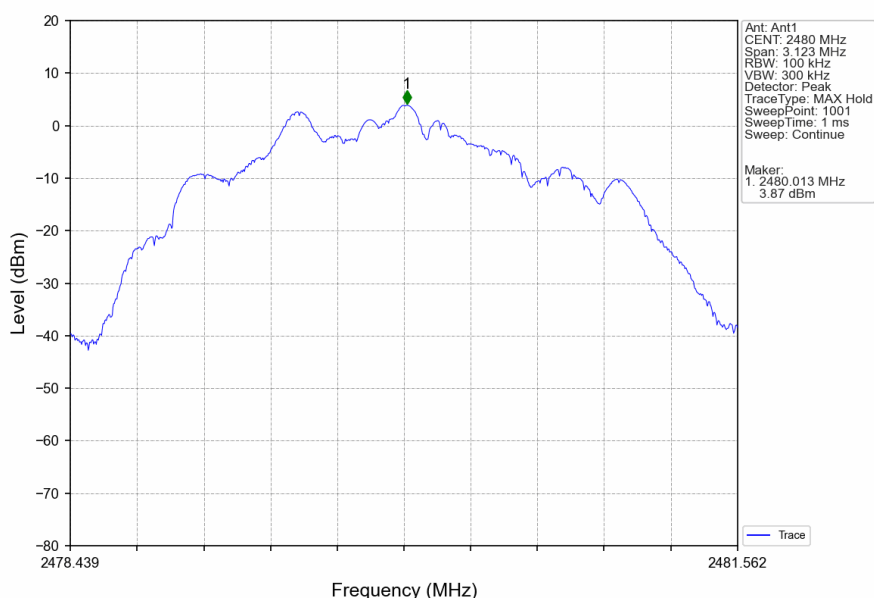
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV



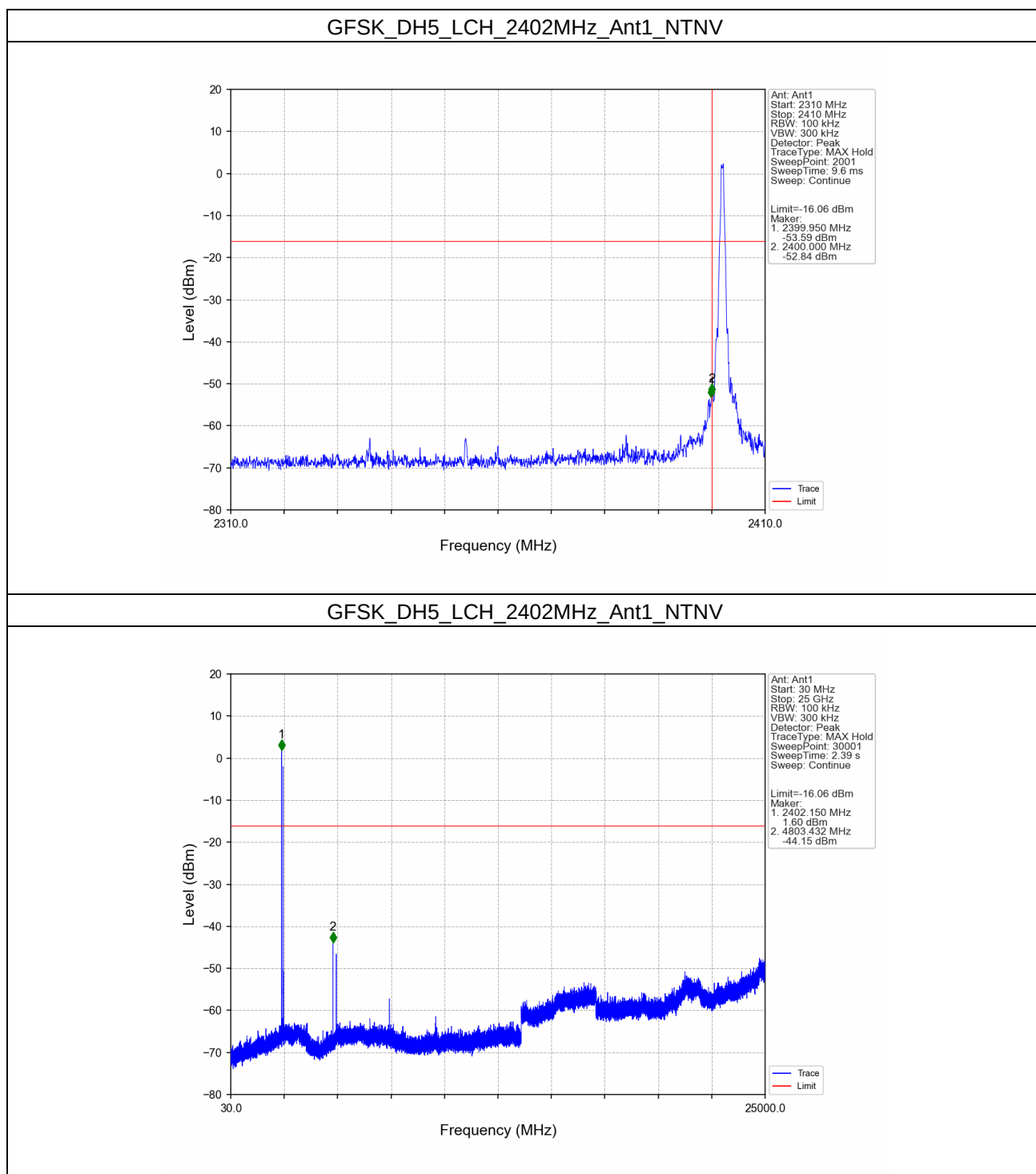
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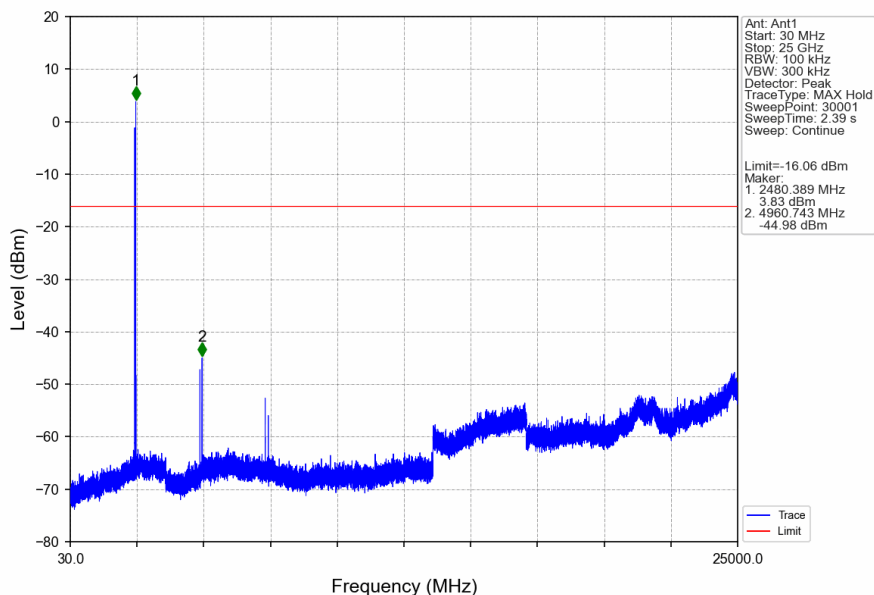
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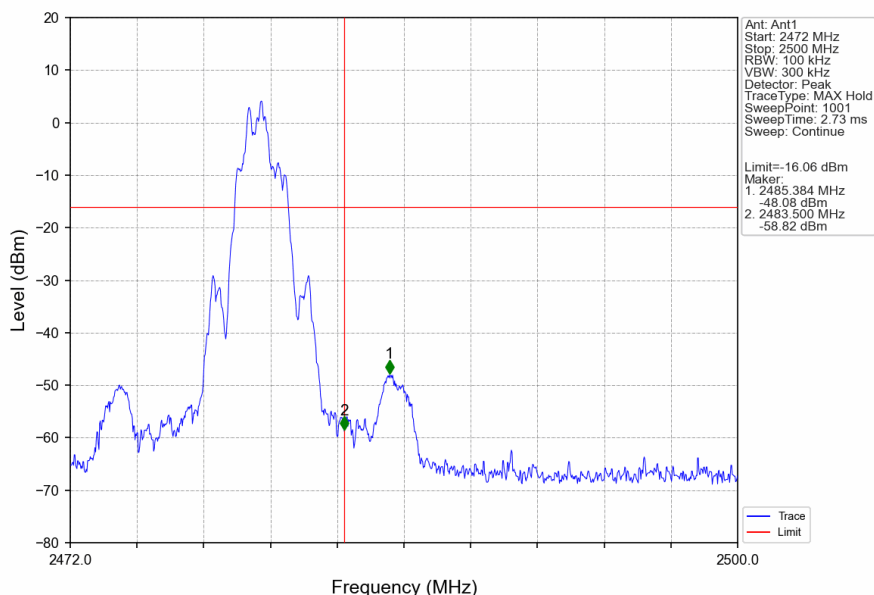
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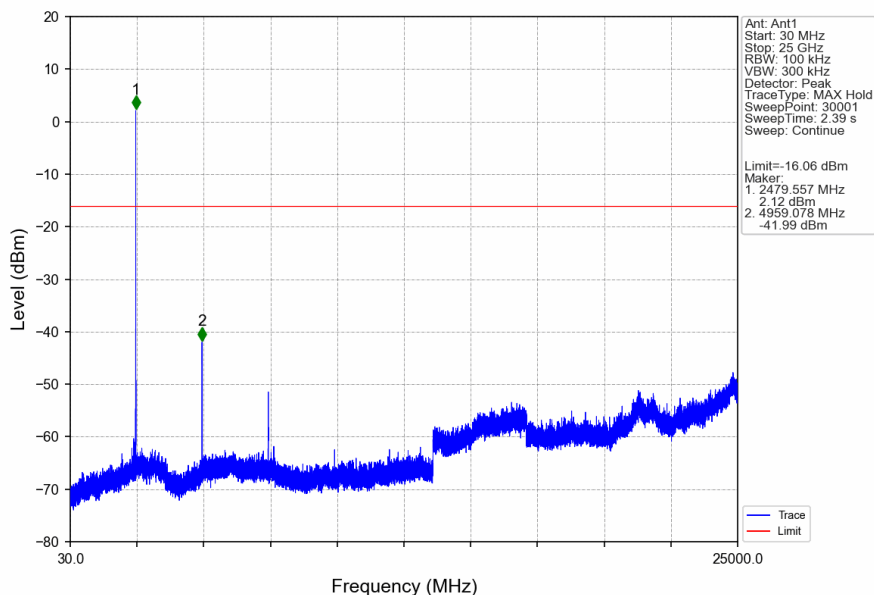
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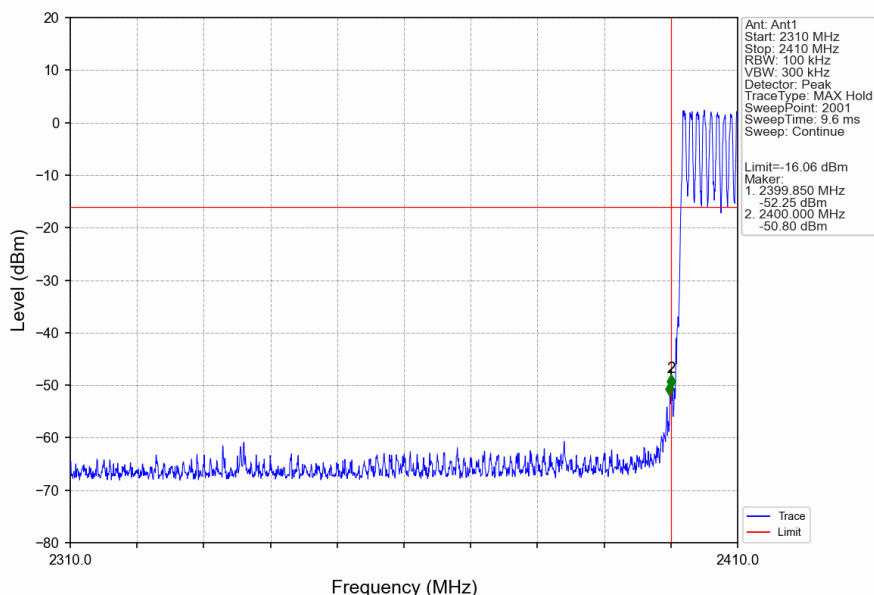
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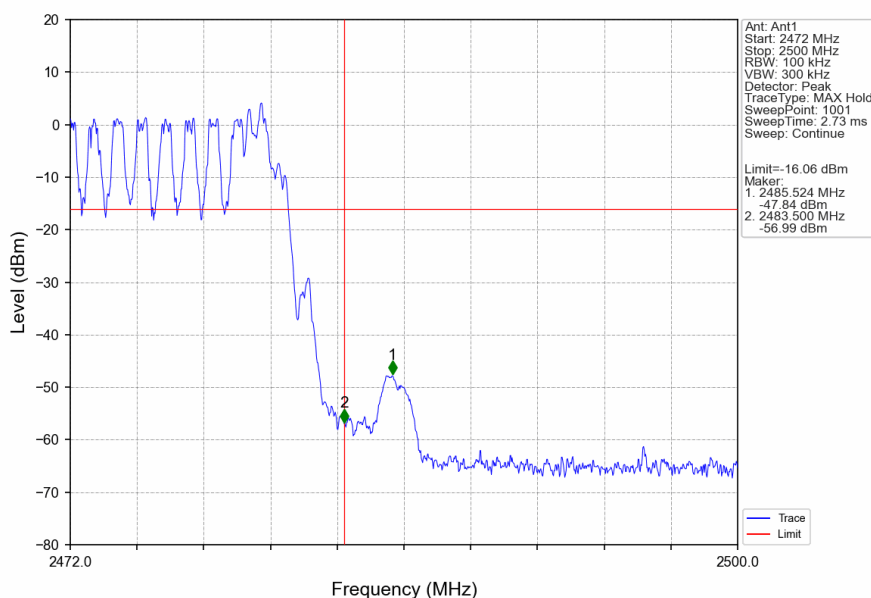
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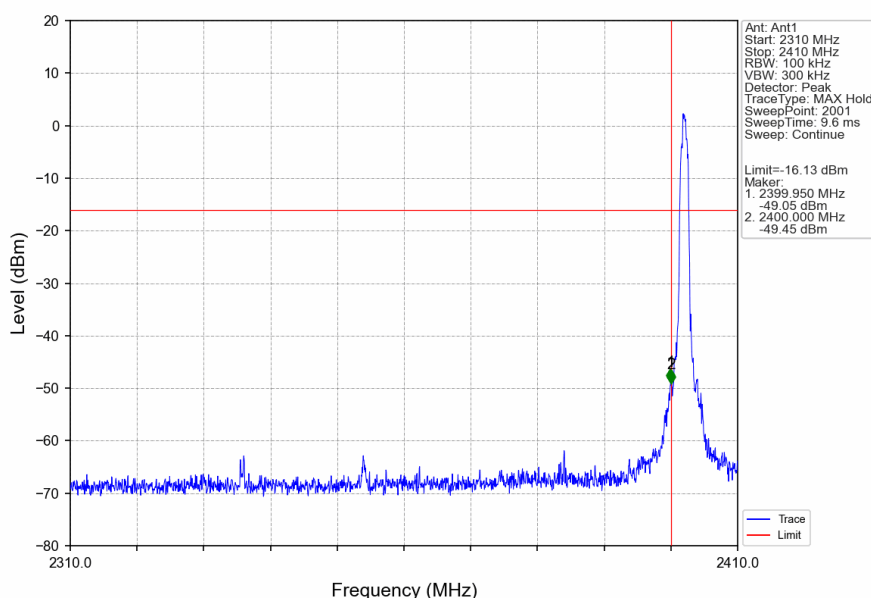
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GFSK_DH5_HOPP_Ant1_NTNV



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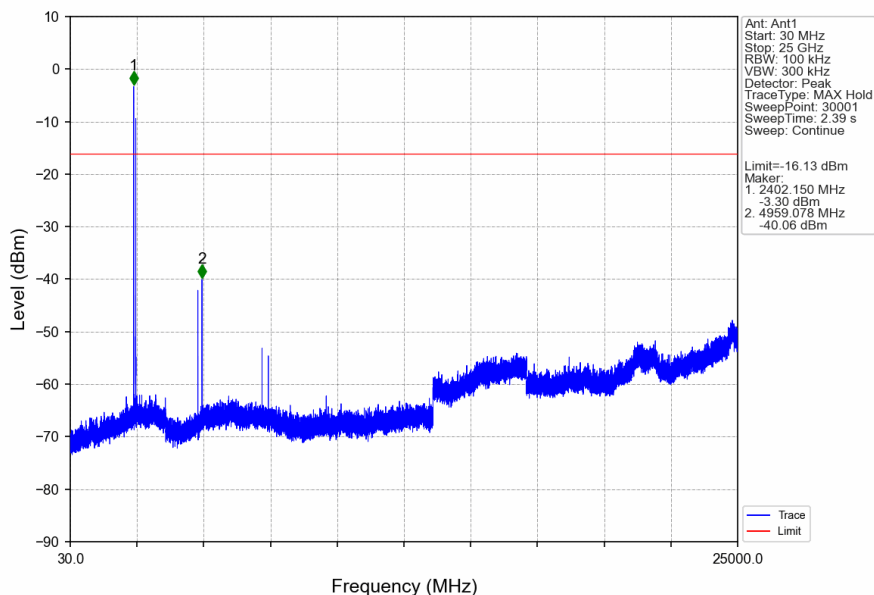
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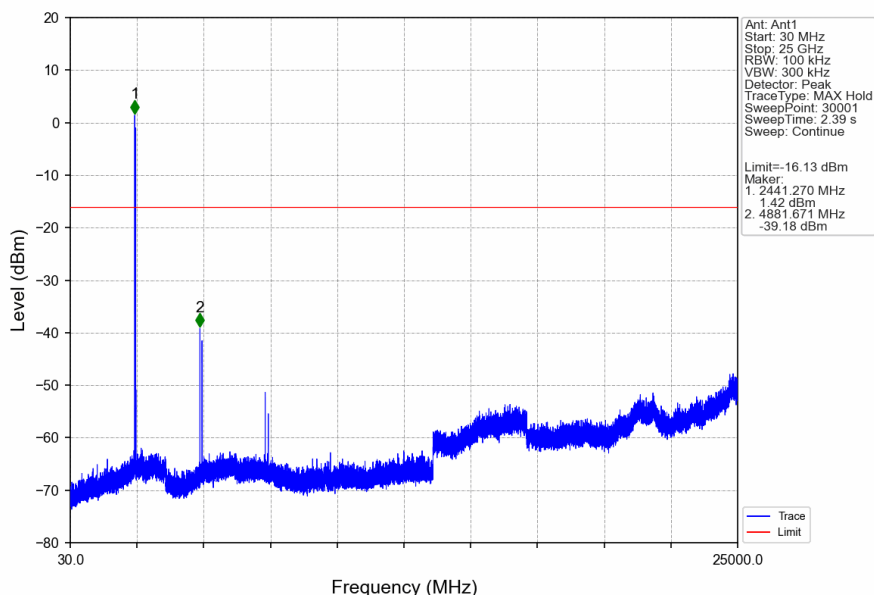
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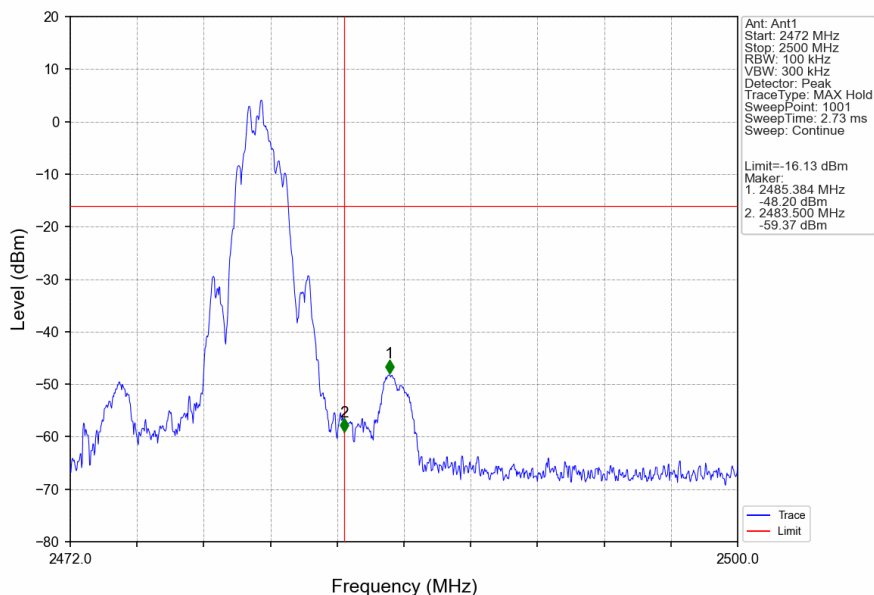
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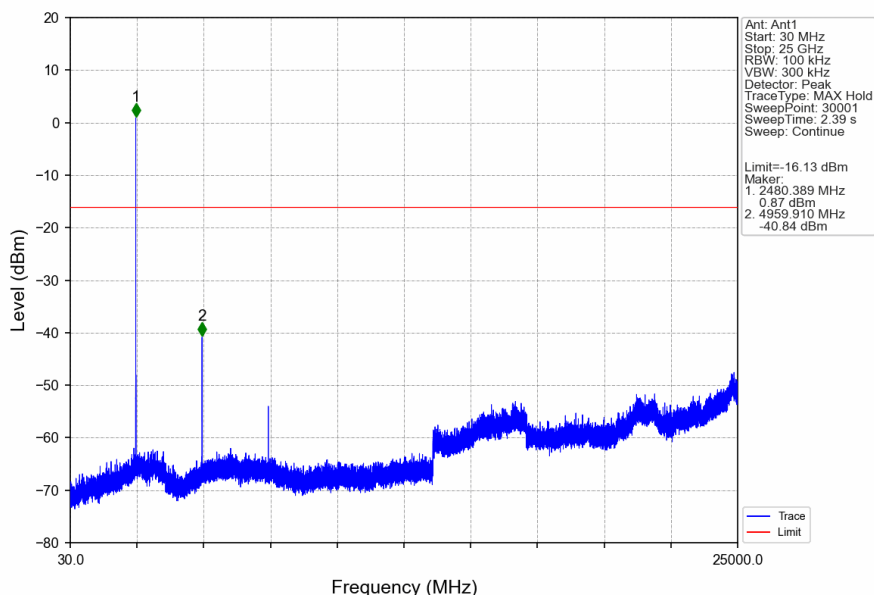
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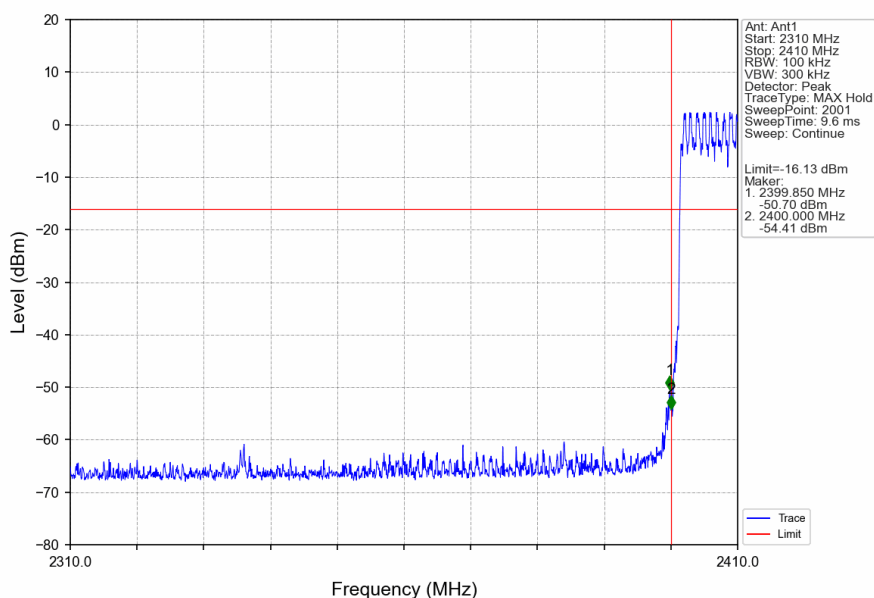
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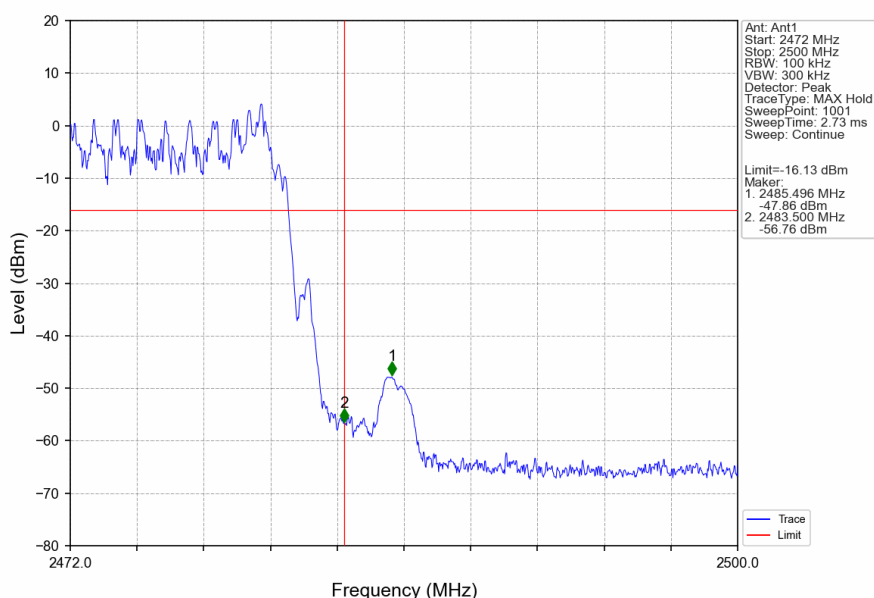
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Pi/4DQPSK_2DH5_HOPP_Ant1_NTNV



Pi/4DQPSK_2DH5_HOPP_Ant1_NTNV



- End of the Report -

