

1. Overview

LT2011(MP-PAC/MP-DIE) is a low-cost, low-power single-chip RF transceiver module operating in the 2.4GHz ISM band. LT2011 is designed for a small amount of data designed wireless controller.

Install: sealing-in other device. (for example)



If using a permanently affixed label, the modular transmitter must be labeled with its own FCC identification number, and, if the FCC identification number is not visible when the module is installed inside another device, then the outside of the device into which the module is installed must also display a label referring to the enclosed module. This exterior label can use wording such as the following: "Contains Transmitter Module FCC ID: Z96MP-PAC." The Grantee may either provide such a label, an example of which must be included in the application for the equipment authorization, or, must provide adequate instructions along with the module which explains this requirement. In the latter case, a copy of these instructions must be included in the application for equipment authorization.

NOTE: Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate this equipment.

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- (1) This device may not cause harmful interference, and
- (2) This device must accept any interference received, including interference that may cause undesired operation.

This device and its antenna(s) must not be co-located or operating in conjunction with any other antenna or transmitter.

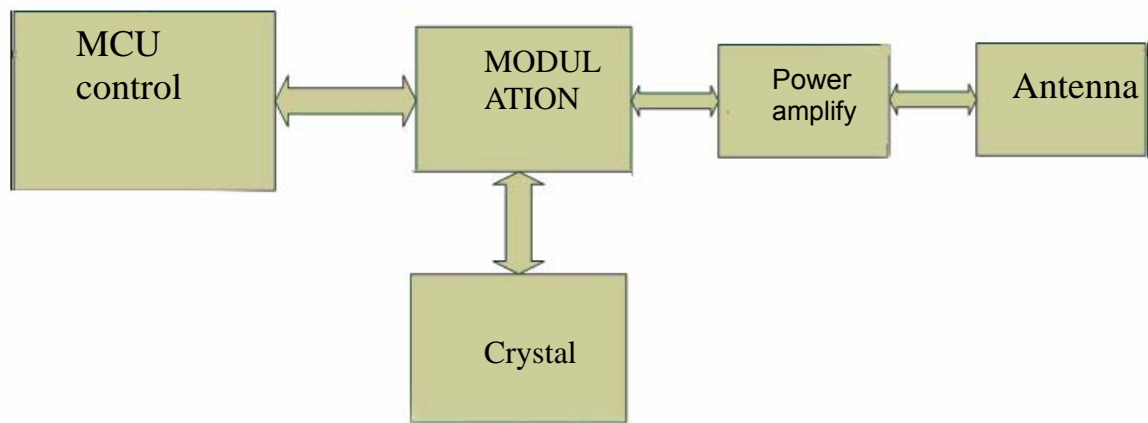
Modulation type:MSK/FSK.

Statement: this module is not used with PC.

2.Features

- ◆ Frequency between the 2412MHz ~ 2472MHz;
- ◆ Design of the output power up to +13 dbm or +14dbm;

3、 Module Block Diagram



4、 Modulation

LT2011 supports amplitude modulation, frequency shift keying and phase shift keying mode

a) Amplitude modulation

LT2011 supports two different forms of amplitude modulationmode: off keying and amplitude shift keying. Off keyingmodulation, respectively, through simply set to "1" or "0" to turn onor turn off power make a big function, and amplitude shift keyingcan be programmed to adjust to a more sophisticated range ofsizes.

b) Frequency Shift Keying

FSK symbol coding as follows:

Format	Symbol	Coding
2FSK	'0'	– Deviation
	'1'	+ Deviation

c) Phase shift keying

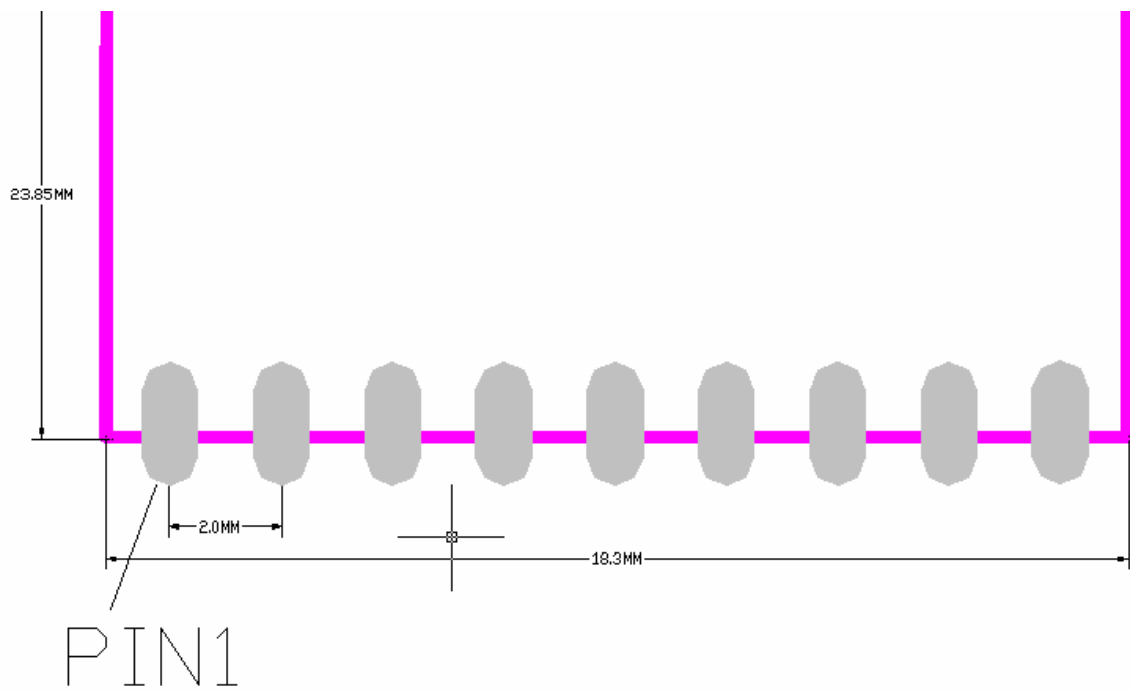
Phase shift keying is in transit to maintain a certain Period to transmit data.

5、Limit rating

Parameter	Min	Max	Unit	Remarks
Input voltage	2.8	3.6	V	All power supply pin have the same voltage input
Max output power		14	dbm	
Max data transfer rate		250	Kbps	
Operating Temperature	–40	85	° C	
Storage Temperature	–50	150	° C	

6、Module size and pin description

Module size



pin description

Pin	Name	Pin Function	Description
1	VCC	Power supply	Positive power supply
2	SI	Digital input	Configuration mode input
3	SCLK	Digital input	Configuration mode, the clock input
4	SO	Digital input	Configuration mode output
5	GD02	Digital input	Configuration mode to read data
6	GND	Ground	Ground
7	GD00	Digital input / output	As a general purpose digital output
8	CS _n	Digital input	Chip select Configuration mode
9	PA_EN	Digital input	PA switch

Integration

