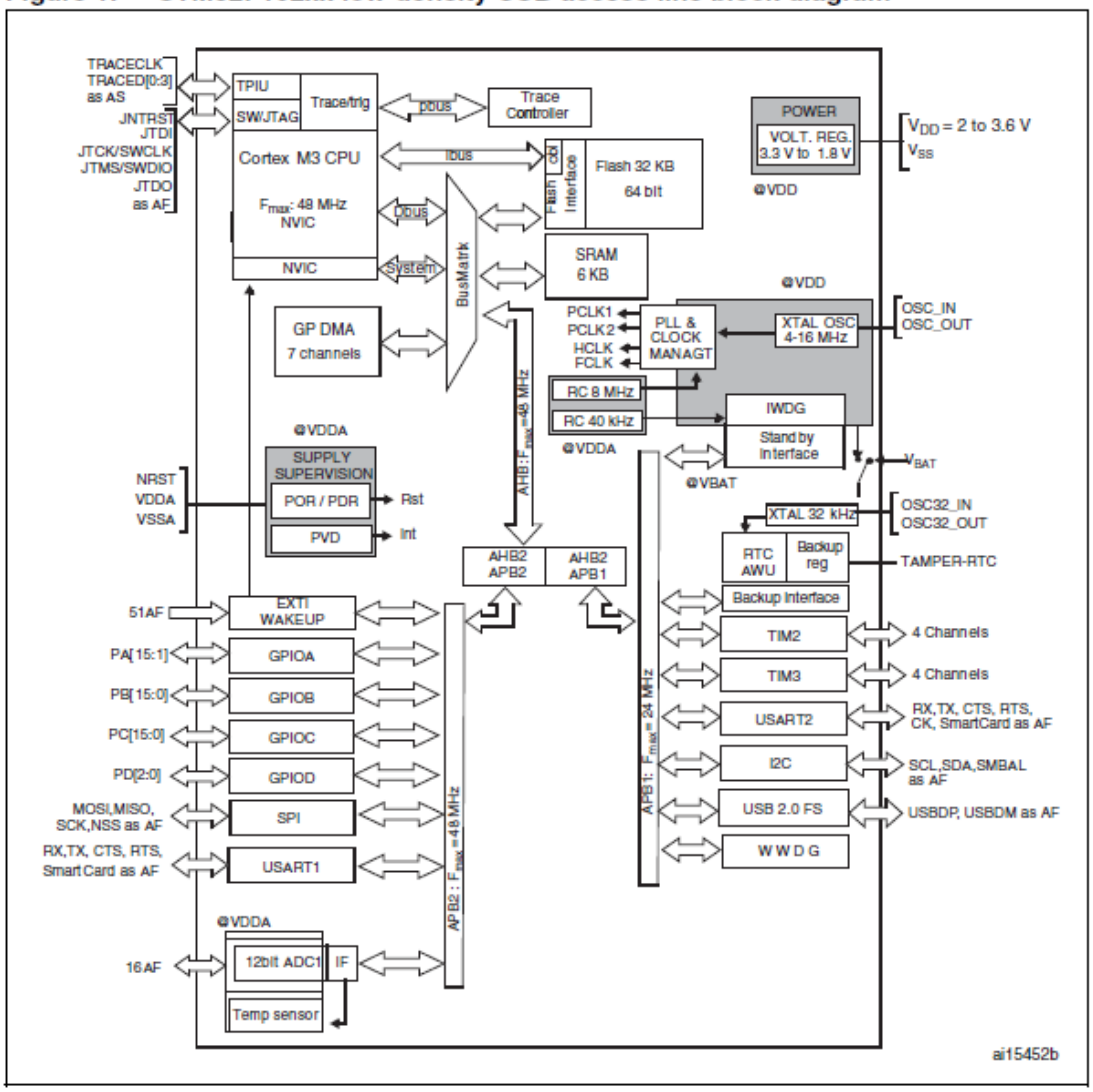
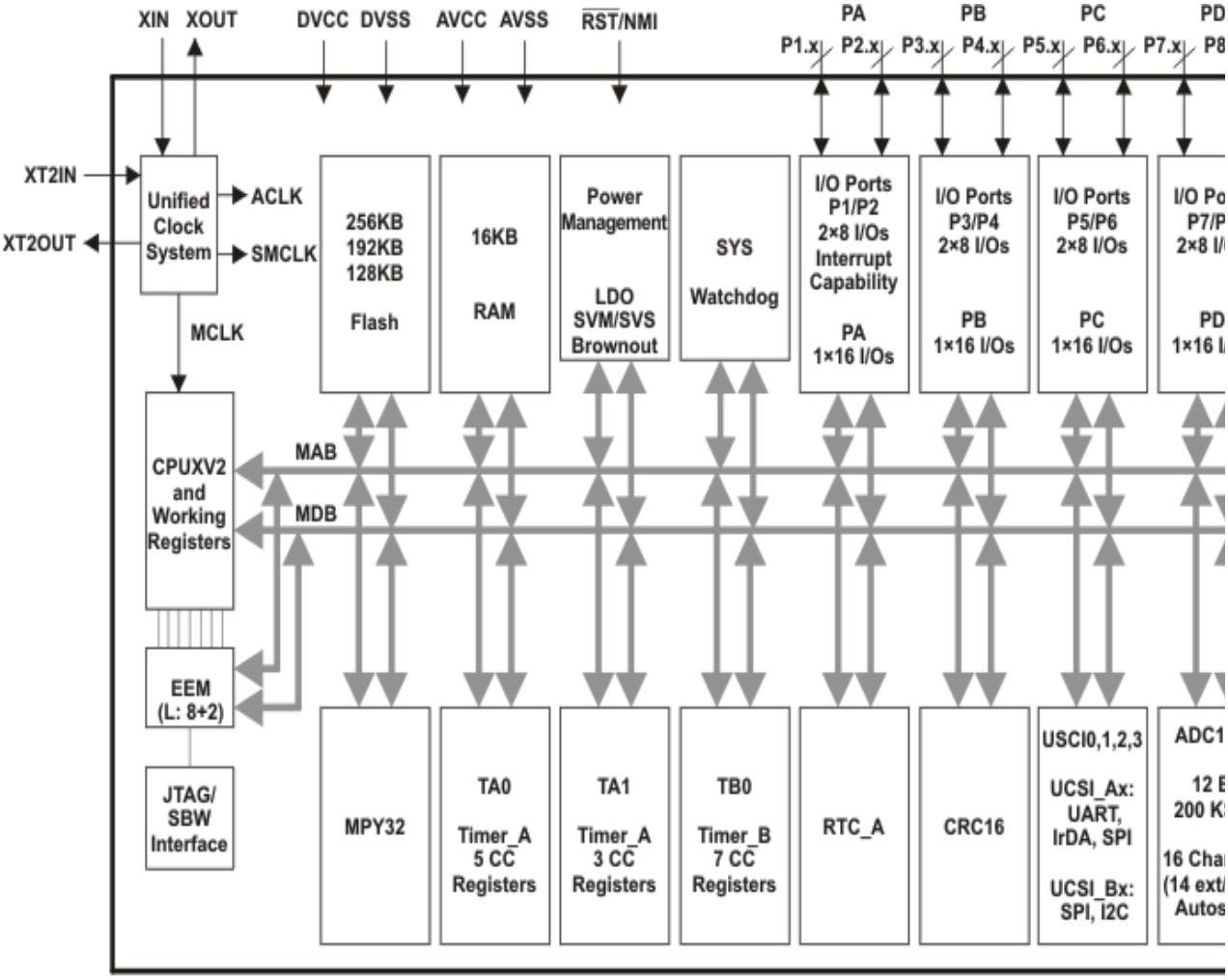
MCU:STM32F102C4T6A

Figure 1. STM32F102xx low-density USB access line block diagram



1. AF = alternate function on I/O port pin.
2. T_A = –40 °C to +85 °C (junction temperature up to 105 °C).

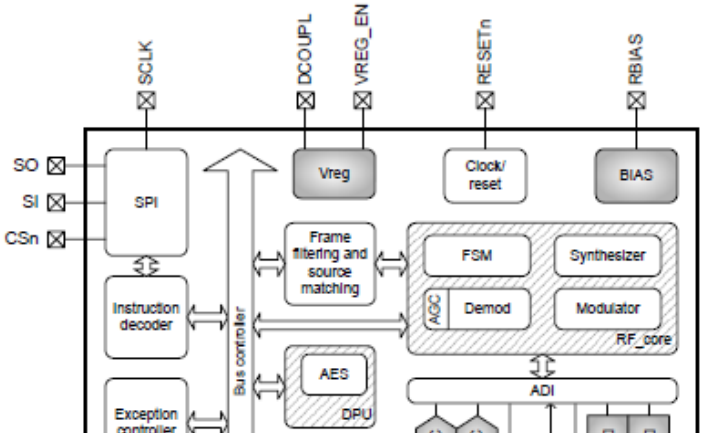
MSP430F5437IPN



Note: Supported memory, peripherals, and ports may vary depending upon the device.

CC2520

8.3 Block Diagram



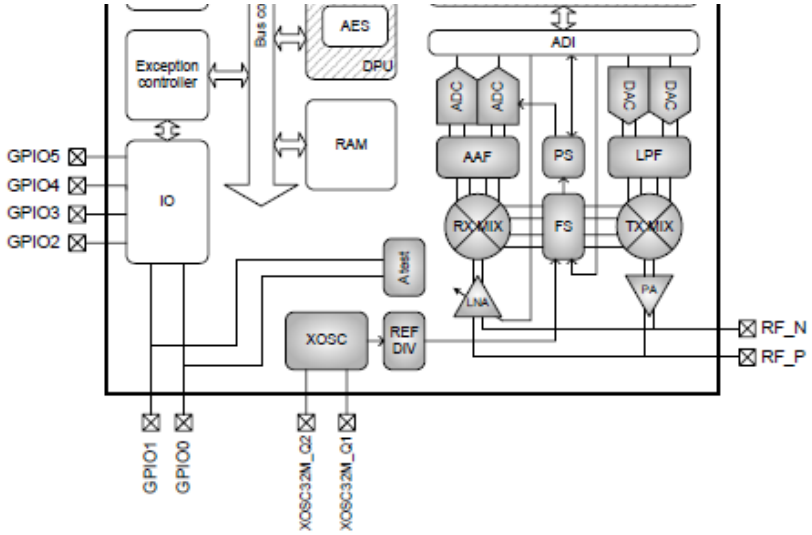
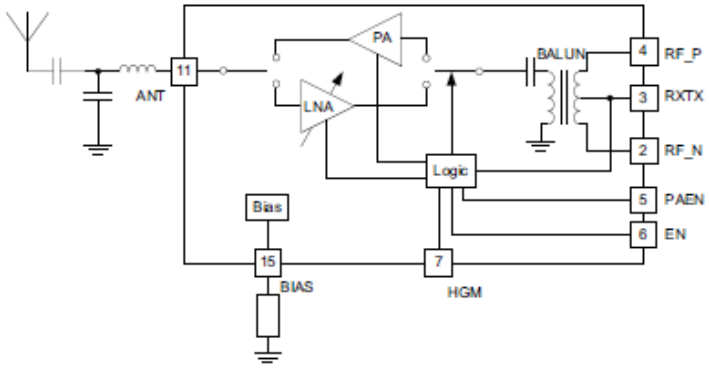
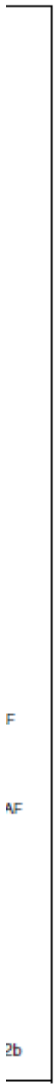


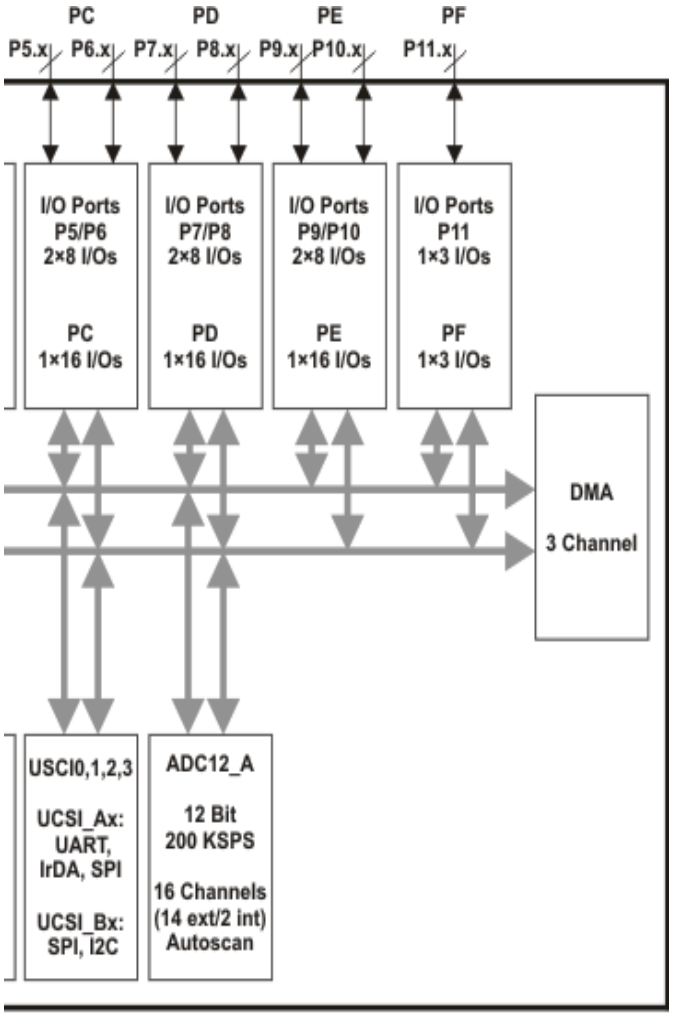
Figure 2: CC2520 block diagram

CC2591

CC2591 BLOCK DIAGRAM




F
AF
2b


PC
PD
PE
PF
P5.x
P6.x
P7.x
P8.x
P9.x
P10.x
P11.x
I/O Ports P5/P6 2×8 I/Os
PC 1×16 I/Os
I/O Ports P7/P8 2×8 I/Os
PD 1×16 I/Os
I/O Ports P9/P10 2×8 I/Os
PE 1×16 I/Os
I/O Ports P11 1×3 I/Os
PF 1×3 I/Os
DMA
3 Channel
USCI0,1,2,3
UCSI_Ax: UART, IrDA, SPI
UCSI_Bx: SPI, I2C
ADC12_A
12 Bit 200 KSPS
16 Channels (14 ext/2 int) Autoscan

rice.