9 kHz ~ 25 GHz Data (***Hopping mode***)

▪ Modulation : GFSK

Frequency (MHz)	ANT Pol	The worst case EUT Position (Axis)	Detector Mode	Reading (dBuV)	T.F (dB/m)	D.C.F (dB)	Distance Factor (dB)	Result (dBuV/m)	Limit (dBuV/m)	Margin (dB)
2388.22	V	X	PK	46.22	0.78	N/A	N/A	47.00	74.00	27.00
2387.96	V	X	AV	36.83	0.78	-24.79	N/A	12.82	54.00	41.18
2483.56	V	X	PK	47.18	1.16	N/A	N/A	48.34	74.00	25.66
2483.51	V	X	AV	37.66	1.16	-24.79	N/A	14.03	54.00	39.97

▪ Modulation : π/4DQPSK

Frequency (MHz)	ANT Pol	The worst case EUT Position (Axis)	Detector Mode	Reading (dBuV)	T.F (dB/m)	D.C.F (dB)	Distance Factor(dB)	Result (dBuV/m)	Limit (dBuV/m)	Margin (dB)
2388.50	V	X	PK	46.82	0.78	N/A	N/A	47.60	74.00	26.40
2388.95	V	X	AV	37.67	0.78	-24.79	N/A	13.66	54.00	40.34
2483.83	V	X	PK	47.96	1.16	N/A	N/A	49.12	74.00	24.88
2483.54	V	X	AV	37.17	1.16	-24.79	N/A	13.54	54.00	40.46

▪ Modulation : 8DPSK

Frequency (MHz)	ANT Pol	The worst case EUT Position (Axis)	Detector Mode	Reading (dBuV)	T.F (dB/m)	D.C.F (dB)	Distance Factor (dB)	Result (dBuV/m)	Limit (dBuV/m)	Margin (dB)
2386.41	V	X	PK	46.93	0.78	N/A	N/A	47.71	74.00	26.29
2386.03	V	X	AV	37.81	0.78	-24.79	N/A	13.80	54.00	40.20
2483.59	V	X	PK	46.19	1.16	N/A	N/A	47.35	74.00	26.65
2483.52	V	X	AV	36.95	1.16	-24.79	N/A	13.32	54.00	40.68

▪ **Note.**

1. No other spurious and harmonic emissions were found greater than listed emissions on above table.
2. Information of Distance Factor

   For finding emissions, the test distance might be reduced from 3m to 1m. In this case, the distance factor(-9.54dB) is applied to the result.

   - Calculation of distance factor = 20 log(applied distance / required distance) = **20 log(1 m / 3 m)** = **-9.54 dB**

   When distance factor is "N/A", the distance is 3 m and distance factor is not applied.
3. D.C.F Calculation. (D.C.F = Duty Cycle Correction Factor)
   - Time to cycle through all channels **= Δt =** T [ms] X 20 minimum hopping channels , where T = pulse width = **2.88 ms**
   - 100 ms / Δt [ms] = H -> Round up to next highest integer, to account for worst case, H' = **100 / (2.88 X 20)** = **1.736** ≒ **2**
   - The Worst Case Dwell Time = T [ms] x H' = **2.88 ms X 2** = **5.76 ms**
   - D.C.F = 20 Log(The Worst Case Dwell Time / 100 ms) dB = **20 log(5.76 / 100)** = **-24.79 dB**
4. Sample Calculation.

   Margin = Limit – Result / Result = Reading + T.F + D.C.F / T.F = AF + CL – AG

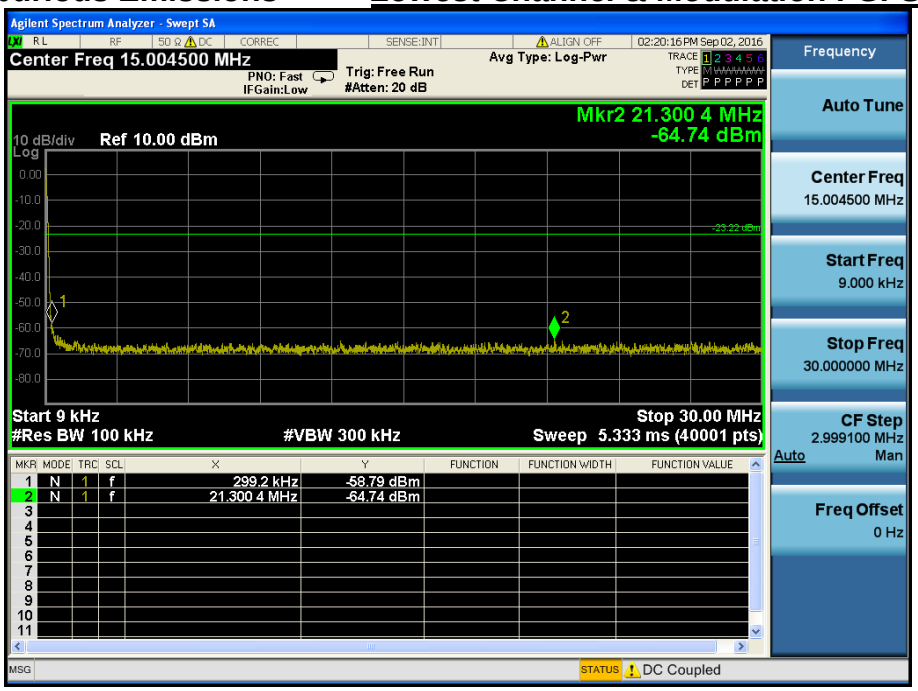
   Where, T.F = Total Factor, AF = Antenna Factor, CL = Cable Loss, AG = Amplifier Gain.

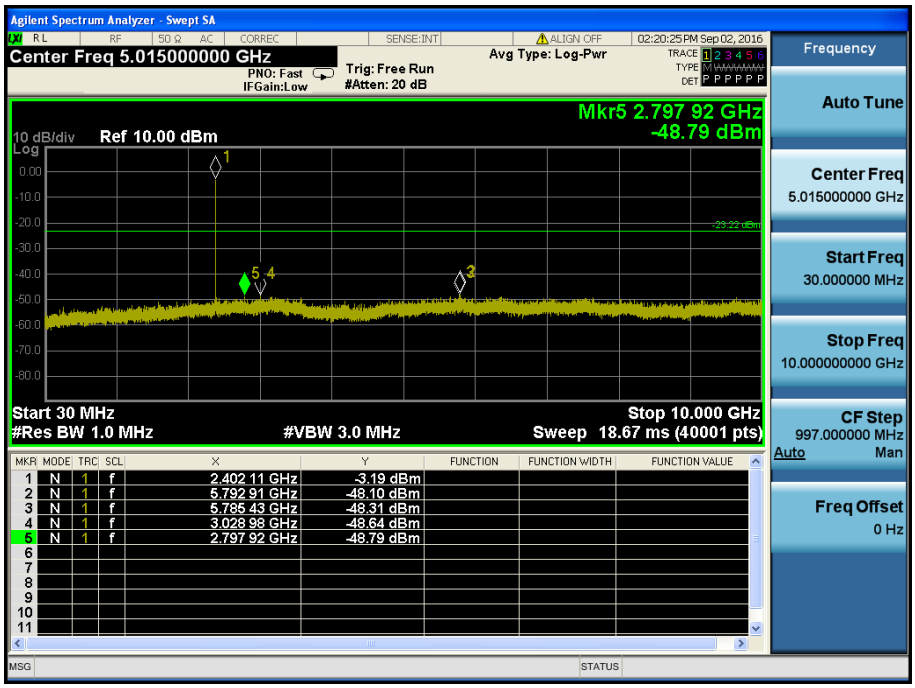
7.4.2. Conducted Spurious Emissions

Low Band-edge ***Lowest Channel & Modulation : GFSK***

Low Band-edge ***Hopping mode & Modulation : GFSK***

Conducted Spurious Emissions ***Lowest Channel & Modulation : GFSK***

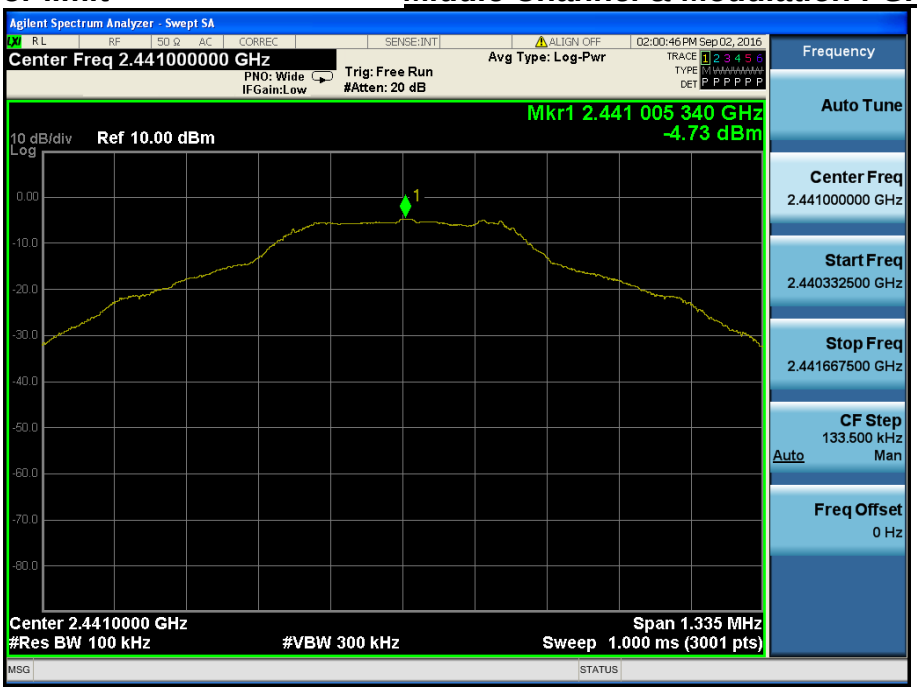




Conducted Spurious Emissions ***Lowest Channel & Modulation : GFSK***

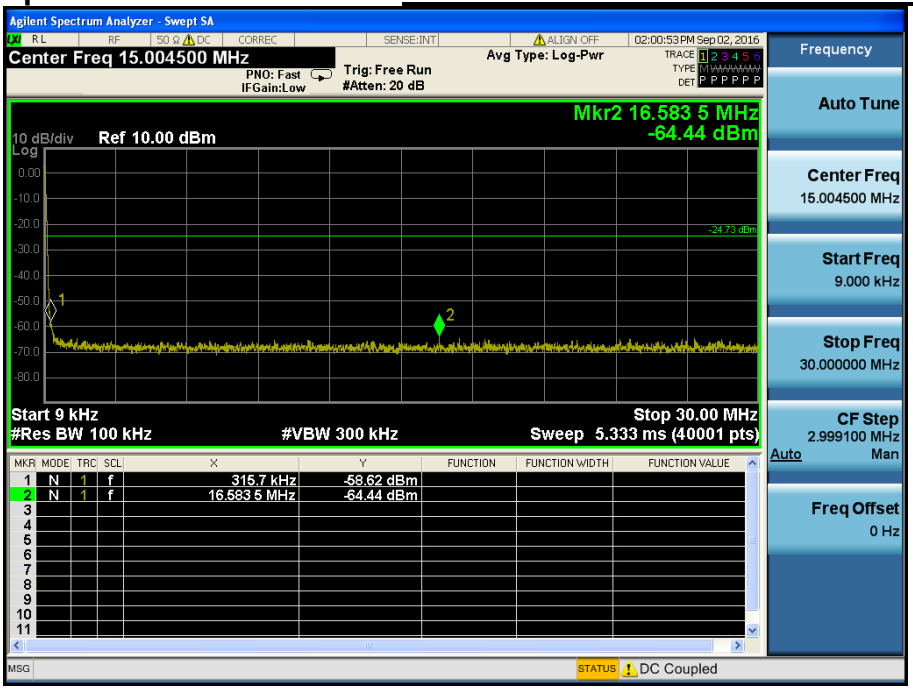
Reference for limit

Middle Channel & Modulation : GFSK

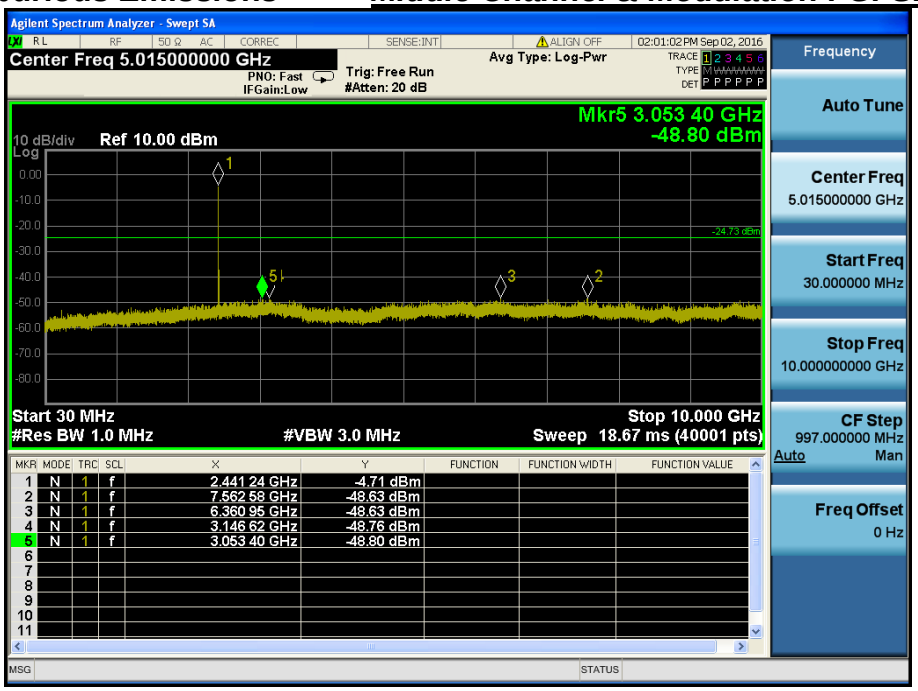


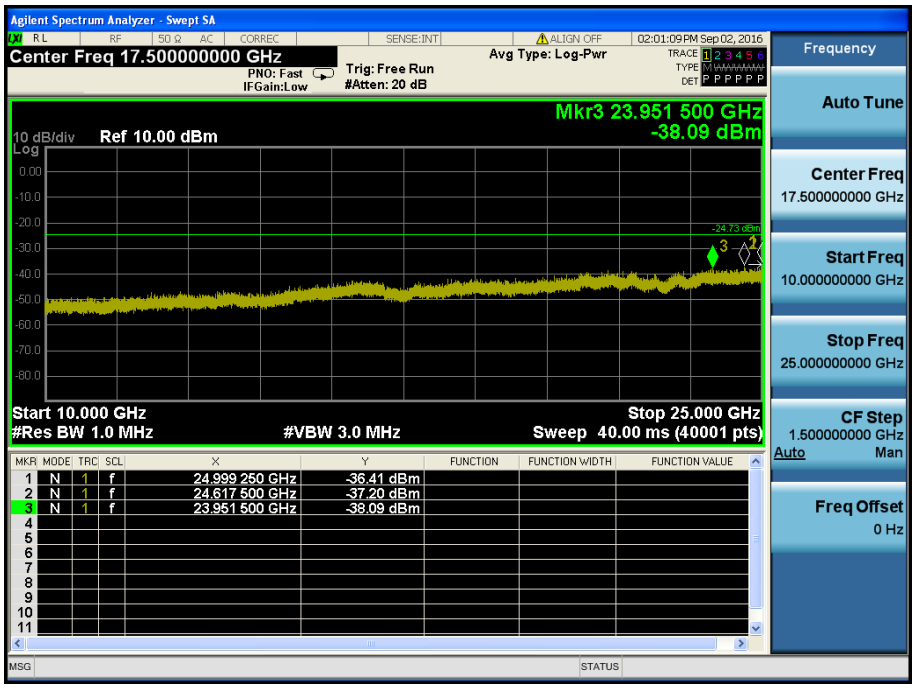
Conducted Spurious Emissions

Middle Channel & Modulation : GFSK

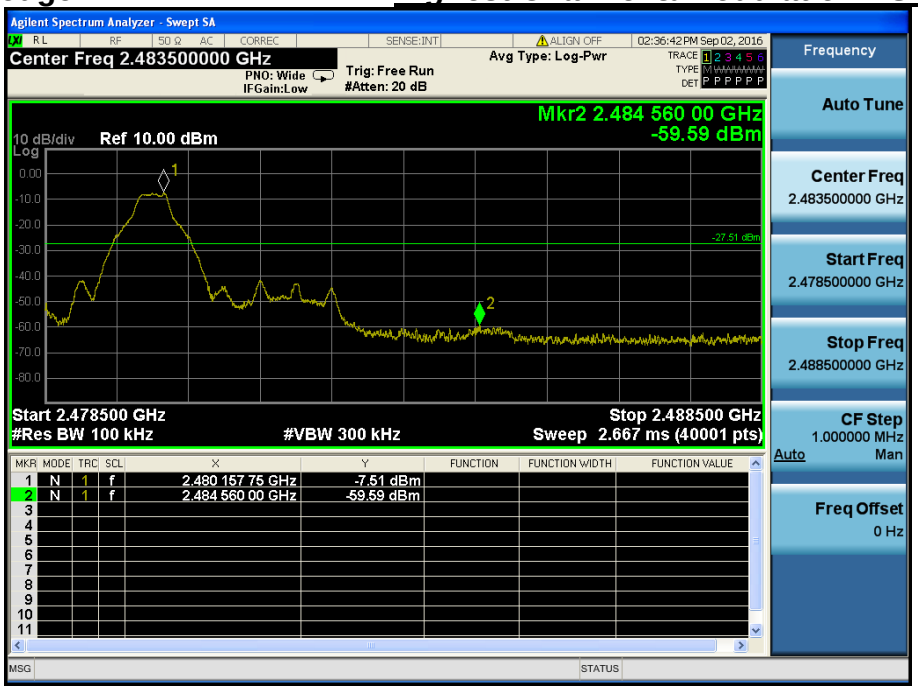


Conducted Spurious Emissions *Middle Channel & Modulation : GFSK*

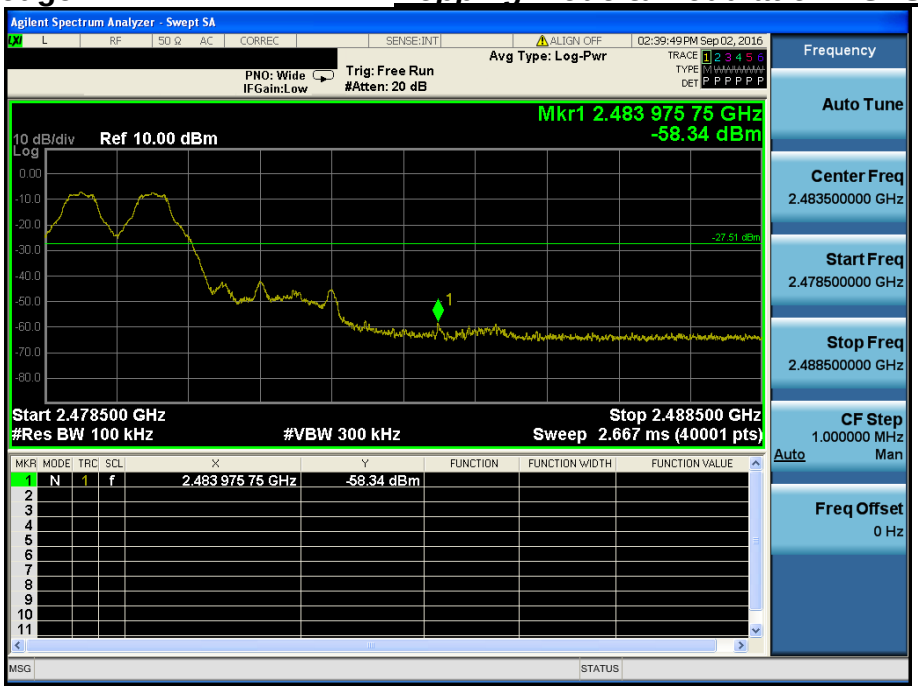




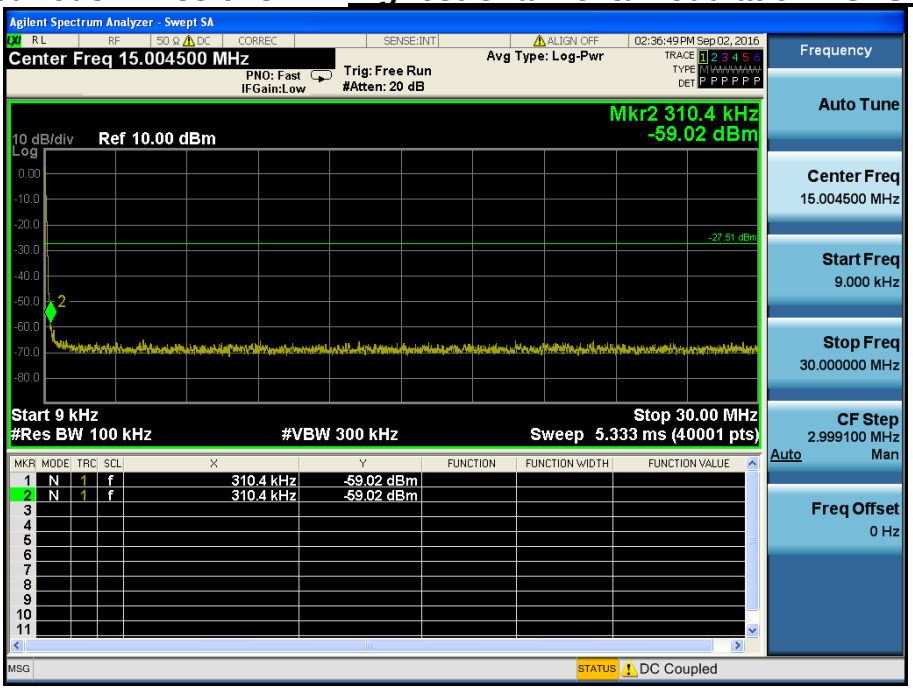
High Band-edge ***Highest Channel & Modulation : GFSK***

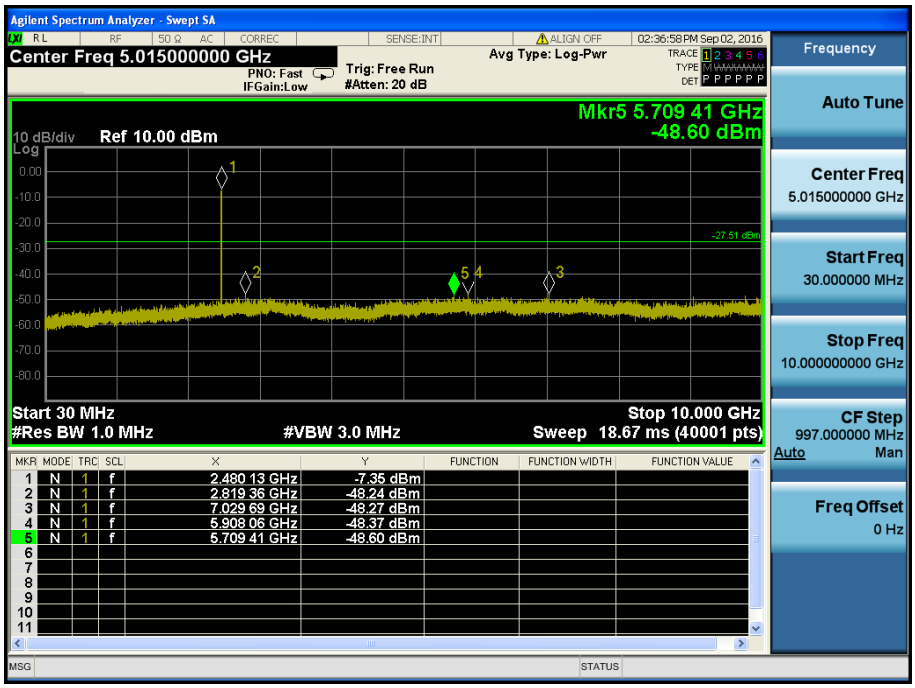


High Band-edge ***Hopping mode & Modulation : GFSK***

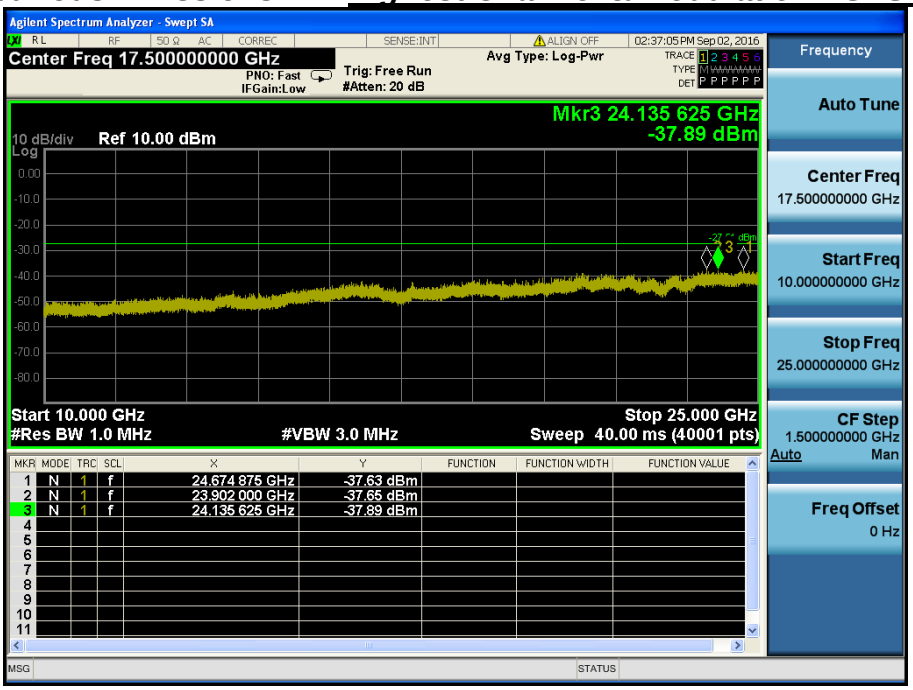


Conducted Spurious Emissions ***Highest Channel & Modulation : GFSK***





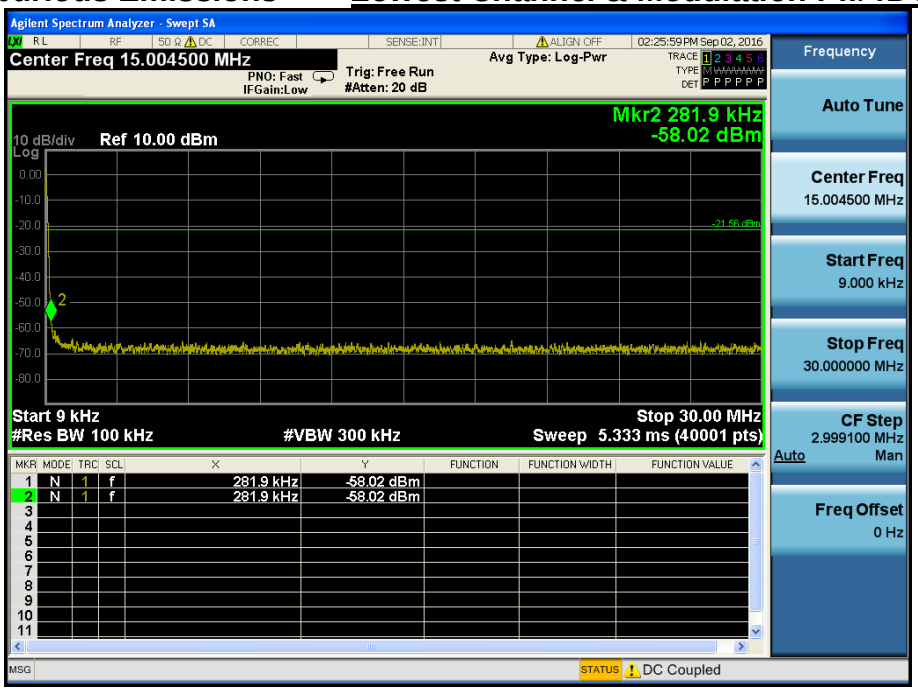
Conducted Spurious Emissions ***Highest Channel & Modulation : GFSK***



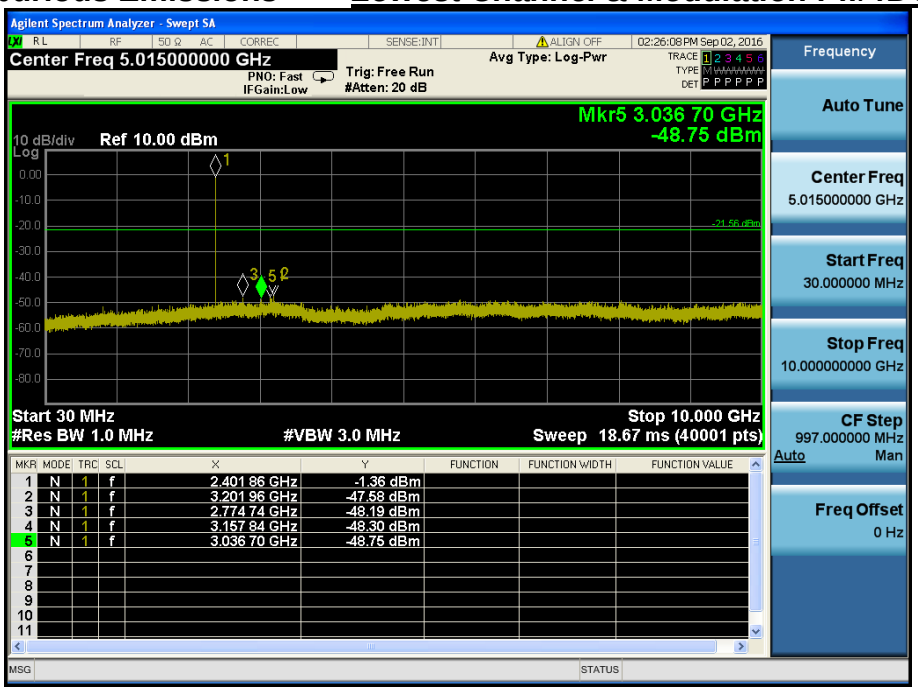
Low Band-edge ***Lowest Channel & Modulation : π/4DQPSK***

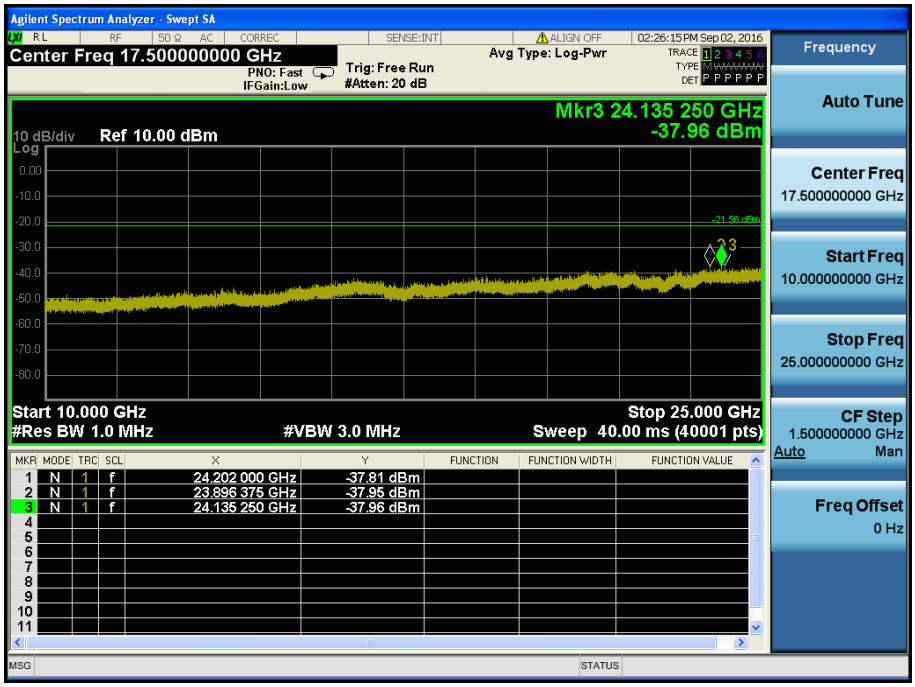
Low Band-edge ***Hopping mode & Modulation : π/4DQPSK***

Conducted Spurious Emissions *Lowest Channel & Modulation : π/4DQPSK*



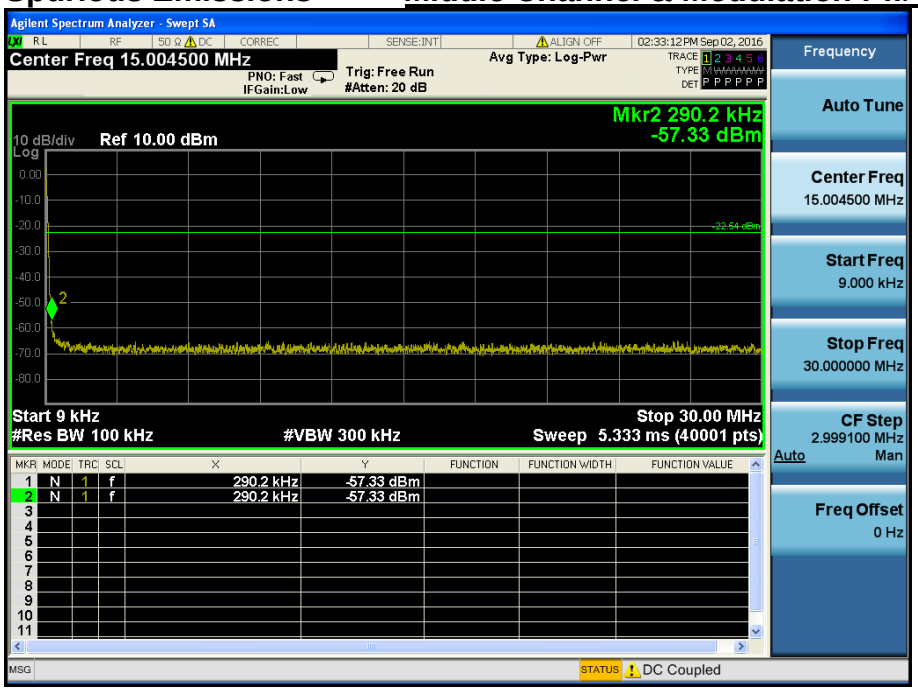
Conducted Spurious Emissions ***Lowest Channel & Modulation : π/4DQPSK***



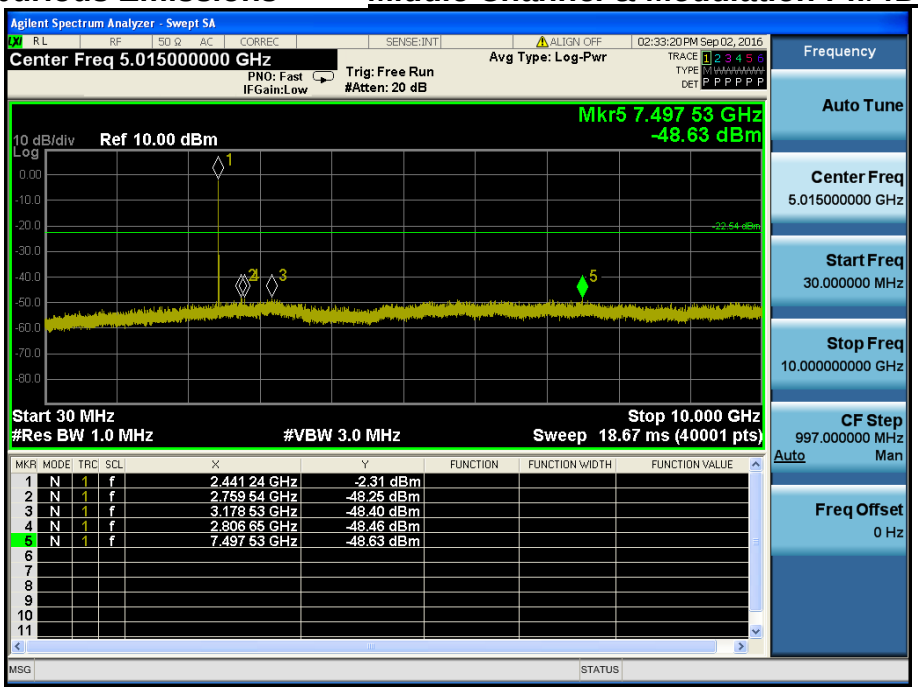


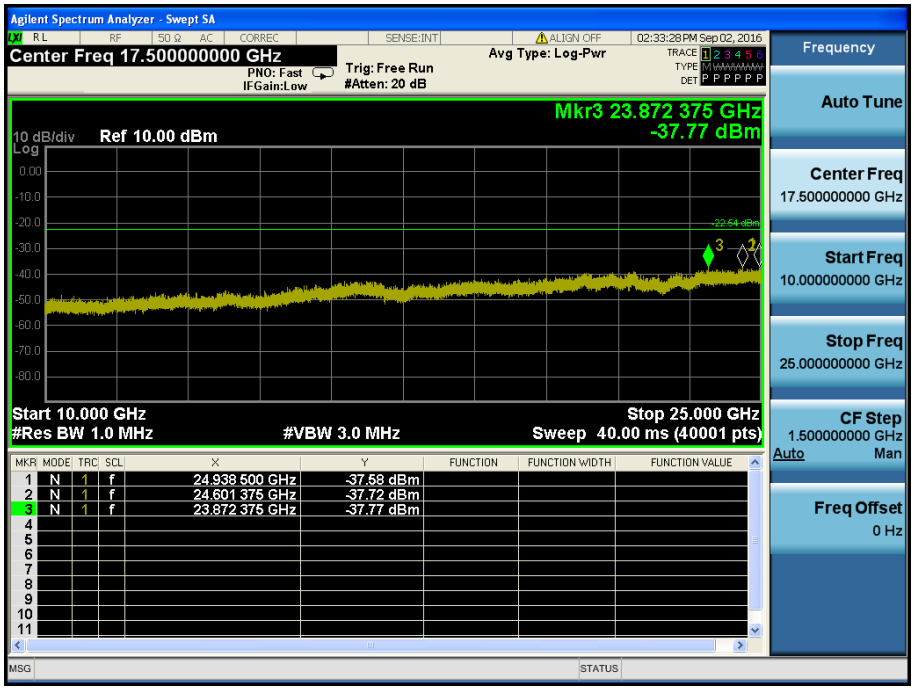
Reference for limit ***Middle Channel & Modulation : π/4DQPSK***

Conducted Spurious Emissions ***Middle Channel & Modulation : π/4DQPSK***



Conducted Spurious Emissions ***Middle Channel & Modulation : π/4DQPSK***

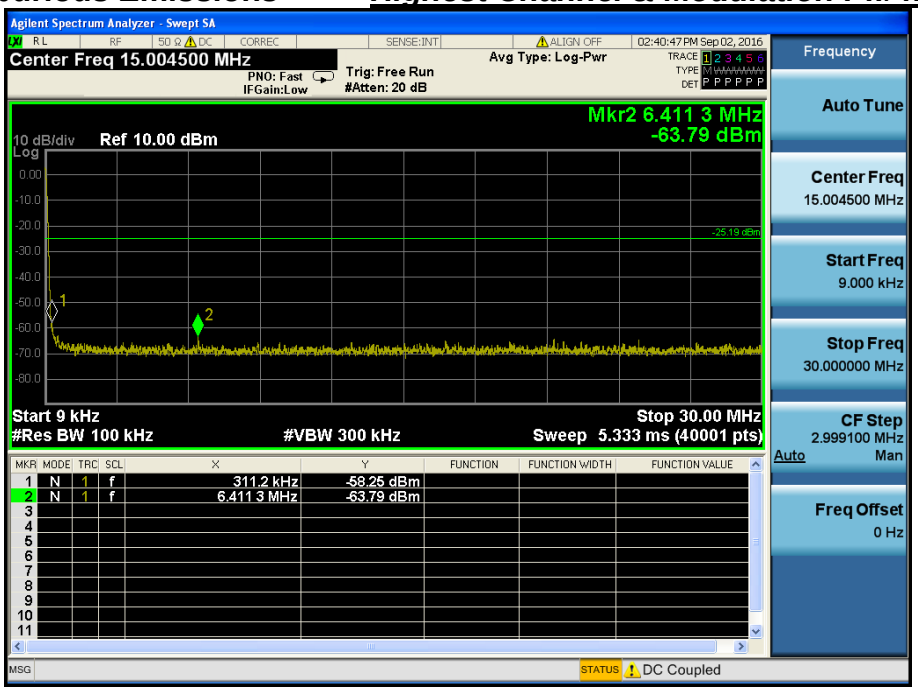


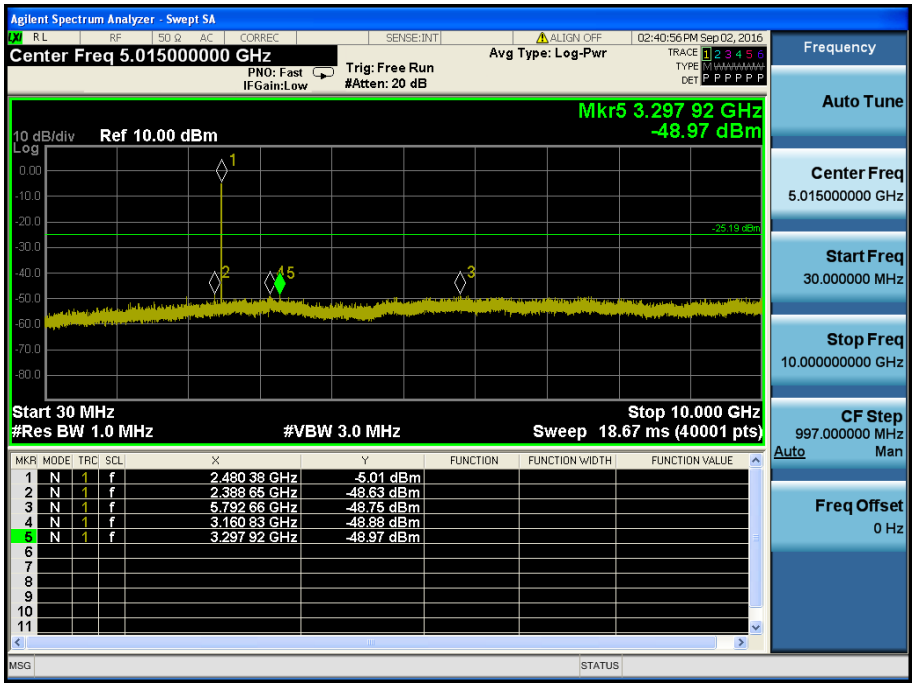


High Band-edge ***Highest Channel & Modulation : π/4DQPSK***

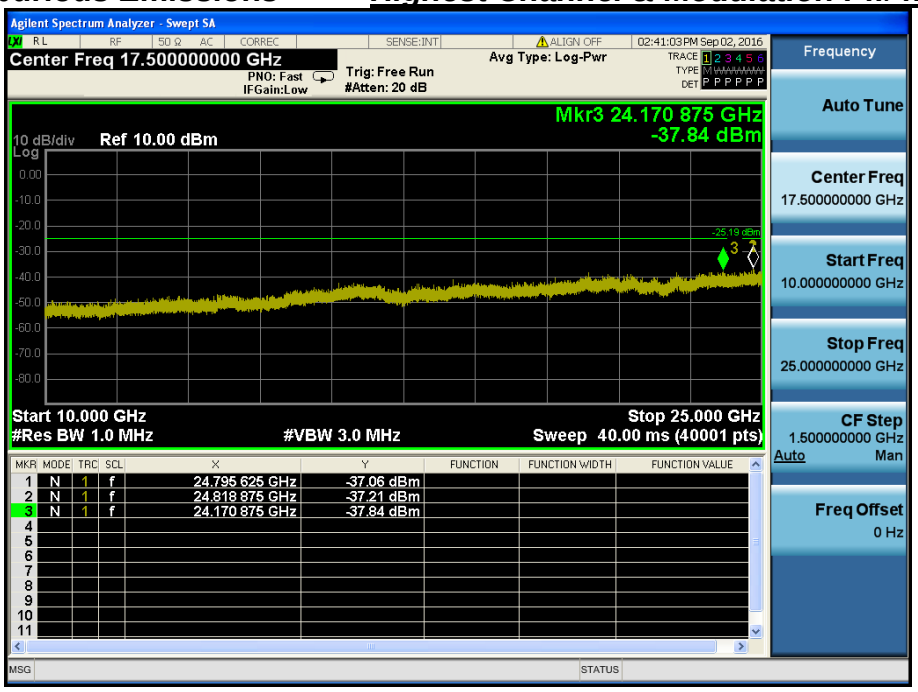
High Band-edge ***Hopping mode & Modulation : π/4DQPSK***

Conducted Spurious Emissions ***Highest Channel & Modulation : π/4DQPSK***

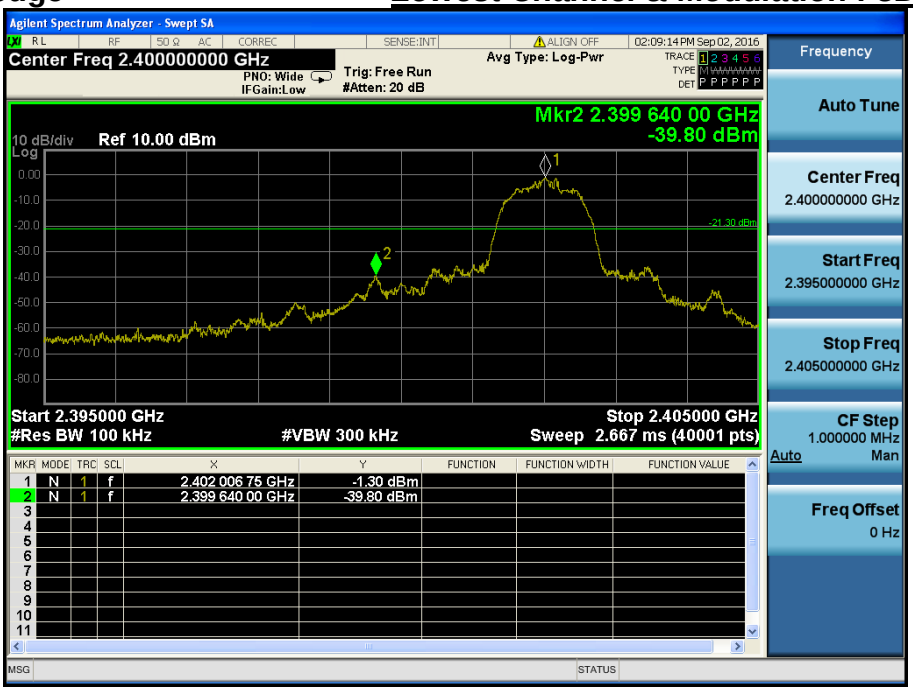




Conducted Spurious Emissions ***Highest Channel & Modulation : π/4DQPSK***

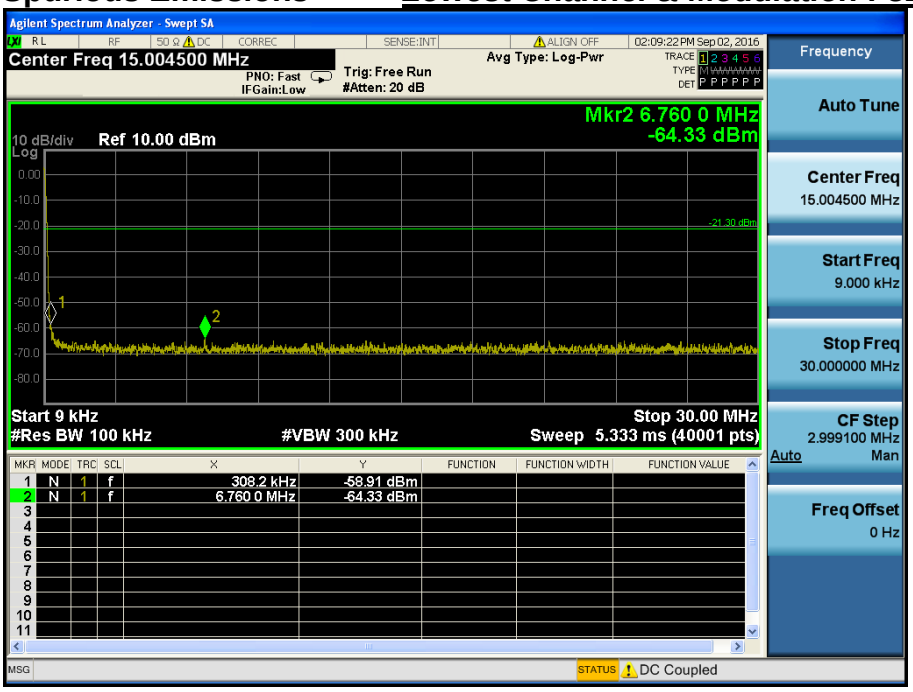


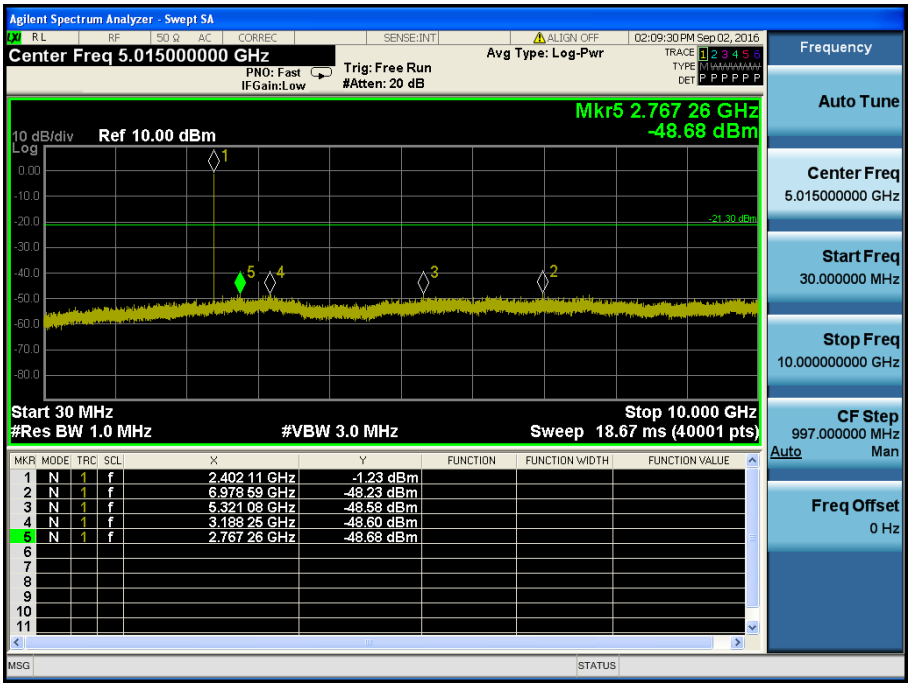
Low Band-edge ***Lowest Channel & Modulation : 8DPSK***



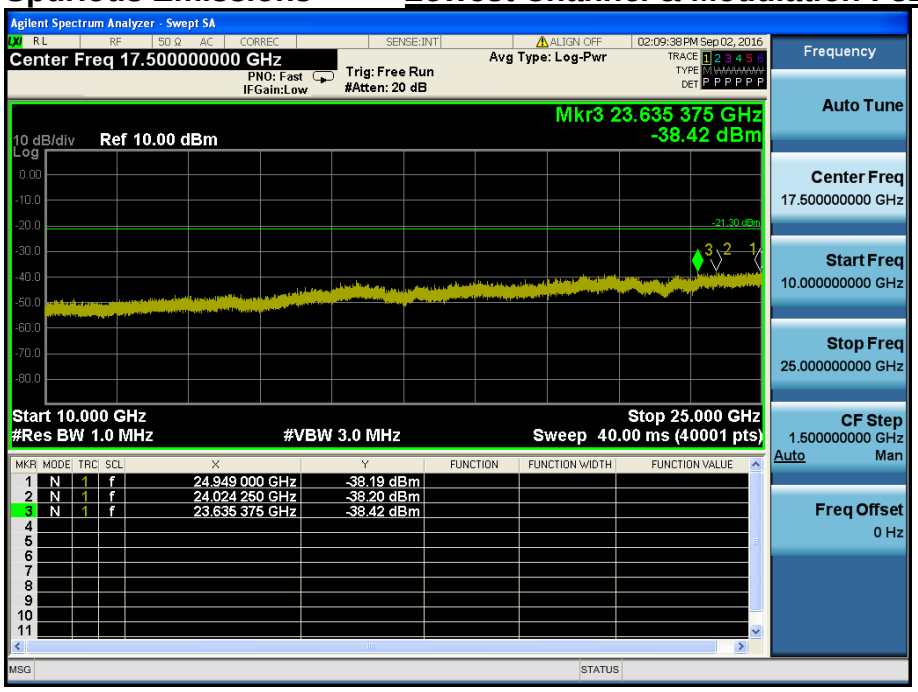
Low Band-edge ***Hopping mode & Modulation : 8DPSK***

Conducted Spurious Emissions ***Lowest Channel & Modulation : 8DPSK***



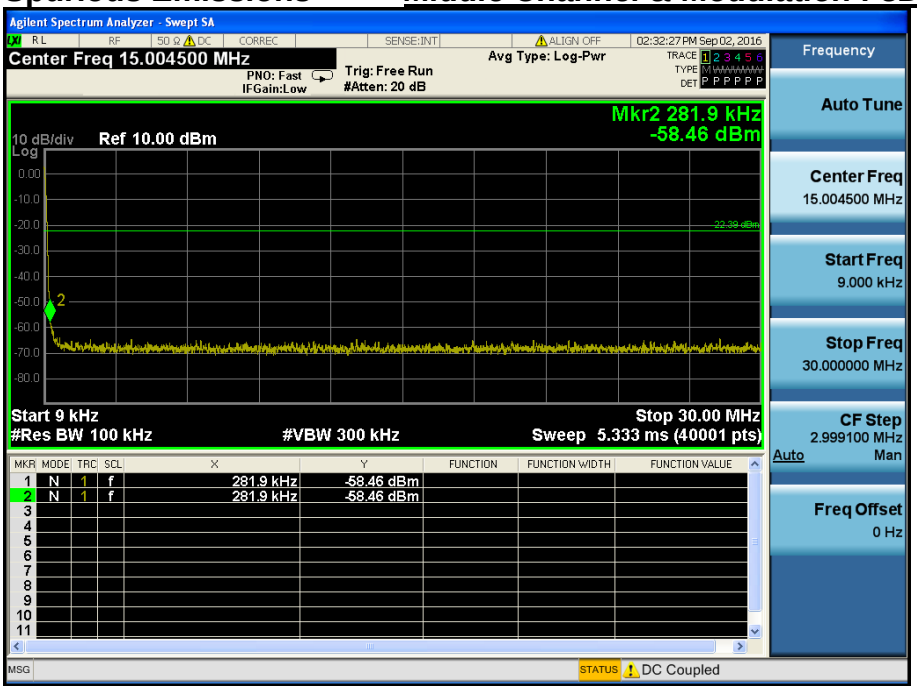


Conducted Spurious Emissions ***Lowest Channel & Modulation : 8DPSK***

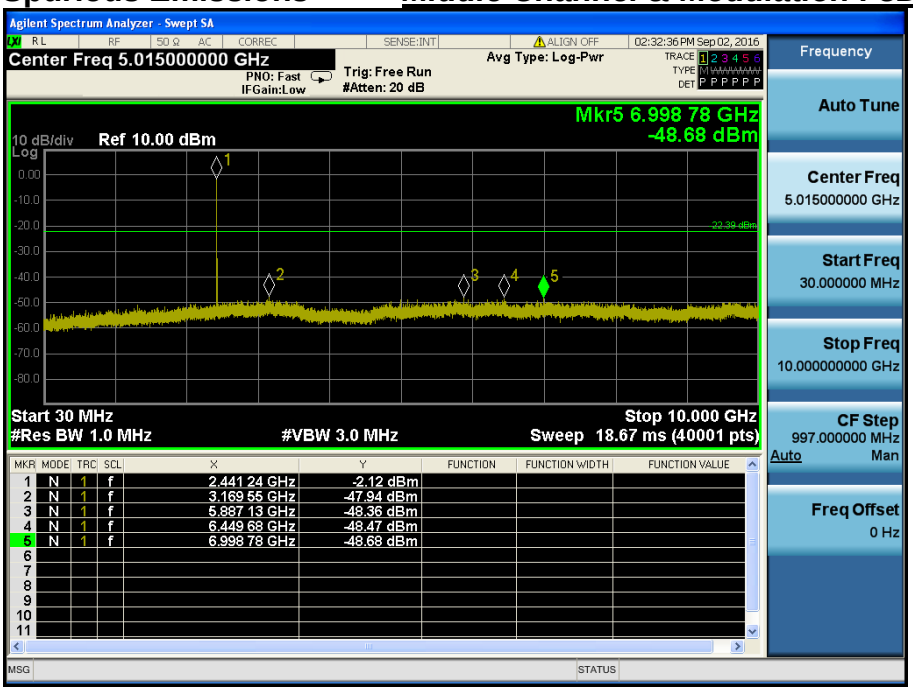


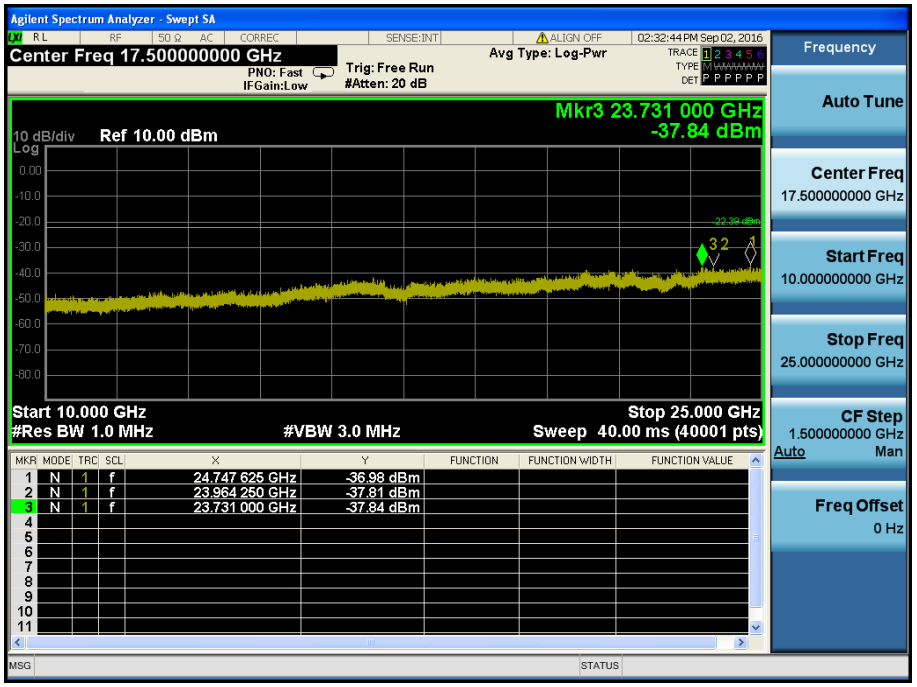
Reference for limit ***Middle Channel & Modulation : 8DPSK***

Conducted Spurious Emissions ***Middle Channel & Modulation : 8DPSK***



Conducted Spurious Emissions ***Middle Channel & Modulation : 8DPSK***

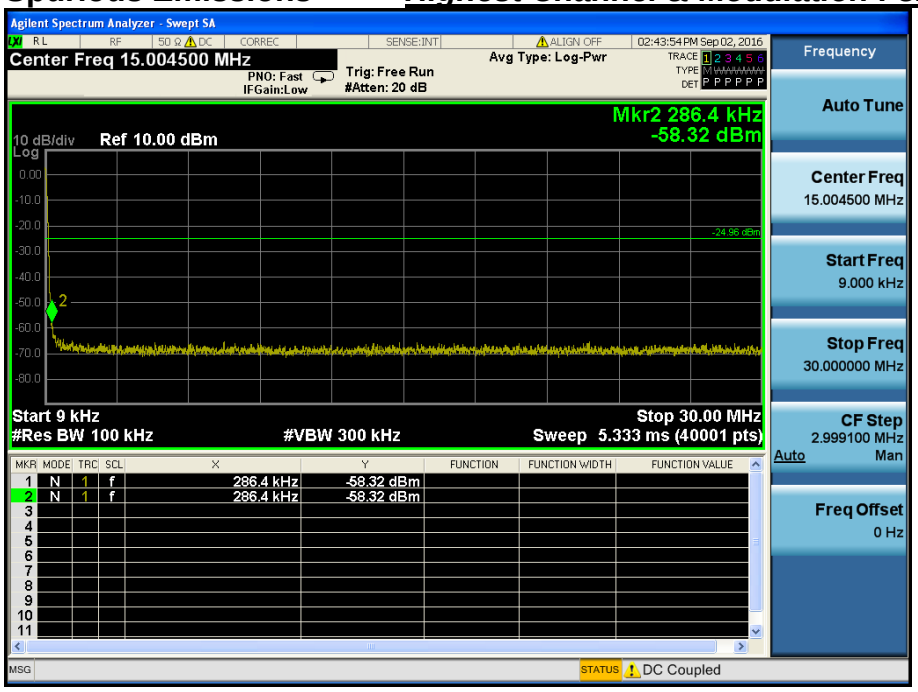


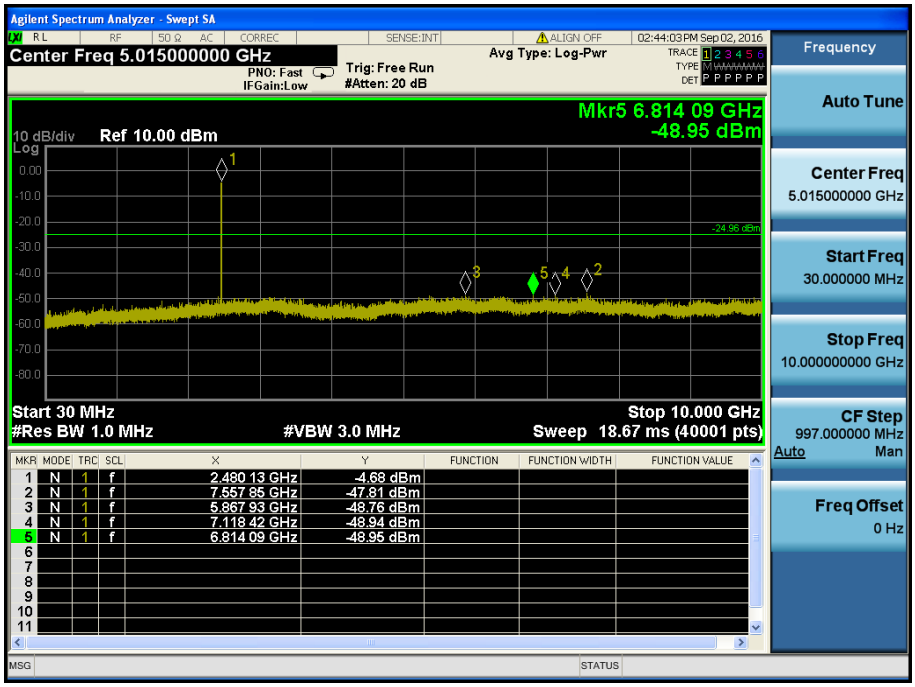


High Band-edge ***Highest Channel & Modulation : 8DPSK***

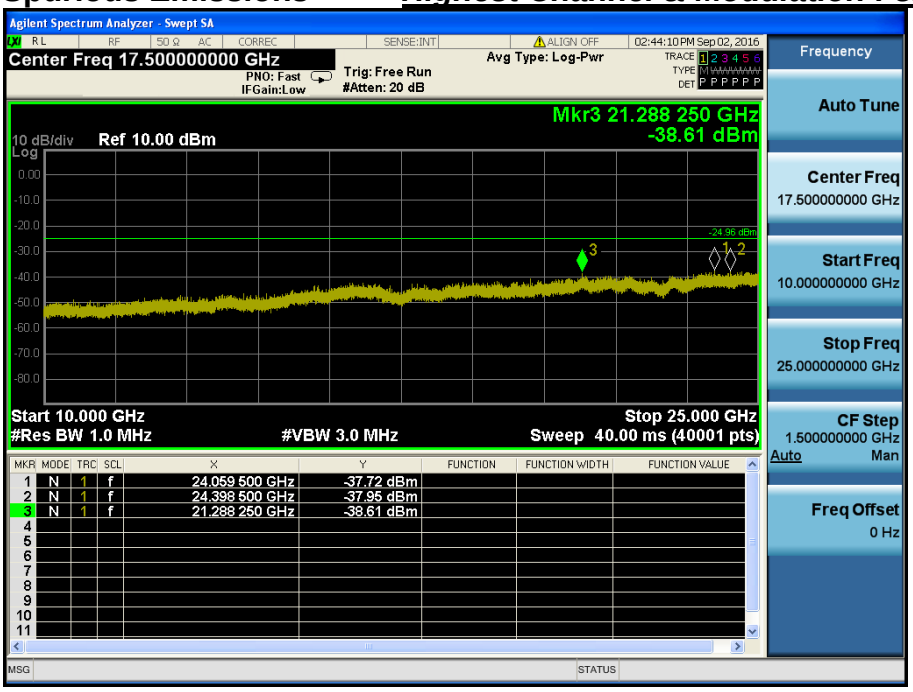
High Band-edge ***Hopping mode & Modulation : 8DPSK***

Conducted Spurious Emissions ***Highest Channel & Modulation : 8DPSK***





Conducted Spurious Emissions ***Highest Channel & Modulation : 8DPSK***



8. Transmitter AC Power Line Conducted Emission

8.1 Test Setup

Not Applicable

8.2 Limit

According to §15.207(a) for an intentional radiator that is designed to be connected to the public utility (AC) power line, the radio frequency voltage that is conducted back onto the AC power line on any frequency or frequencies, within the band 150 kHz to 30 MHz, shall not exceed the limits in the following table, as measured using a 50 uH/50 ohm line impedance stabilization network (LISN).

Compliance with the provision of this paragraph shall on the measurement of the radio frequency voltage between each power line and ground at the power terminal. The lower applies at the boundary between the frequency ranges.

Frequency Range (MHz)	Conducted Limit (dBuV)	
	Quasi-Peak	Average
0.15 ~ 0.5	66 to 56 *	56 to 46 *
0.5 ~ 5	56	46
5 ~ 30	60	50

* Decreases with the logarithm of the frequency

8.3 Test Procedures

Conducted emissions from the EUT were measured according to the ANSI C63.10.

1. The test procedure is performed in a 6.5 m × 3.5 m × 3.5 m (L × W × H) shielded room. The EUT along with its peripherals were placed on a 1.0 m (W) × 1.5 m (L) and 0.8 m in height wooden table and the EUT was adjusted to maintain a 0.4 meter space from a vertical reference plane.
2. The EUT was connected to power mains through a line impedance stabilization network (LISN) which provides 50 ohm coupling impedance for measuring instrument and the chassis ground was bounded to the horizontal ground plane of shielded room.
3. All peripherals were connected to the second LISN and the chassis ground also bounded to the horizontal ground plane of shielded room.
4. The excess power cable between the EUT and the LISN was bundled. The power cables of peripherals were unbundled. All connecting cables of EUT and peripherals were moved to find the maximum emission.

9. Antenna Requirement

Describe how the EUT complies with the requirement that either its antenna is permanently attached, or that it employs a unique antenna connector, for every antenna proposed for use with the EUT.

Conclusion: Comply

The antenna is permanently attached on PCB. (Refer to Internal photo file.)

- **Minimum Standard :**

An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device.The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator shall be considered sufficient to comply with the provisions.

10. Occupied Bandwidth (99 %)

10.1 Test Setup

Refer to the APPENDIX I.

10.2 Limit

Limit : Not Applicable

10.3 Test Procedure

The 99 % power bandwidth was measured with a calibrated spectrum analyzer.

The resolution bandwidth (RBW) shall be in the range of 1 % to 5 % of the occupied bandwidth (OBW) and video bandwidth (VBW) shall be approximately 3 × RBW.

Spectrum analyzer plots are included on the following pages.

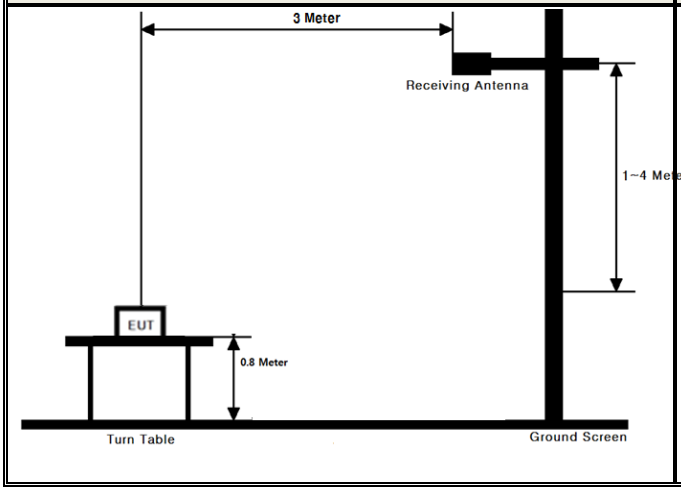
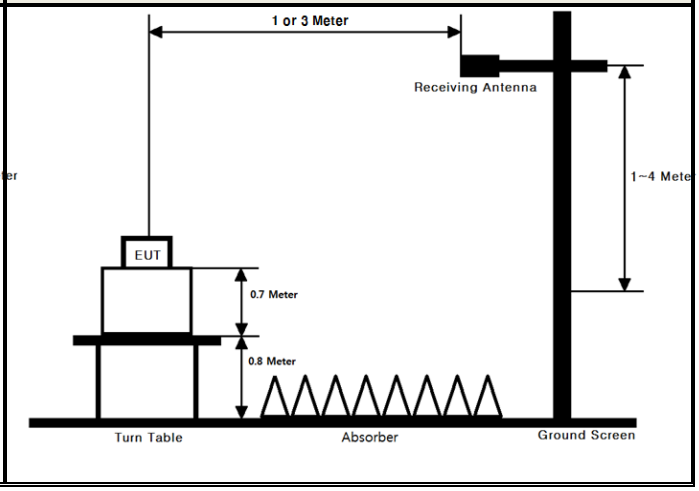
10.4 Test Results

Not Applicable

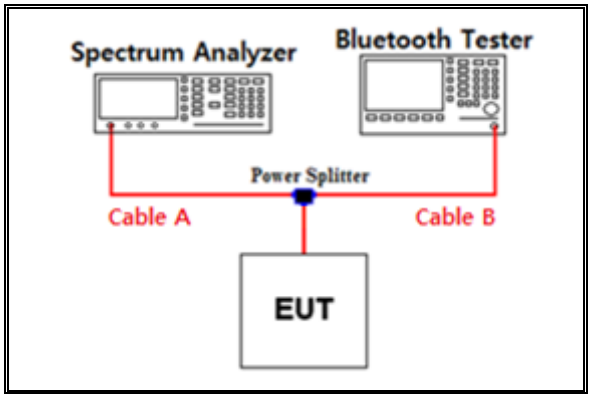
APPENDIX I

Test set up diagrams

- **Radiated Measurement**

Below 1GHz	Above 1GHz
	

- **Conducted Measurement**



Path loss information

Frequency (GHz)	Path Loss (dB)	Frequency (GHz)	Path Loss (dB)
0.03	5.95	15	8.54
1	6.21	20	9.14
2402 & 2440 & 2480	6.71	25	9.72
5	6.91	-	-
10	8.08	-	-

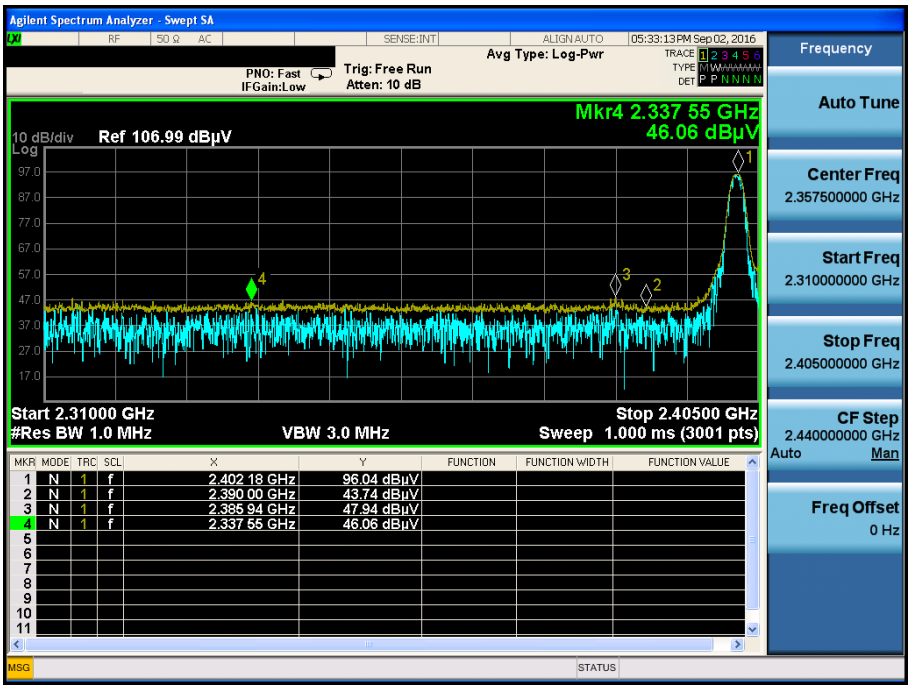
Note 1 : The path loss from EUT to Spectrum analyzer were measured and used for test.

Path loss (S/A's Correction factor) = Cable A + Power splitter

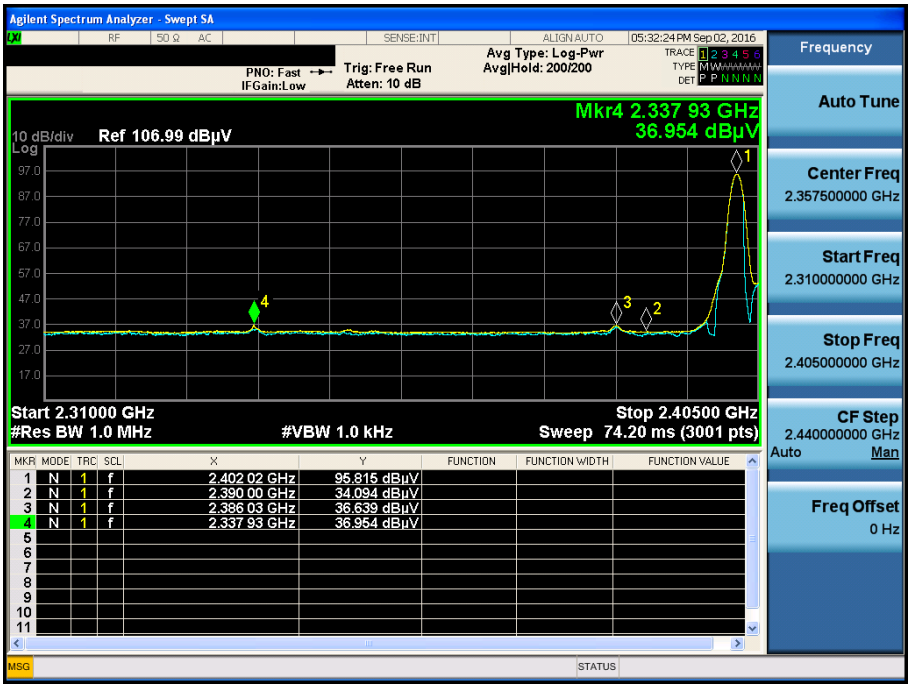
APPENDIX II

Unwanted Emissions (Radiated) Test Plot

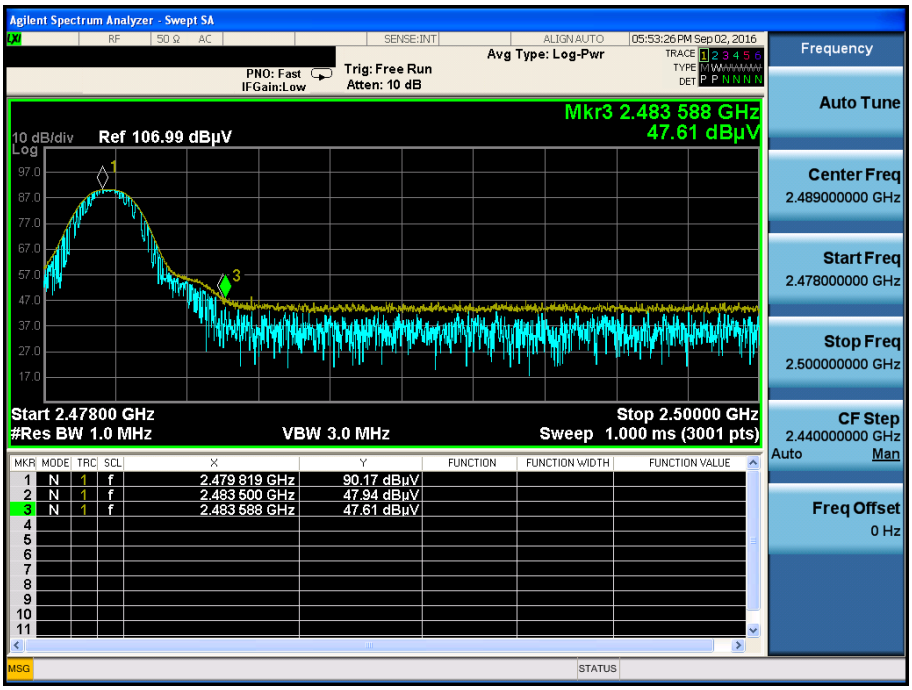
GFSK & Lowest & X & Ver — Detector Mode : PK



GFSK & Lowest & X & Ver — Detector Mode : AV

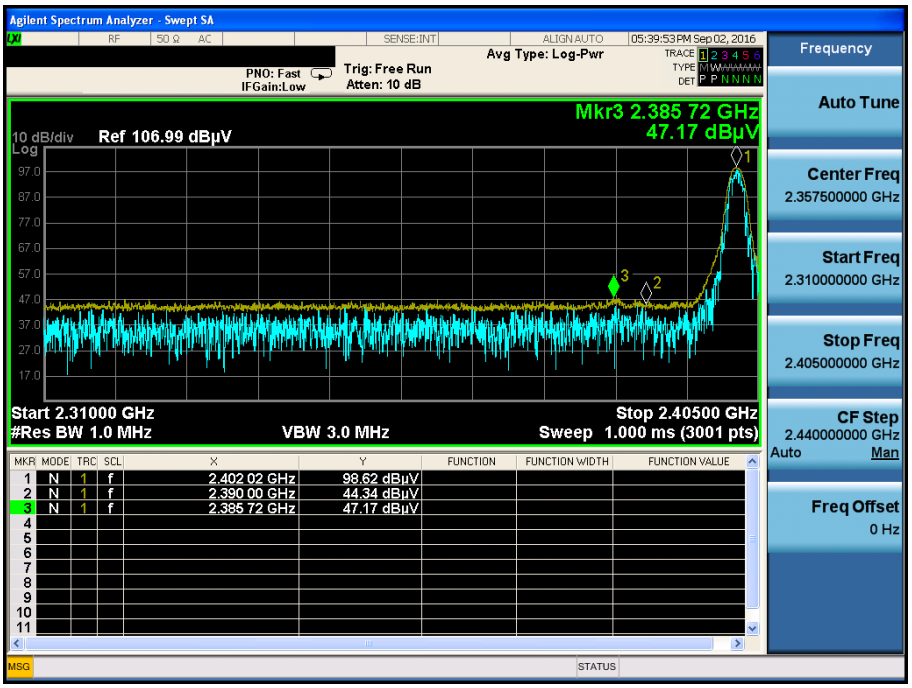


GFSK & Highest & X & Ver — Detector Mode : PK

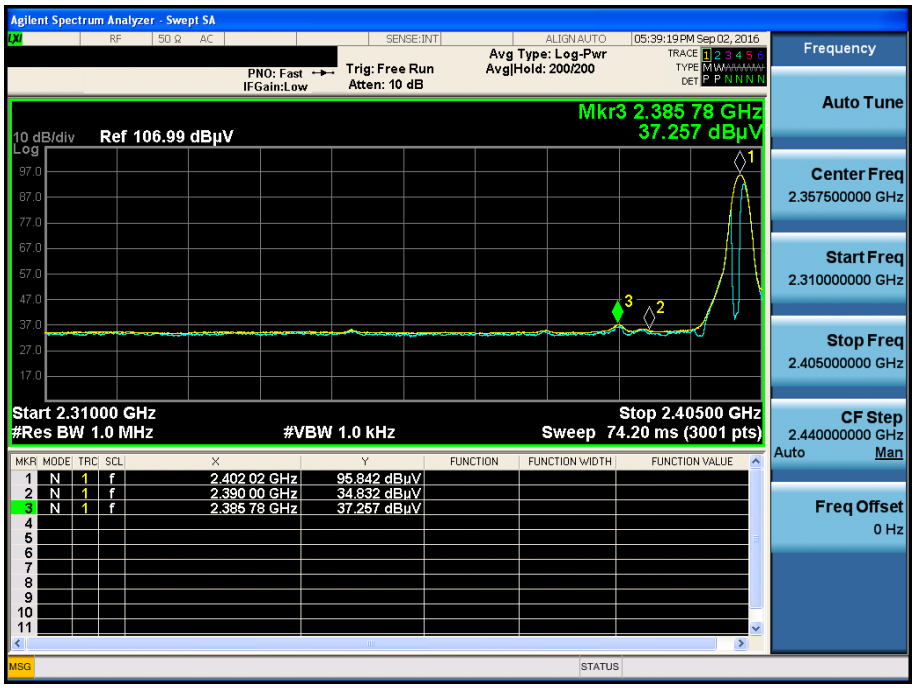


GFSK & Highest & X & Ver — Detector Mode : AV

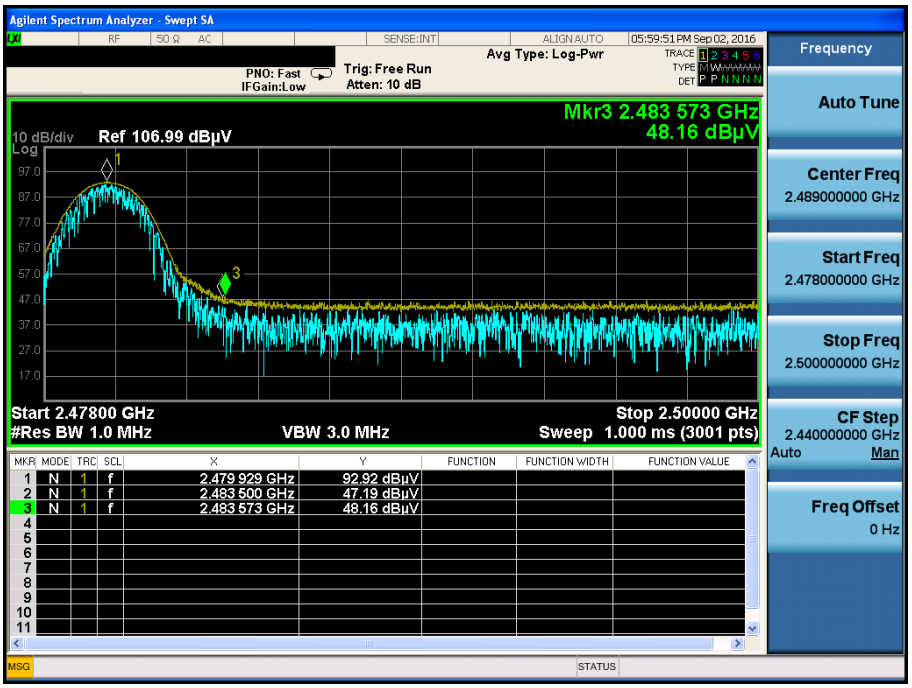
π/4DQPSK & Lowest & X & Ver — Detector Mode : PK



π/4DQPSK & Lowest & X & Ver — Detector Mode : AV

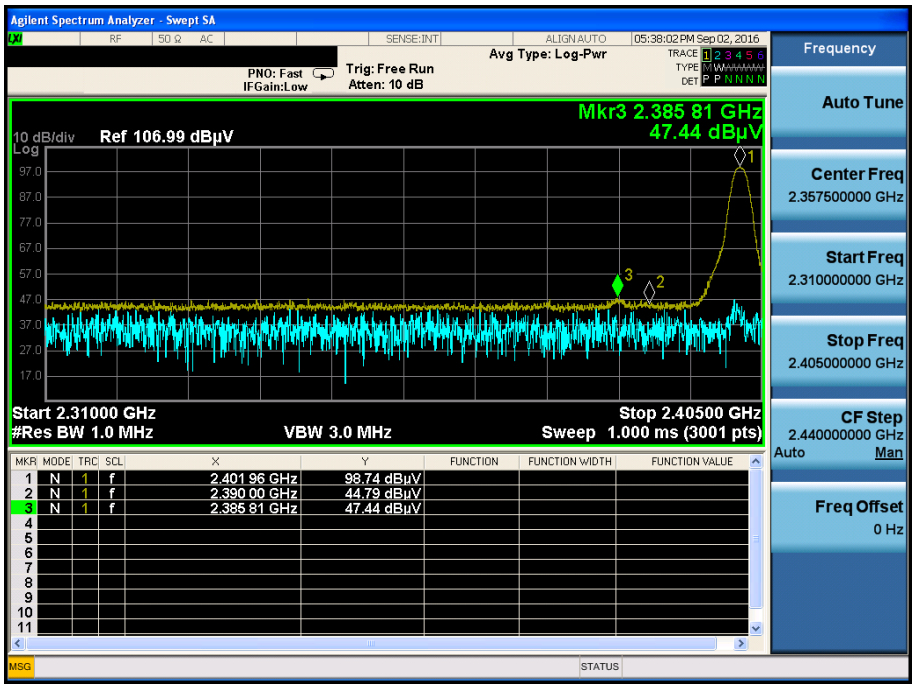


π/4DQPSK & Highest & X & Ver — Detector Mode : PK

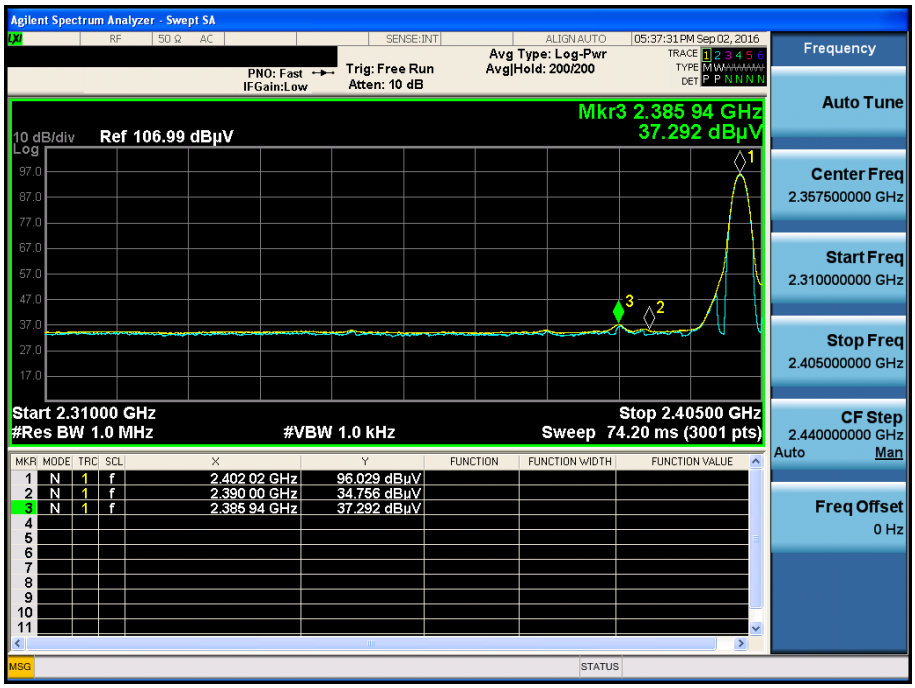


π/4DQPSK & Highest & X & Ver — Detector Mode : AV

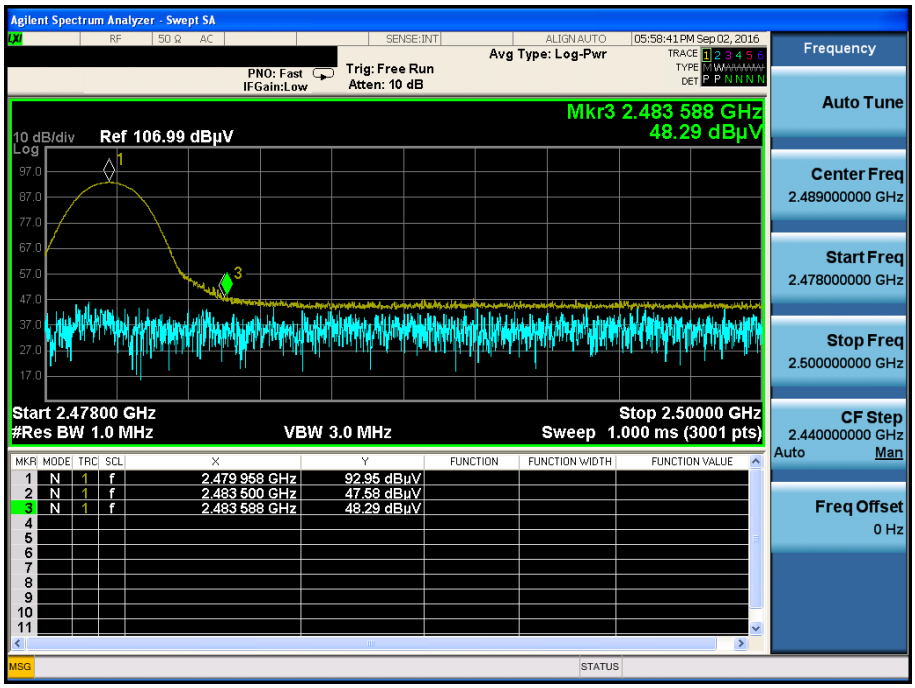
8DPSK & Lowest & X & Ver — Detector Mode : PK



8DPSK & Lowest & X & Ver — Detector Mode : AV

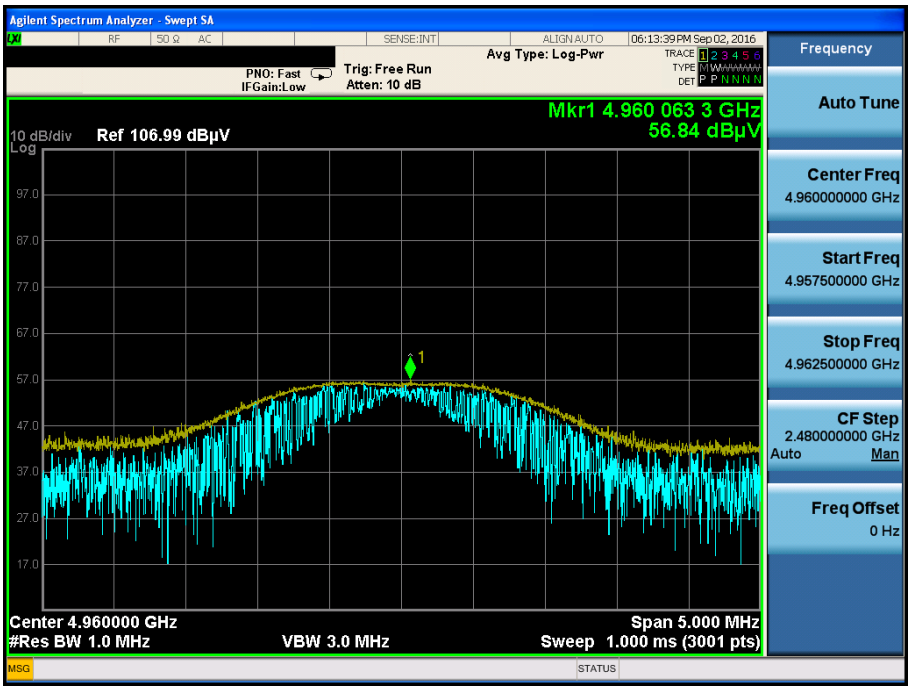


8DPSK & Highest & X & Ver — Detector Mode : PK

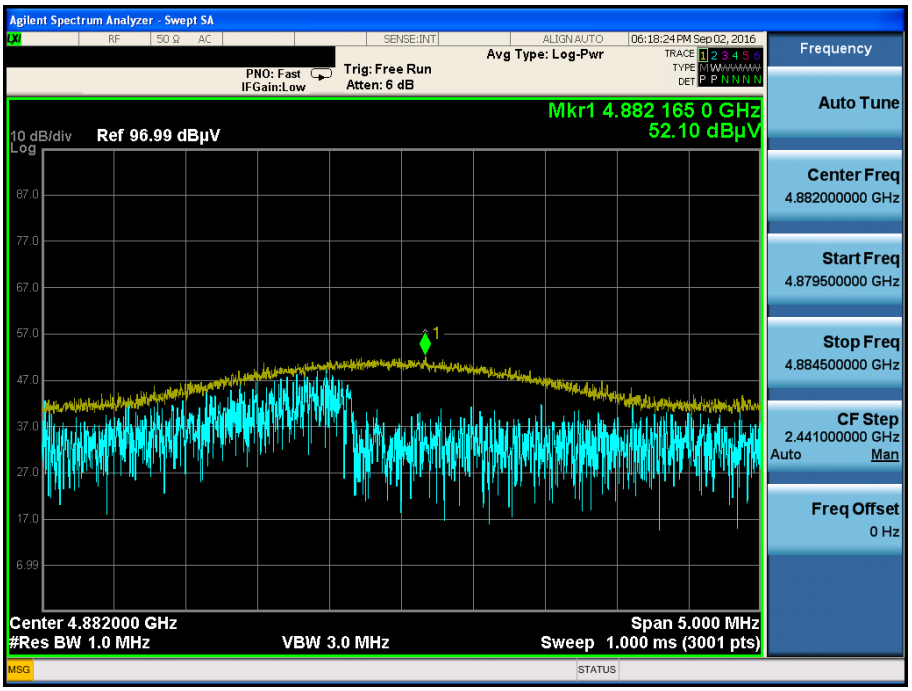


8DPSK & Highest & X & Ver — Detector Mode : AV

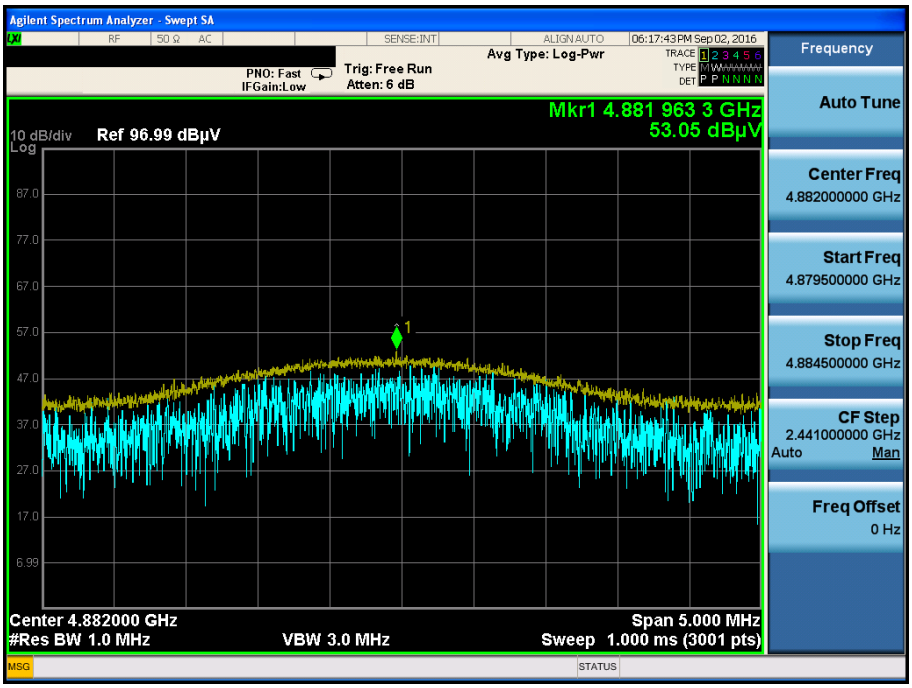
GFSK & Highest & X & Ver — Detector Mode : PK



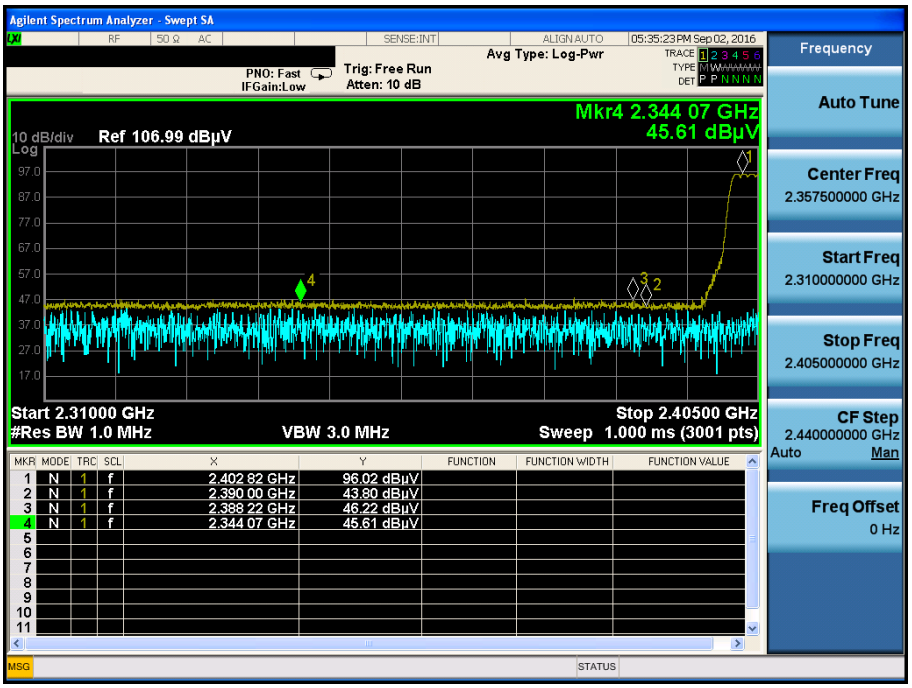
π/4DQPSK & Middle & X & Ver — Detector Mode : PK



8DPSK & Middle & X & Ver **Detector Mode : PK**

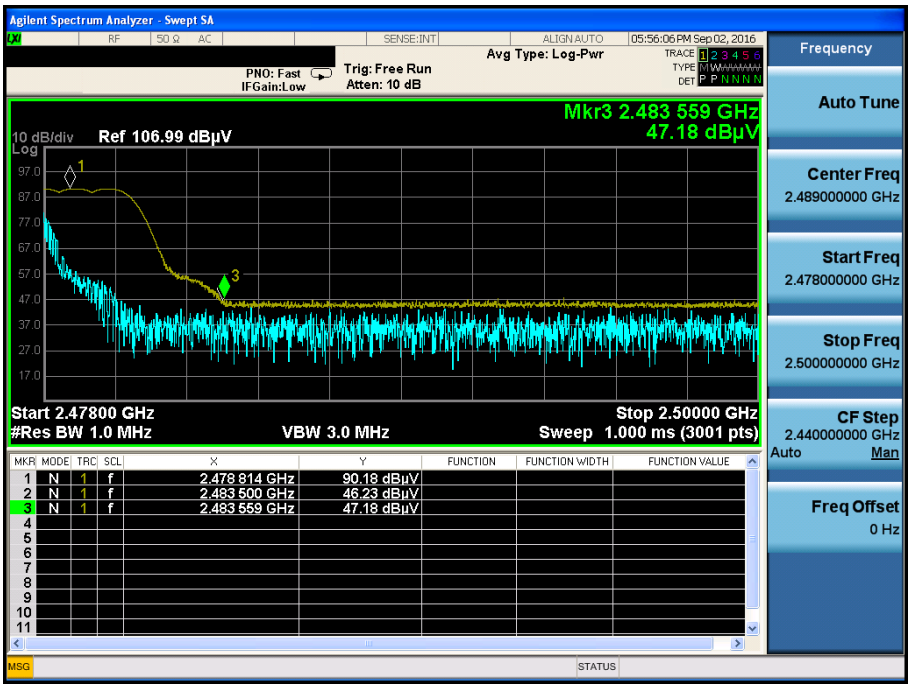


GFSK & Hopping mode & X & Ver — Detector Mode : PK

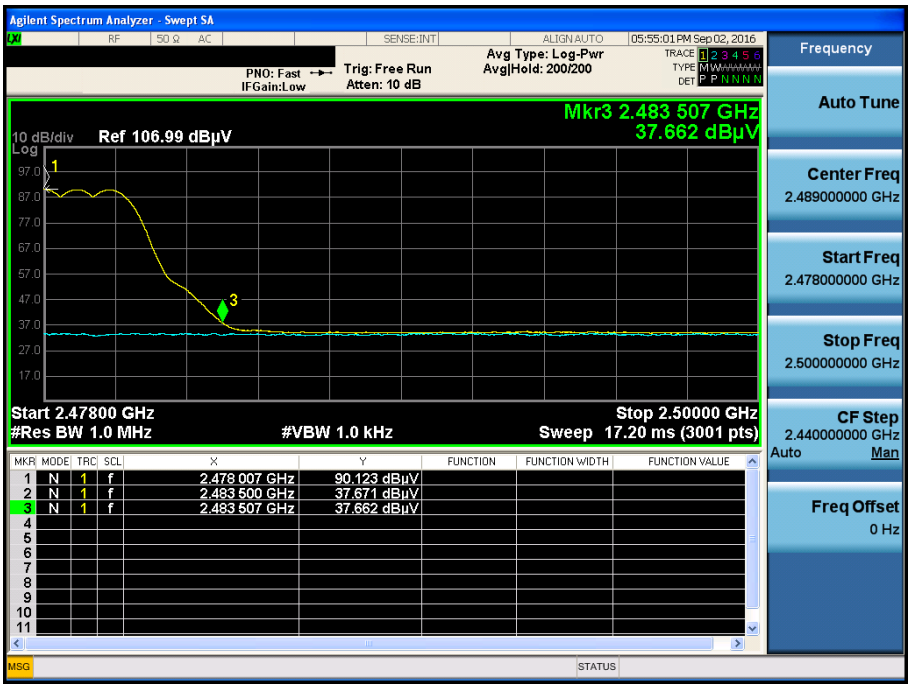


GFSK & Hopping mode & X & Ver — Detector Mode : AV

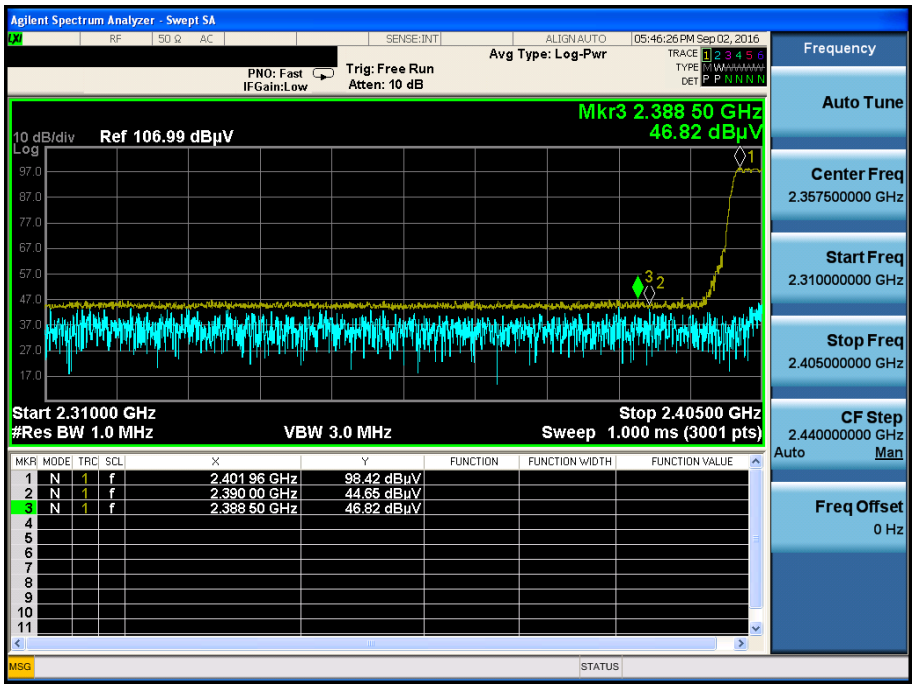
GFSK & Hopping mode & X & Ver — Detector Mode : PK



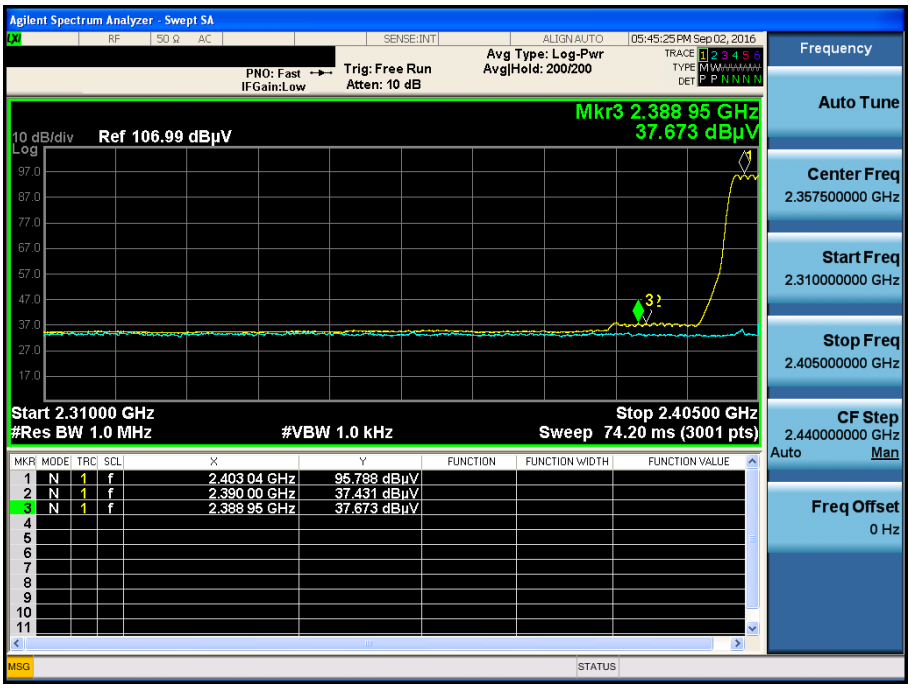
GFSK & Hopping mode & X & Ver — Detector Mode : AV



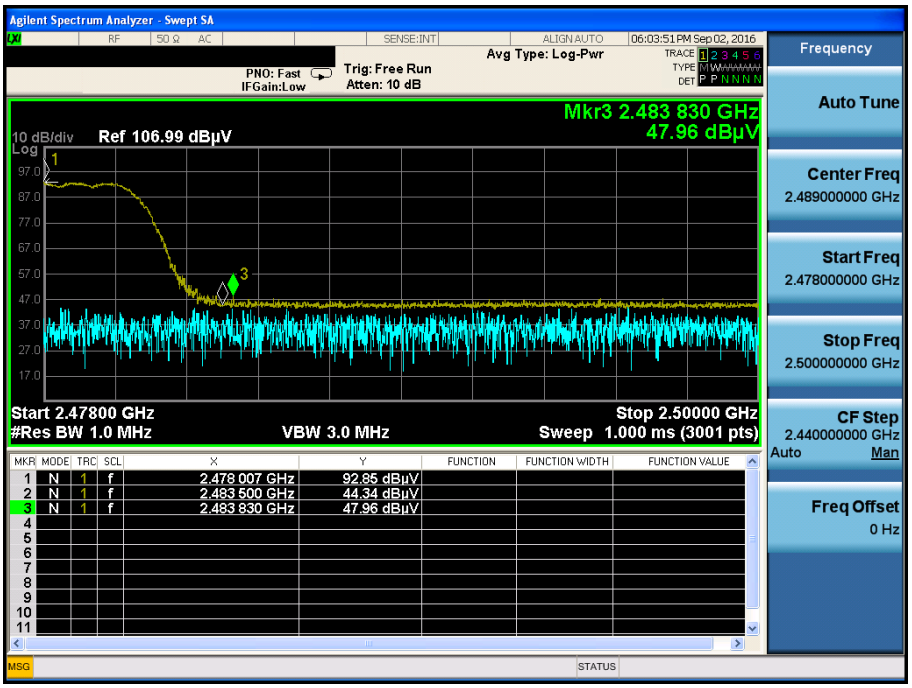
π/4DQPSK & Hopping mode & X & Ver — Detector Mode : PK



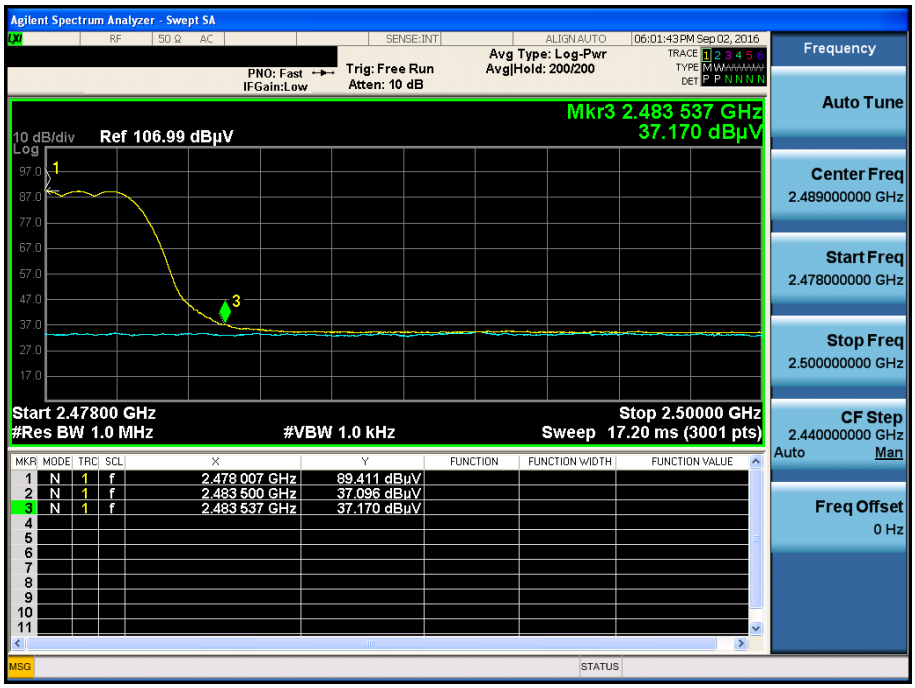
π/4DQPSK & Hopping mode & X & Ver — Detector Mode : AV



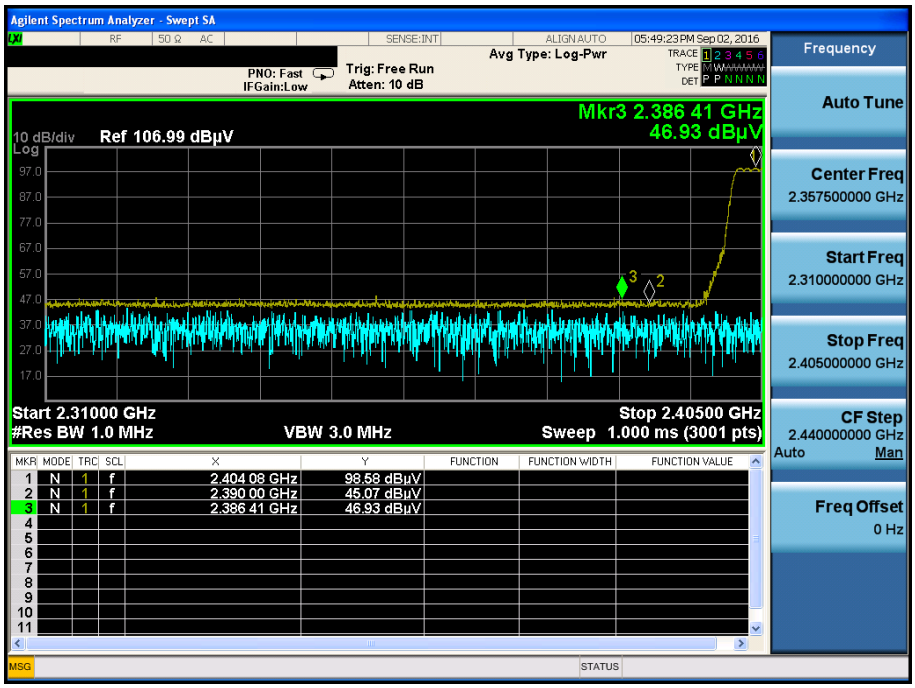
π/4DQPSK & Hopping mode & X & Ver — Detector Mode : PK



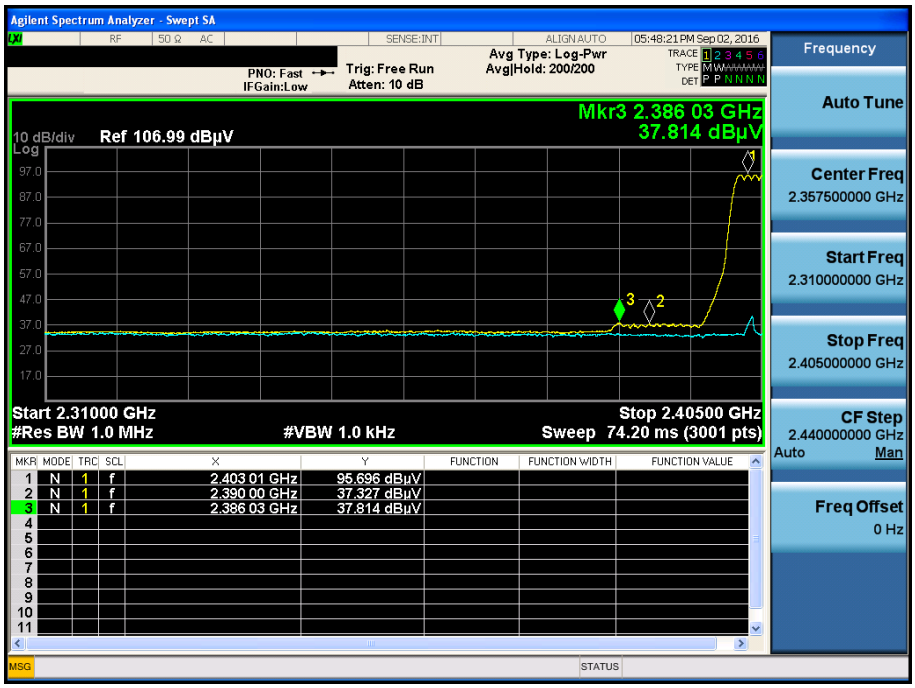
π/4DQPSK & Hopping mode & X & Ver — Detector Mode : AV



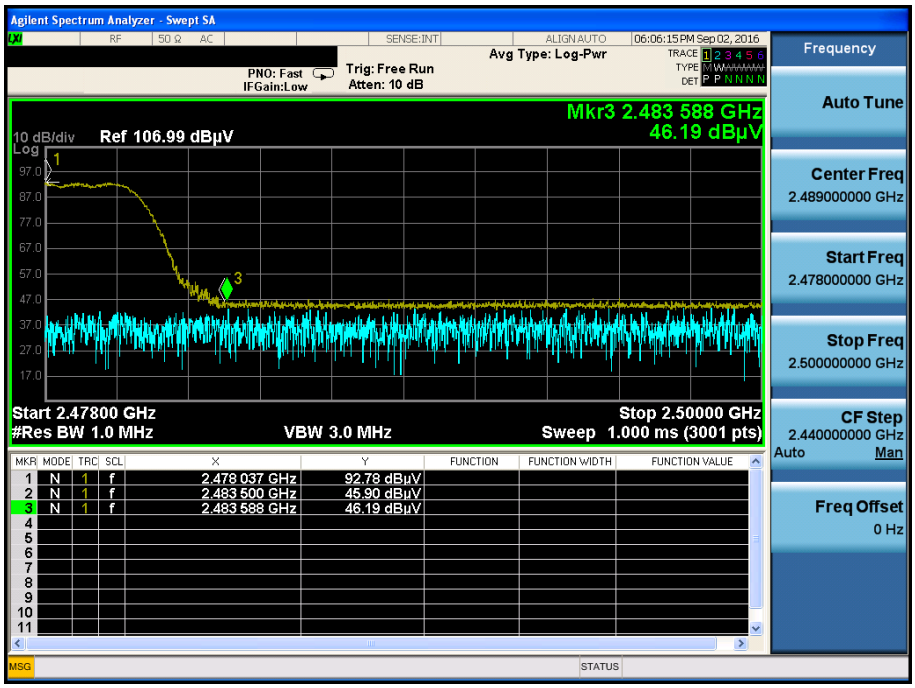
8DPSK & Hopping mode & X & Ver — Detector Mode : PK



8DPSK & Hopping mode & X & Ver — Detector Mode : AV



8DPSK & Hopping mode & X & Ver — Detector Mode : PK



8DPSK & Hopping mode & X & Ver — Detector Mode : AV