

FCC ID: YPS-LRC201249R
Model:NINR233(Receiver portion)
Technical Description of Receiver

The receiver operates at 49.860MHz, and controls by the corresponding transmitter to move forward . backward, left and right.

The circuit of the receiver comprises of,

1. Super-regenerative circuit
Consisting: L1,C1,C2,Q1,T1,C3,C5,R1,EC1,L1,R2,C4
2. Decoder
Consisting: RX10
3. Control Drive Motor Circuit
Consisting: RX318M,CJ7731,C14,104,C13
5. A Power Supply
Consisting: 1*9V Ni-MH battery pack
6. Regulator
Consisting: Z1,EC1,C12,R10
7. Clock Frequency
Consisting: Y1

The receiver uses an internal, integral and non-detachable antenna.