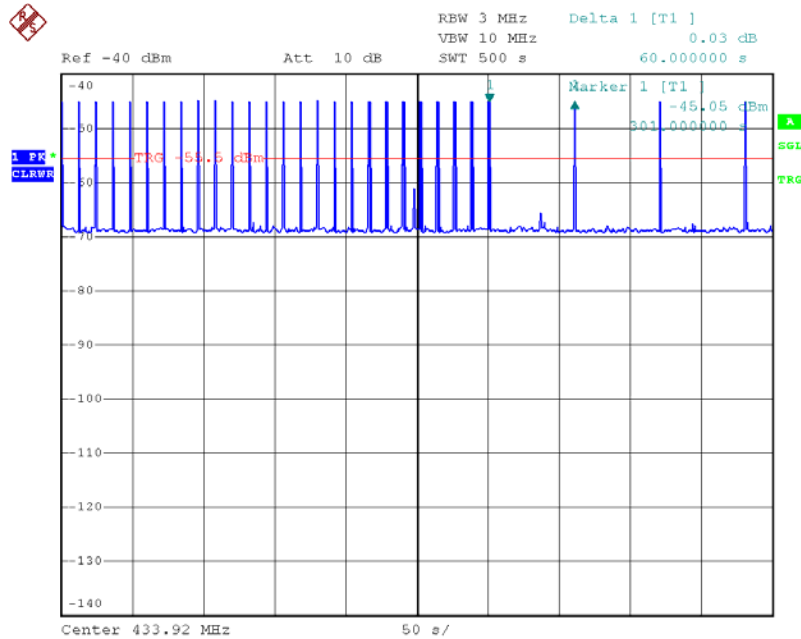


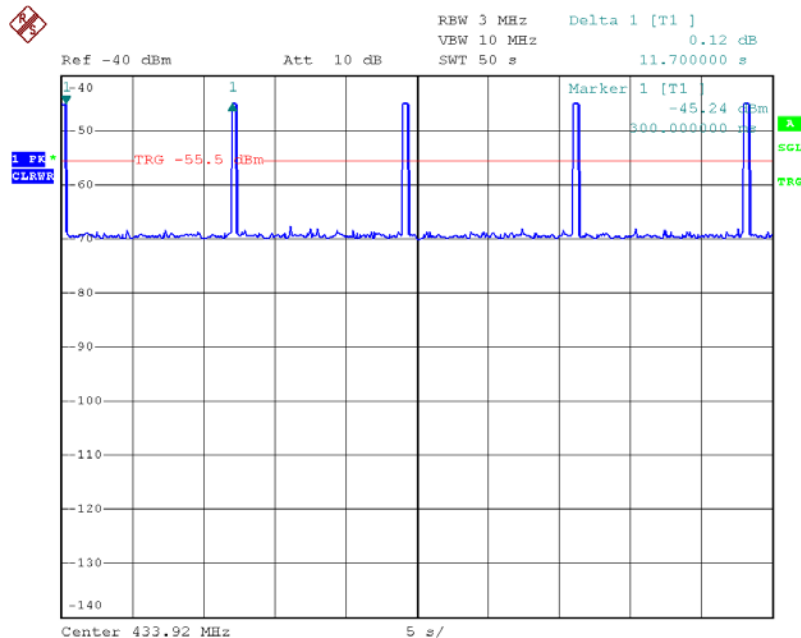
INTERTEK TESTING SERVICES

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Date: 7.DEC.2011 14:59:03



Date: 7.DEC.2011 14:50:08

Timing Plot – Pursuant to FCC Part 15 Section 15. 231(e) Requirement (Pass/Fail)