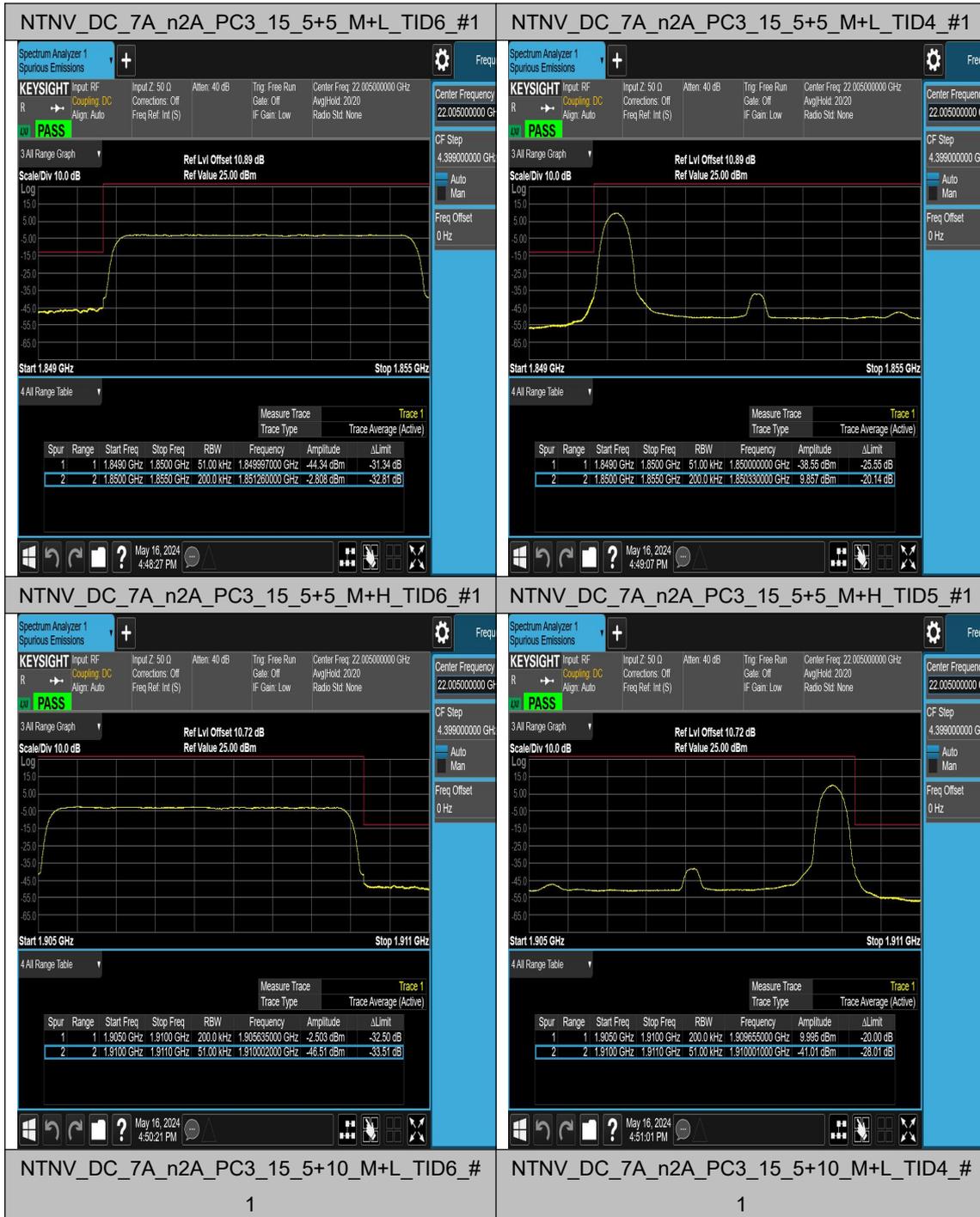


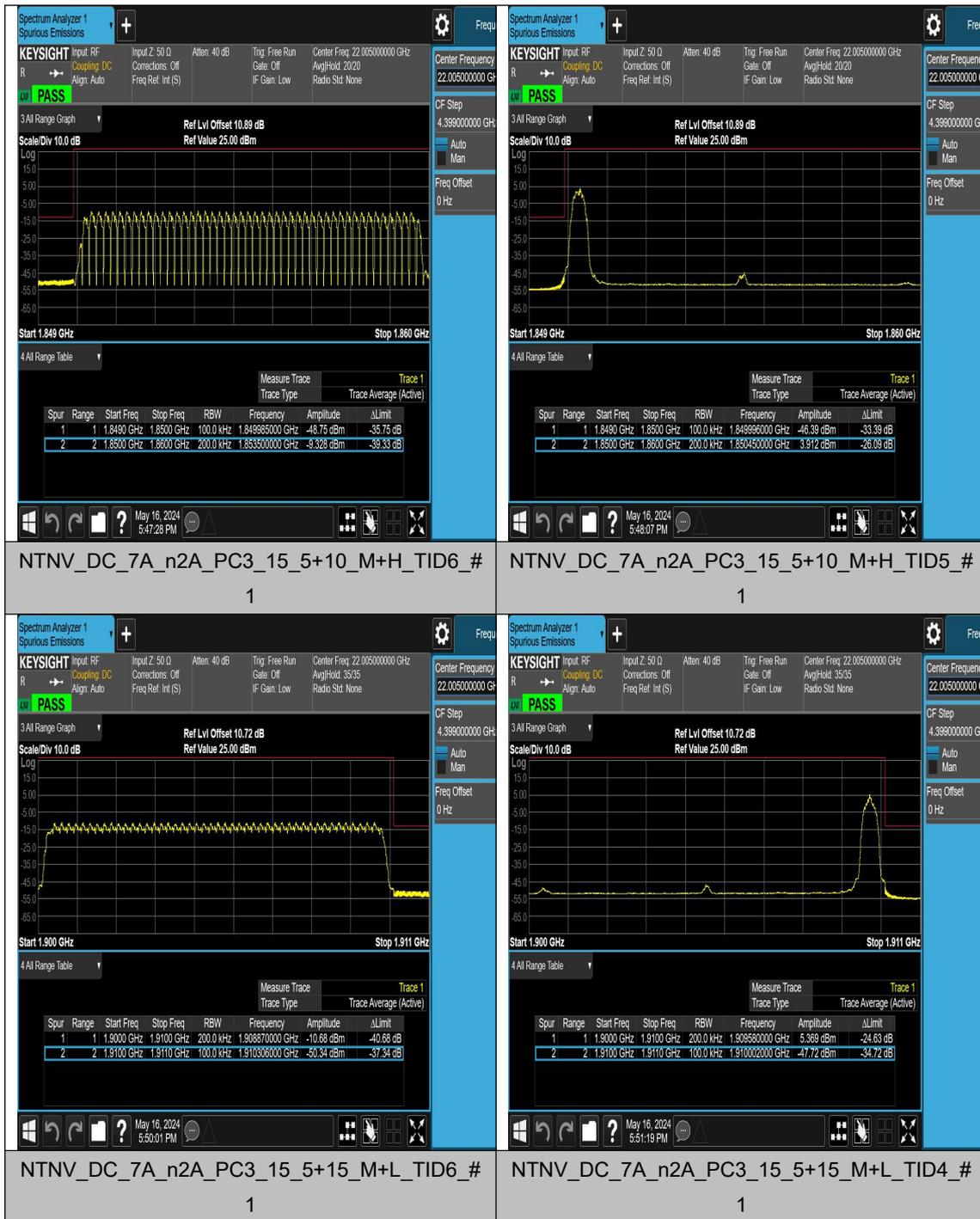
Appendix D: Band Edge for NSA

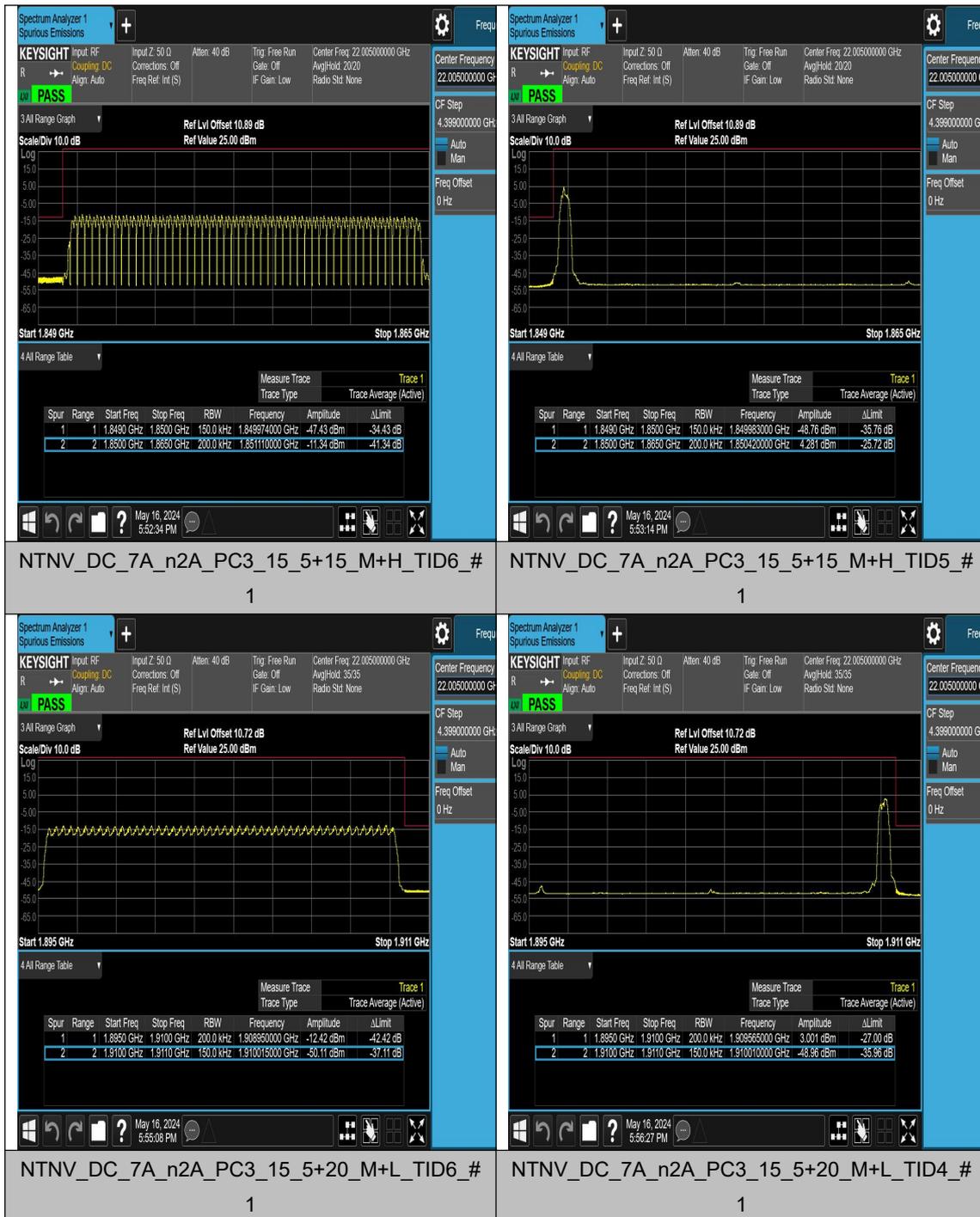
Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	PASS

Test Graphs









Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS

DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Outer_Full	12000	20000	-13	PASS

DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+5	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Outer_Full	12000	20000	-13	PASS

DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+10	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS

DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS

DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+15	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Outer_Full	12000	20000	-13	PASS

DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+L	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Left	12000	20000	-13	PASS

DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+M	Edge_1RB_Right	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-QPSK	M+H	Edge_1RB_Right	12000	20000	-13	PASS

DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Outer_Full	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Left	12000	20000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	3000	12000	-13	PASS
DC_7A_n2A	15	5+20	DFT-16QAM	M+H	Edge_1RB_Right	12000	20000	-13	PASS

Test Graphs

