NTNV_DC_2A_n77A-3700-3980_PC2_30_5+80_M+H_TID6_#1

Spur	Range	Start Freq	Stop Freq	RBW	Frequency	Amplitude	ΔLimit
1	1	3.6900 GHz	3.6950 GHz	1.000 MHz	3.694735000 GHz	-35.06 dBm	-22.06 dB
2	2	3.6950 GHz	3.6990 GHz	510.0 kHz	3.698808000 GHz	-37.66 dBm	-24.66 dB
3	3	3.6990 GHz	3.7000 GHz	360.0 kHz	3.699420090 GHz	-39.29 dBm	-26.29 dB
4	4	3.7000 GHz	3.7800 GHz	510.0 kHz	3.748565000 GHz	-8.361 dBm	-38.36 dB

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+80_M+H_TID5_#1

Spur	Range	Start Freq	Stop Freq	RBW	Frequency	Amplitude	ΔLimit
1	1	3.6900 GHz	3.6950 GHz	1.000 MHz	3.694335000 GHz	-35.67 dBm	-22.67 dB
2	2	3.6950 GHz	3.6990 GHz	510.0 kHz	3.698888000 GHz	-38.14 dBm	-25.14 dB
3	3	3.6990 GHz	3.7000 GHz	360.0 kHz	3.699970830 GHz	-37.90 dBm	-24.90 dB
4	4	3.7000 GHz	3.7800 GHz	510.0 kHz	3.701125000 GHz	11.60 dBm	-18.40 dB

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+90_M+L_TID6_#1

Spur	Range	Start Freq	Stop Freq	RBW	Frequency	Amplitude	ΔLimit
1	1	3.9000 GHz	3.9800 GHz	510.0 kHz	3.958065000 GHz	-8.206 dBm	-38.21 dB
2	2	3.9800 GHz	3.9810 GHz	360.0 kHz	3.980635615 GHz	-38.31 dBm	-26.31 dB
3	3	3.9810 GHz	3.9850 GHz	510.0 kHz	3.983432000 GHz	-36.97 dBm	-23.97 dB
4	4	3.9850 GHz	3.9900 GHz	1.000 MHz	3.989095000 GHz	-34.44 dBm	-21.44 dB

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+90_M+L_TID4_#1

Spur	Range	Start Freq	Stop Freq	RBW	Frequency	Amplitude	ΔLimit
1	1	3.9000 GHz	3.9800 GHz	510.0 kHz	3.978865000 GHz	12.02 dBm	-17.98 dB
2	2	3.9800 GHz	3.9810 GHz	360.0 kHz	3.980040825 GHz	-38.38 dBm	-25.38 dB
3	3	3.9810 GHz	3.9850 GHz	510.0 kHz	3.981436000 GHz	-37.99 dBm	-24.99 dB
4	4	3.9850 GHz	3.9900 GHz	1.000 MHz	3.989410000 GHz	-35.42 dBm	-22.42 dB

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+90_M+H_TID6_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+90_M+H_TID5_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+100_M+L_TID6_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+100_M+L_TID4_#1

	Spectrum Analyzer 1 Spurious Emissions	

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+100_M+H_TID6_#1

	Spectrum Analyzer 1 Spurious Emissions	

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+100_M+H_TID5_#1

Top-left screen table:

Spur	Range	Start Freq	Stop Freq	RBW	Frequency	Amplitude	ΔLimit
1	1	3.6900 GHz	3.6950 GHz	1.000 MHz	3.694500000 GHz	-35.29 dBm	-22.29 dB
2	2	3.6950 GHz	3.6990 GHz	510.0 kHz	3.698224000 GHz	-37.88 dBm	-24.88 dB
3	3	3.6990 GHz	3.7000 GHz	360.0 kHz	3.699236810 GHz	-39.29 dBm	-26.29 dB
4	4	3.7000 GHz	3.8000 GHz	510.0 kHz	3.750995000 GHz	-9.110 dBm	-39.11 dB

Top-right screen table:

Spur	Range	Start Freq	Stop Freq	RBW	Frequency	Amplitude	ΔLimit
1	1	3.6900 GHz	3.6950 GHz	1.000 MHz	3.693545000 GHz	-35.64 dBm	-22.64 dB
2	2	3.6950 GHz	3.6990 GHz	510.0 kHz	3.698292000 GHz	-38.27 dBm	-25.27 dB
3	3	3.6990 GHz	3.7000 GHz	360.0 kHz	3.699993010 GHz	-37.39 dBm	-24.39 dB
4	4	3.7000 GHz	3.8000 GHz	510.0 kHz	3.700995000 GHz	11.59 dBm	-18.41 dB

Bottom-left screen table:

Spur	Range	Start Freq	Stop Freq	RBW	Frequency	Amplitude	ΔLimit
1	1	3.8800 GHz	3.9800 GHz	510.0 kHz	3.950395000 GHz	-8.894 dBm	-38.89 dB
2	2	3.9800 GHz	3.9810 GHz	360.0 kHz	3.980711565 GHz	-38.58 dBm	-25.58 dB
3	3	3.9810 GHz	3.9850 GHz	510.0 kHz	3.984556000 GHz	-36.95 dBm	-23.95 dB
4	4	3.9850 GHz	3.9900 GHz	1.000 MHz	3.988075000 GHz	-34.45 dBm	-21.45 dB

Bottom-right screen table:

Spur	Range	Start Freq	Stop Freq	RBW	Frequency	Amplitude	ΔLimit
1	1	3.8800 GHz	3.9800 GHz	510.0 kHz	3.978995000 GHz	11.52 dBm	-18.48 dB
2	2	3.9800 GHz	3.9810 GHz	360.0 kHz	3.979996005 GHz	-37.17 dBm	-24.17 dB
3	3	3.9810 GHz	3.9850 GHz	510.0 kHz	3.982676000 GHz	-37.93 dBm	-24.93 dB
4	4	3.9850 GHz	3.9900 GHz	1.000 MHz	3.987795000 GHz	-35.34 dBm	-22.34 dB

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SCS	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS

DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS

DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

3700-3980									
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS

DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS

DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

3700-3980									
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS

DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-	30	5+40	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

3700-3980									
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

3700-3980									
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS

DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS

DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-	30	5+60	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS

DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS

DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

3700-3980									
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS

DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS

DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

3700-3980									
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS

DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-	30	5+90	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS

3700-3980								
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS

DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS

DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS

3700-3980		0							
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS

DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS

3700-3980		0							
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS

DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS

Test Graphs

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_M+L_TID18_NS_01_0.009_0.15_#1	NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_M+L_TID18_NS_01_0.15_30_#1
NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_M+L_TID18_NS_01_30_1000_#1	NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_M+L_TID18_NS_01_1000_6000_#1
NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_M+L_TID18_NS_01_6000_26000_#1	NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_M+L_TID18_NS_01_26000_40000_#1

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID16_NS_01_0.009_0.15_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID16_NS_01_0.15_30_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID16_NS_01_30_1000_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID16_NS_01_1000_6000_#1

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID16_NS_01_6000_26000_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID16_NS_01_26000_40000_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID17_NS_01_0.009_0.15_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID17_NS_01_0.15_30_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID17_NS_01_30_1000_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID17_NS_01_1000_6000_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID17_NS_01_6000_26000_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID17_NS_01_26000_40000_#1

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID21_NS_01_0.009_0.15_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID21_NS_01_0.15_30_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID21_NS_01_30_1000_#1

NTNV_DC_2A_n77A-3700-3980_PC2_30_5+10_
M+L_TID21_NS_01_1000_6000_#1

Waltek Testing Group (Shenzhen) Co., Ltd.
http://www.waltek.com.cn