









## Appendix D: Band Edge for NSA

### Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Verdict
DC_2A_n77A-3700-39 80	30	5+10	DFT-QPSK	M+L	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n77A-3700-39 80	30	5+10	DFT-QPSK	M+H	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n77A-3700-39 80	30	5+15	DFT-QPSK	M+L	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n77A-3700-39 80	30	5+15	DFT-QPSK	M+H	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n77A-3700-39 80	30	5+20	DFT-QPSK	M+L	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n77A-3700-39 80	30	5+20	DFT-QPSK	M+H	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n77A-3700-39 80	30	5+40	DFT-QPSK	M+L	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n77A-3700-39 80	30	5+40	DFT-QPSK	M+H	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n77A-3700-39 80	30	5+50	DFT-QPSK	M+L	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n77A-3700-39 80	30	5+50	DFT-QPSK	M+H	Outer_Full	PASS

DC_2A_n77A-3700-39 80	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n77A-3700-39 80	30	5+60	DFT-QPSK	M+L	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n77A-3700-39 80	30	5+60	DFT-QPSK	M+H	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n77A-3700-39 80	30	5+80	DFT-QPSK	M+L	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n77A-3700-39 80	30	5+80	DFT-QPSK	M+H	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n77A-3700-39 80	30	5+90	DFT-QPSK	M+L	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n77A-3700-39 80	30	5+90	DFT-QPSK	M+H	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	PASS
DC_2A_n77A-3700-39 80	30	5+100	DFT-QPSK	M+L	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	PASS
DC_2A_n77A-3700-39 80	30	5+100	DFT-QPSK	M+H	Outer_Full	PASS
DC_2A_n77A-3700-39 80	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	PASS

### Test Graphs











