

Appendix for 2A_N77A(3450-3550MHz)

Product Name: Smart Phone

Model No: K50

Appendix A: Average Power Output

Test Result

Band	SC S	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	25.00	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+L	Inner_Full	25.93	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	22.55	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	22.61	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	23.98	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+L	Inner_Full	24.95	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.64	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	22.74	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	25.40	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+M	Inner_Full	26.60	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	23.12	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	23.09	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	24.60	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+M	Inner_Full	25.42	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	23.04	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	23.20	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	25.69	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+H	Inner_Full	26.72	PC2	PASS

DC_2A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	23.37	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	23.44	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	24.73	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+H	Inner_Full	25.63	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	23.54	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	23.36	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	24.96	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+L	Inner_Full	25.93	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	22.50	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	22.69	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	24.02	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+L	Inner_Full	25.02	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	22.65	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	22.55	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	25.51	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+M	Inner_Full	26.42	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	23.14	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	23.03	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	24.50	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+M	Inner_Full	25.54	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	23.23	PC2	PASS
DC_2A_n77A-37	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	23.12	PC2	PASS

00-3980								
DC_2A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	25.84	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+H	Inner_Full	26.67	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	23.47	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	23.41	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	24.72	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+H	Inner_Full	25.85	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	23.34	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	23.50	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	24.98	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+L	Inner_Full	25.94	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.61	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	22.69	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	23.96	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+L	Inner_Full	24.98	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.50	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	22.77	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	25.44	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+M	Inner_Full	26.50	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	23.09	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	23.03	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	24.49	PC2	PASS

DC_2A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+M	Inner_Full	25.50	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	23.25	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	23.00	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	25.74	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+H	Inner_Full	26.84	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	23.31	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	23.52	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	24.85	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+H	Inner_Full	25.82	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	23.50	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	23.37	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	24.97	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+L	Inner_Full	26.04	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	22.43	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	22.57	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	24.05	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+L	Inner_Full	25.00	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.52	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	22.60	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	25.41	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+M	Inner_Full	26.44	PC2	PASS
DC_2A_n77A-37	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	22.89	PC2	PASS

00-3980								
DC_2A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	23.00	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	24.45	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+M	Inner_Full	25.47	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	23.01	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.12	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	25.67	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+H	Inner_Full	26.70	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	22.89	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.31	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	24.69	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+H	Inner_Full	25.76	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	22.99	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	23.25	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	25.41	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+L	Inner_Full	26.47	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	22.89	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	22.99	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	24.47	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+L	Inner_Full	25.48	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	23.00	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	23.08	PC2	PASS

DC_2A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	25.93	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+M	Inner_Full	26.95	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	23.38	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.63	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	25.01	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+M	Inner_Full	25.96	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	23.38	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	23.75	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	25.91	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+H	Inner_Full	26.95	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	23.40	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.66	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	24.90	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+H	Inner_Full	25.94	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	23.49	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	23.69	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	25.41	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+L	Inner_Full	26.40	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	22.81	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	22.97	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	24.41	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+L	Inner_Full	25.43	PC2	PASS

00-3980								
DC_2A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.55	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	23.09	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	25.79	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+M	Inner_Full	26.90	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.25	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.54	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	24.90	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+M	Inner_Full	25.95	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	23.41	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	23.51	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	25.89	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+H	Inner_Full	26.84	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.29	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.59	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	24.88	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+H	Inner_Full	25.86	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	23.41	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	23.66	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+L	Outer_Full	25.50	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+L	Inner_Full	26.48	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	22.91	PC2	PASS

DC_2A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	23.04	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+L	Outer_Full	24.49	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+L	Inner_Full	25.51	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.83	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	23.06	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+M	Outer_Full	25.98	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+M	Inner_Full	26.95	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	23.37	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	23.52	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+M	Outer_Full	24.99	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+M	Inner_Full	25.96	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	23.21	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	23.63	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+H	Outer_Full	25.91	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+H	Inner_Full	26.86	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	23.24	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	23.60	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+H	Outer_Full	24.98	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+H	Inner_Full	25.81	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	23.33	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	23.68	PC2	PASS
DC_2A_n77A-37	30	5+80	DFT-QPSK	M+L	Outer_Full	25.82	PC2	PASS

00-3980								
DC_2A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+L	Inner_Full	26.89	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.31	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.27	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	24.72	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+L	Inner_Full	25.85	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	23.49	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	23.42	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	25.95	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+M	Inner_Full	27.00	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.43	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	23.52	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	24.79	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+M	Inner_Full	25.90	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	23.50	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	23.66	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	25.94	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+H	Inner_Full	26.91	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.43	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	23.68	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	24.91	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+H	Inner_Full	25.86	PC2	PASS

DC_2A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	23.44	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	23.67	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	25.84	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+L	Inner_Full	26.91	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.25	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	23.34	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	24.88	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+L	Inner_Full	25.91	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	23.24	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	23.47	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	25.95	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+M	Inner_Full	26.97	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	23.24	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	23.49	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	24.86	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+M	Inner_Full	25.95	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	23.28	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	23.53	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	25.95	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+H	Inner_Full	26.88	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.43	PC2	PASS
DC_2A_n77A-37	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	23.53	PC2	PASS

00-3980								
DC_2A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	24.94	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+H	Inner_Full	25.89	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	23.54	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	23.75	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	25.83	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+L	Inner_Full	26.81	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	22.94	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	23.40	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	24.86	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+L	Inner_Full	25.86	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	22.98	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	23.42	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	25.95	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+M	Inner_Full	26.98	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	22.99	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	23.27	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	24.95	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+M	Inner_Full	25.99	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	23.07	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	23.40	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	25.90	PC2	PASS

DC_2A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+H	Inner_Full	26.70	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.26	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	23.45	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	24.90	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+H	Inner_Full	25.82	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	23.17	PC2	PASS
DC_2A_n77A-37 00-3980	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	23.54	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

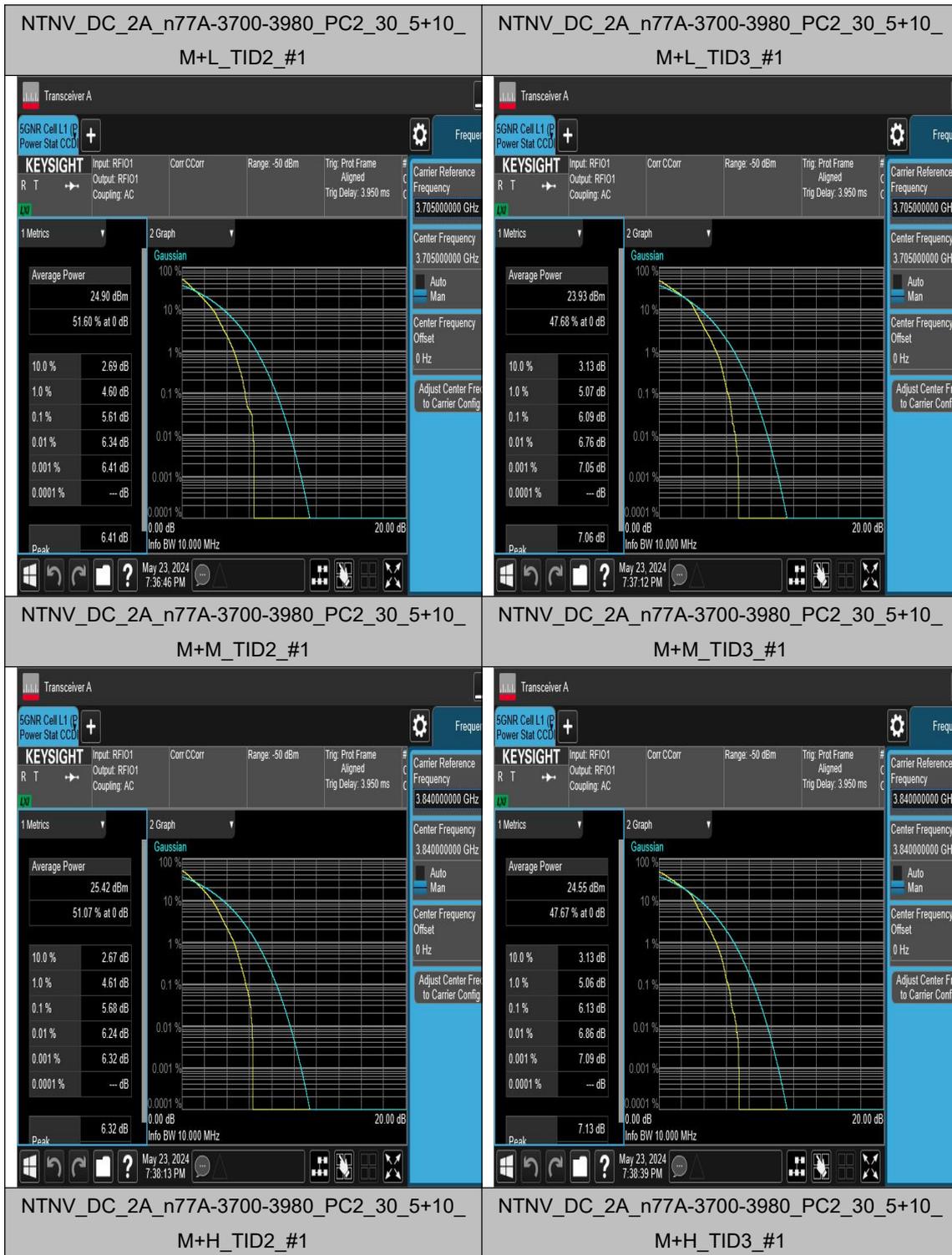
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n7 7A-3700-39 80	30	5+10	DFT-QPSK	M+L	Outer_Full	5.61	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+10	DFT-16QAM	M+L	Outer_Full	6.09	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+10	DFT-QPSK	M+M	Outer_Full	5.68	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+10	DFT-16QAM	M+M	Outer_Full	6.13	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+10	DFT-QPSK	M+H	Outer_Full	5.58	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+10	DFT-16QAM	M+H	Outer_Full	6.06	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+15	DFT-QPSK	M+L	Outer_Full	5.61	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+15	DFT-16QAM	M+L	Outer_Full	6.29	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+15	DFT-QPSK	M+M	Outer_Full	5.55	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+15	DFT-16QAM	M+M	Outer_Full	6.38	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+15	DFT-QPSK	M+H	Outer_Full	5.53	≤13	PASS
DC_2A_n7	30	5+15	DFT-16QAM	M+H	Outer_Full	6.38	≤13	PASS

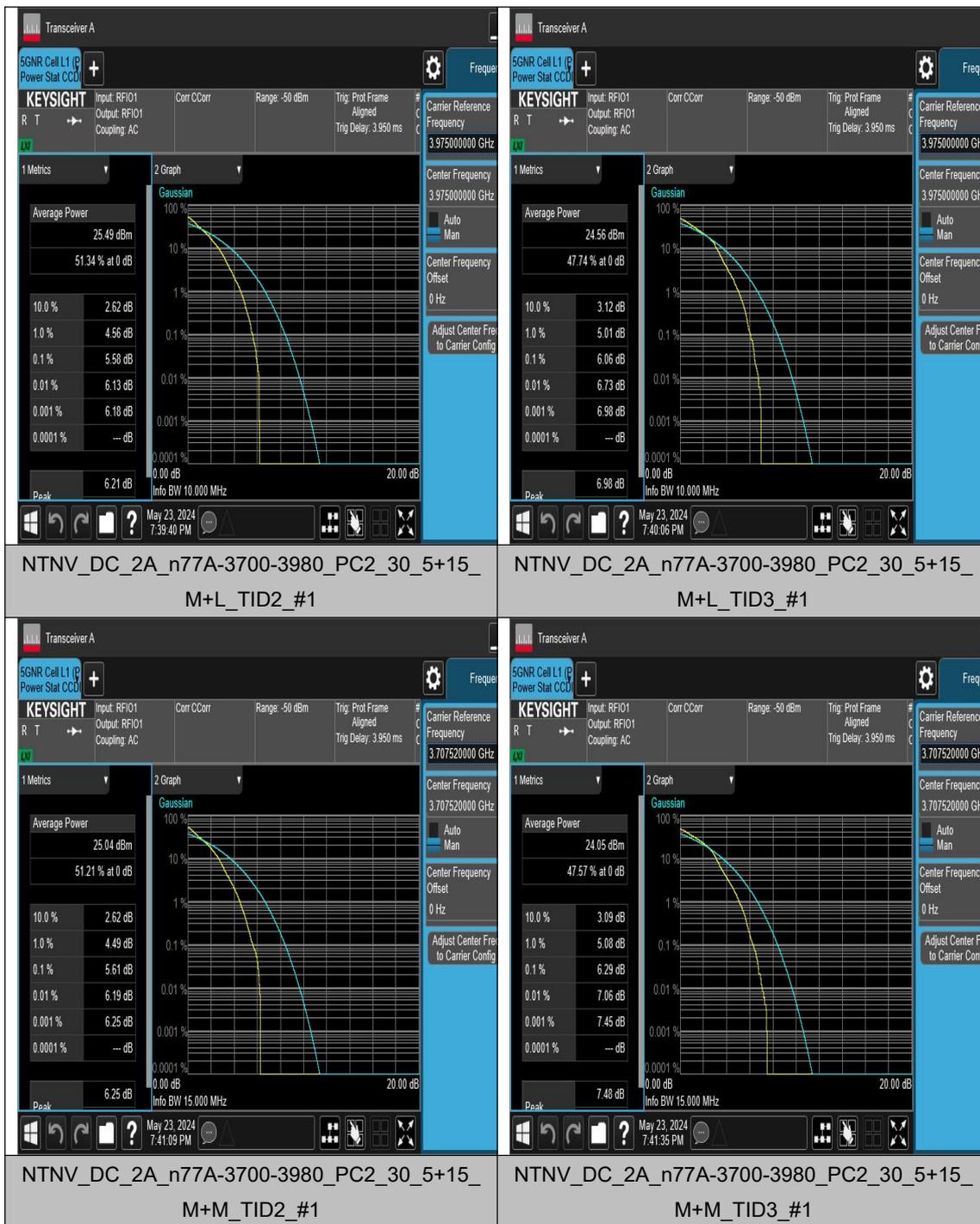
7A-3700-39 80								
DC_2A_n7 7A-3700-39 80	30	5+20	DFT-QPSK	M+L	Outer_Full	5.72	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+20	DFT-16QAM	M+L	Outer_Full	6.42	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+20	DFT-QPSK	M+M	Outer_Full	5.75	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+20	DFT-16QAM	M+M	Outer_Full	6.49	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+20	DFT-QPSK	M+H	Outer_Full	5.73	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+20	DFT-16QAM	M+H	Outer_Full	6.48	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+40	DFT-QPSK	M+L	Outer_Full	5.70	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+40	DFT-16QAM	M+L	Outer_Full	6.26	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+40	DFT-QPSK	M+M	Outer_Full	5.65	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+40	DFT-16QAM	M+M	Outer_Full	6.22	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+40	DFT-QPSK	M+H	Outer_Full	5.63	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+40	DFT-16QAM	M+H	Outer_Full	6.27	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+50	DFT-QPSK	M+L	Outer_Full	5.91	≤13	PASS
DC_2A_n7 7A-3700-39	30	5+50	DFT-16QAM	M+L	Outer_Full	6.39	≤13	PASS

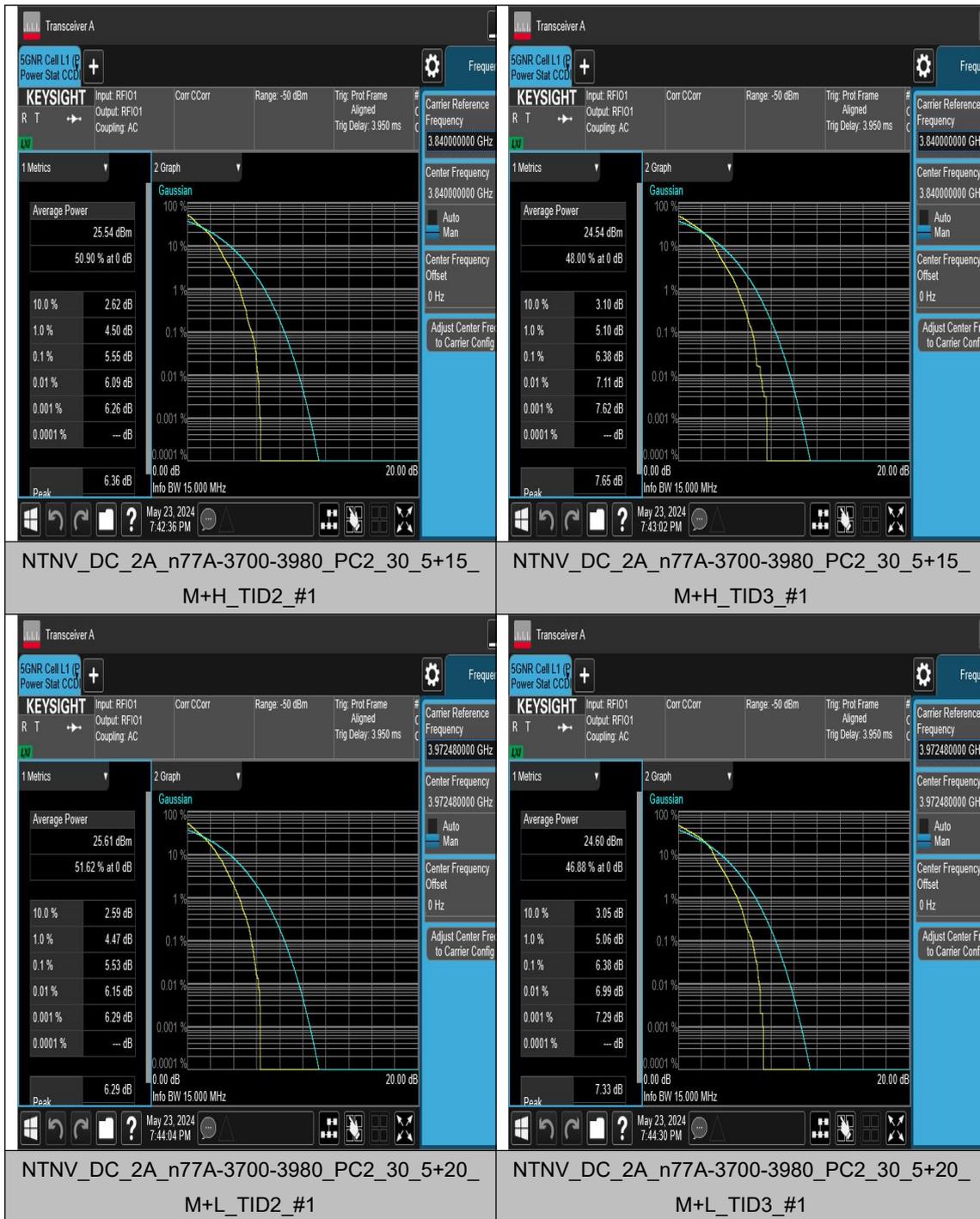
80								
DC_2A_n7 7A-3700-39 80	30	5+50	DFT-QPSK	M+M	Outer_Full	5.86	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+50	DFT-16QAM	M+M	Outer_Full	6.33	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+50	DFT-QPSK	M+H	Outer_Full	5.82	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+50	DFT-16QAM	M+H	Outer_Full	6.33	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+60	DFT-QPSK	M+L	Outer_Full	5.68	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+60	DFT-16QAM	M+L	Outer_Full	6.29	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+60	DFT-QPSK	M+M	Outer_Full	5.62	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+60	DFT-16QAM	M+M	Outer_Full	6.35	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+60	DFT-QPSK	M+H	Outer_Full	5.61	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+60	DFT-16QAM	M+H	Outer_Full	6.31	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+80	DFT-QPSK	M+L	Outer_Full	5.71	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+80	DFT-16QAM	M+L	Outer_Full	6.43	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+80	DFT-QPSK	M+M	Outer_Full	5.79	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+80	DFT-16QAM	M+M	Outer_Full	6.36	≤13	PASS

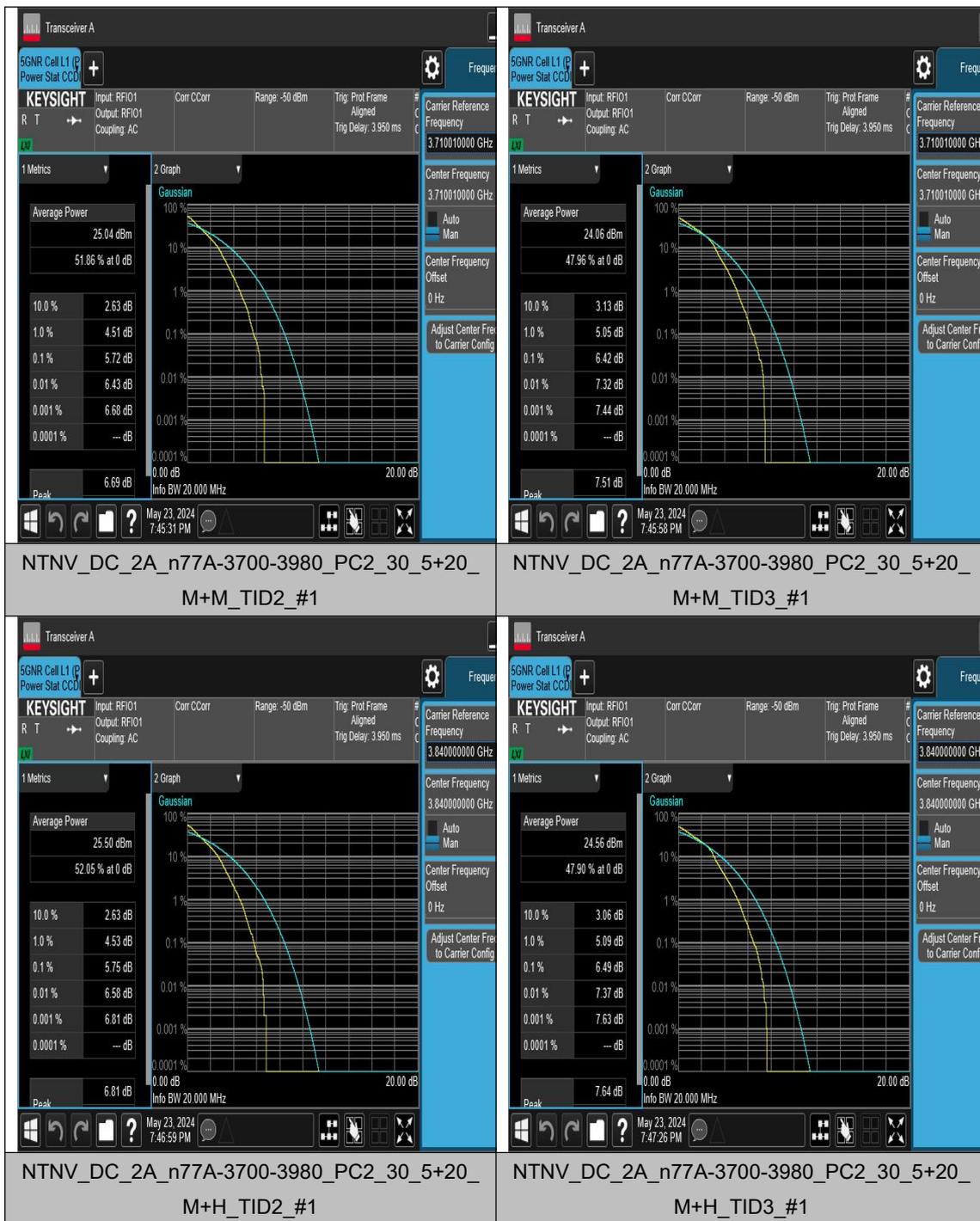
DC_2A_n7 7A-3700-39 80	30	5+80	DFT-QPSK	M+H	Outer_Full	5.66	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+80	DFT-16QAM	M+H	Outer_Full	6.38	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+90	DFT-QPSK	M+L	Outer_Full	5.76	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+90	DFT-16QAM	M+L	Outer_Full	6.57	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+90	DFT-QPSK	M+M	Outer_Full	5.70	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+90	DFT-16QAM	M+M	Outer_Full	6.45	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+90	DFT-QPSK	M+H	Outer_Full	5.68	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+90	DFT-16QAM	M+H	Outer_Full	6.44	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+100	DFT-QPSK	M+L	Outer_Full	5.78	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+100	DFT-16QAM	M+L	Outer_Full	6.57	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+100	DFT-QPSK	M+M	Outer_Full	5.78	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+100	DFT-16QAM	M+M	Outer_Full	6.47	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+100	DFT-QPSK	M+H	Outer_Full	5.72	≤13	PASS
DC_2A_n7 7A-3700-39 80	30	5+100	DFT-16QAM	M+H	Outer_Full	6.53	≤13	PASS

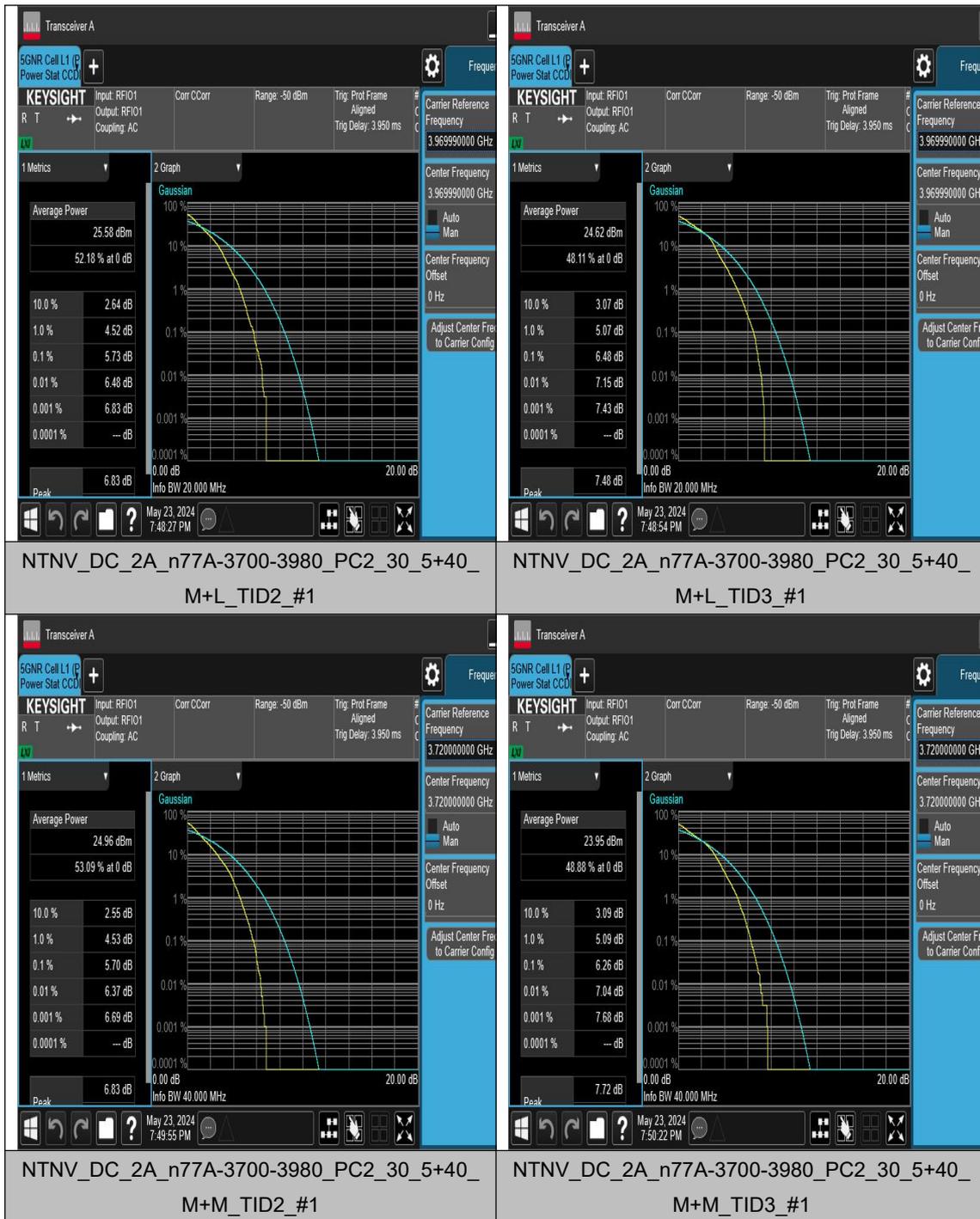
Test Graphs

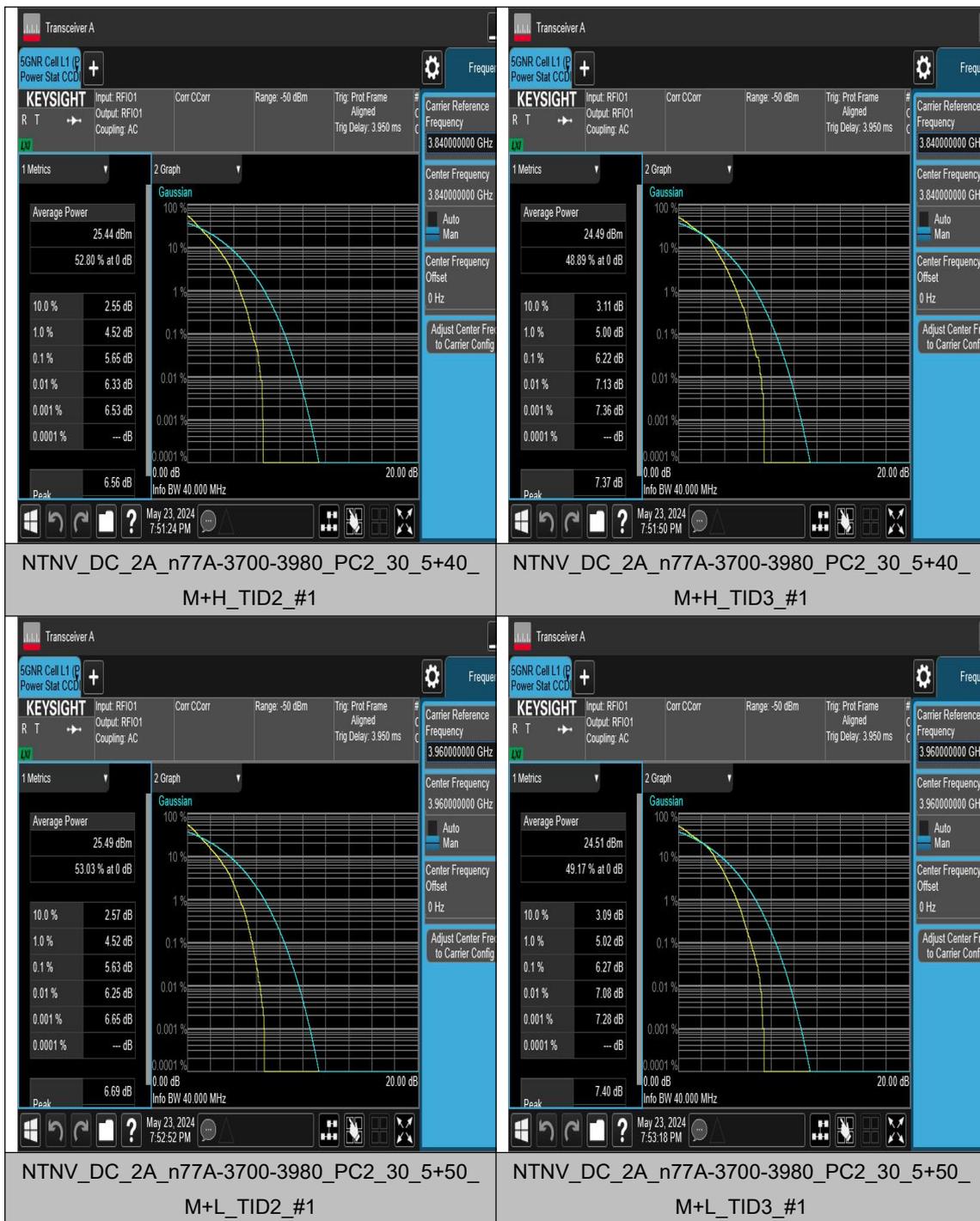


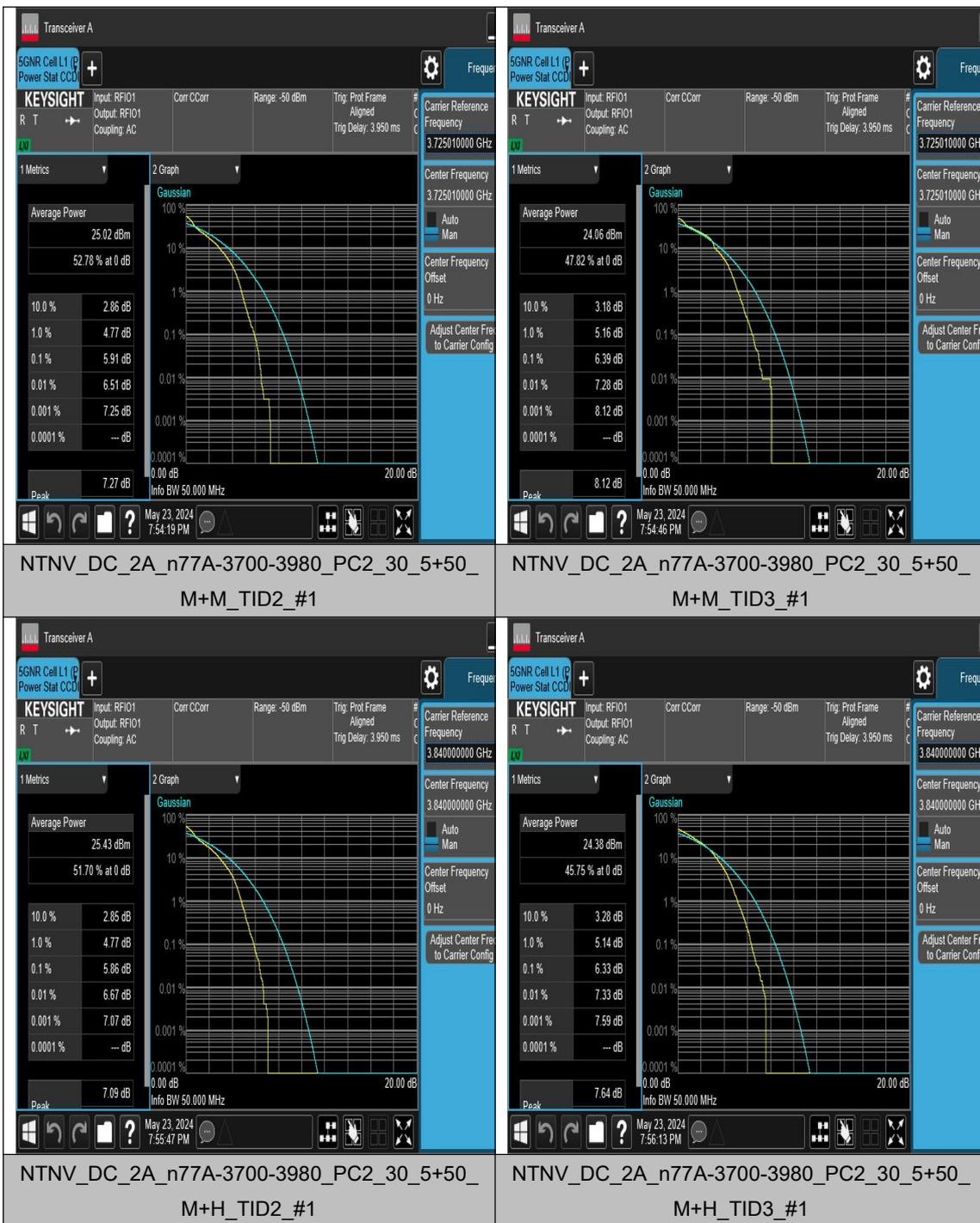


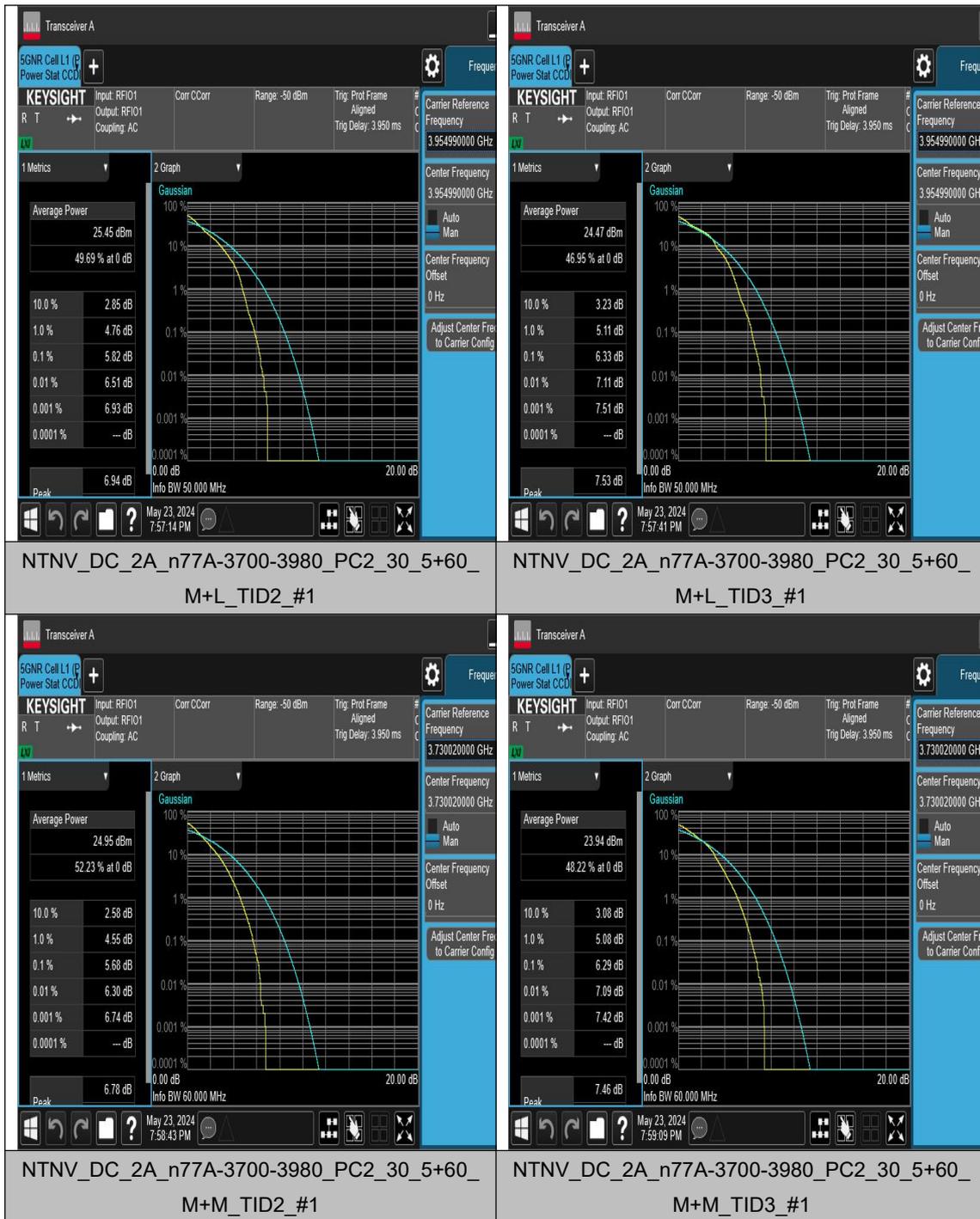


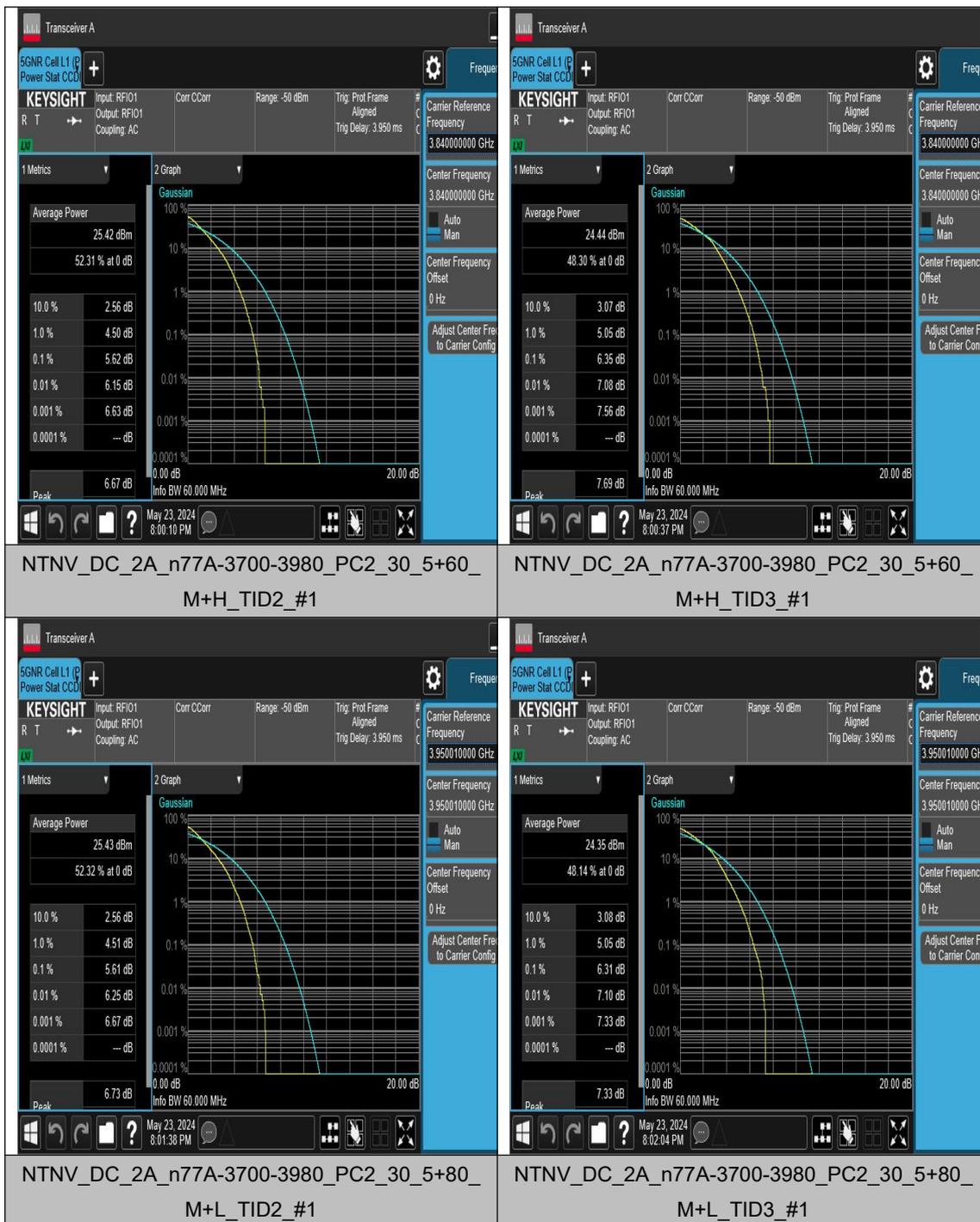


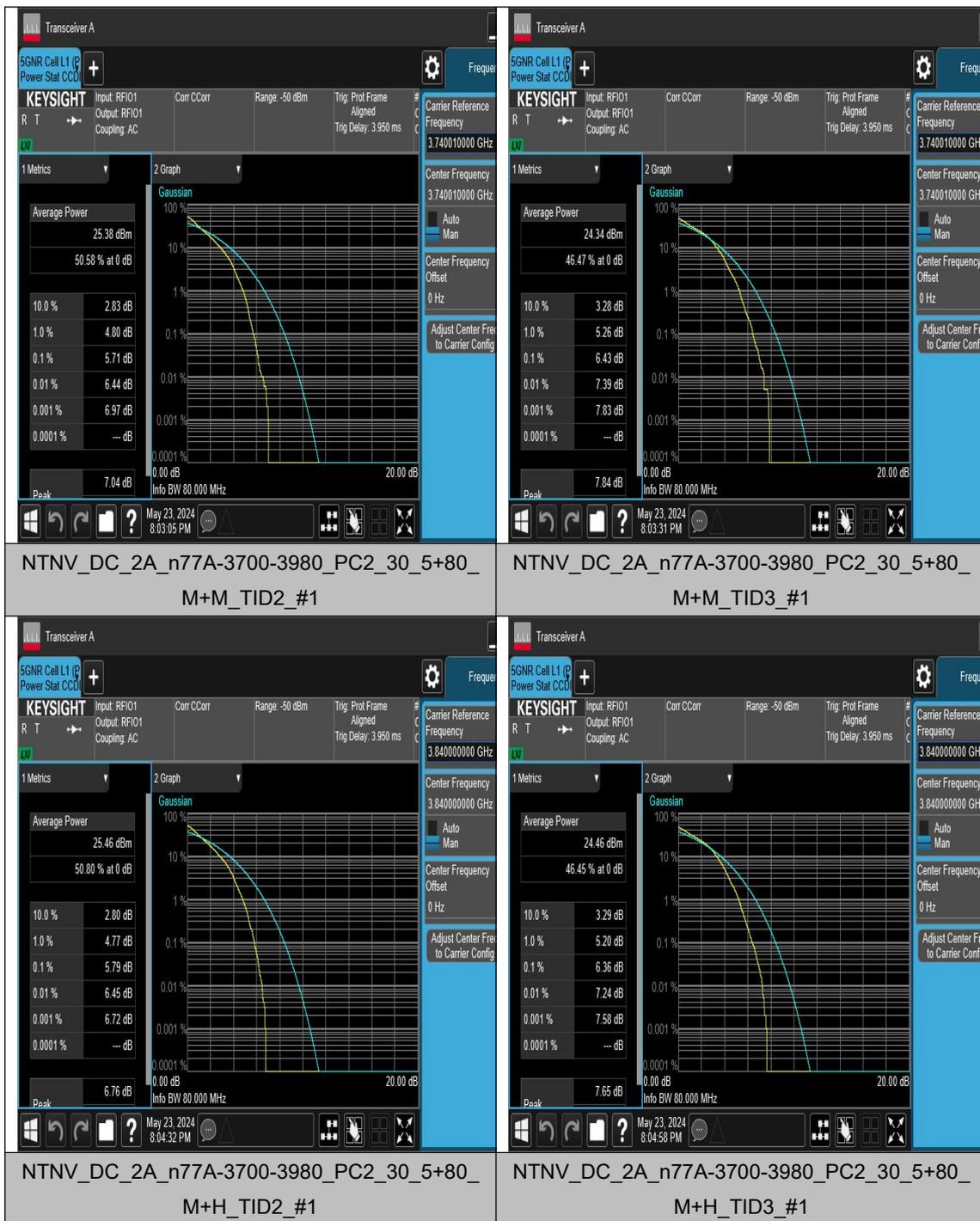


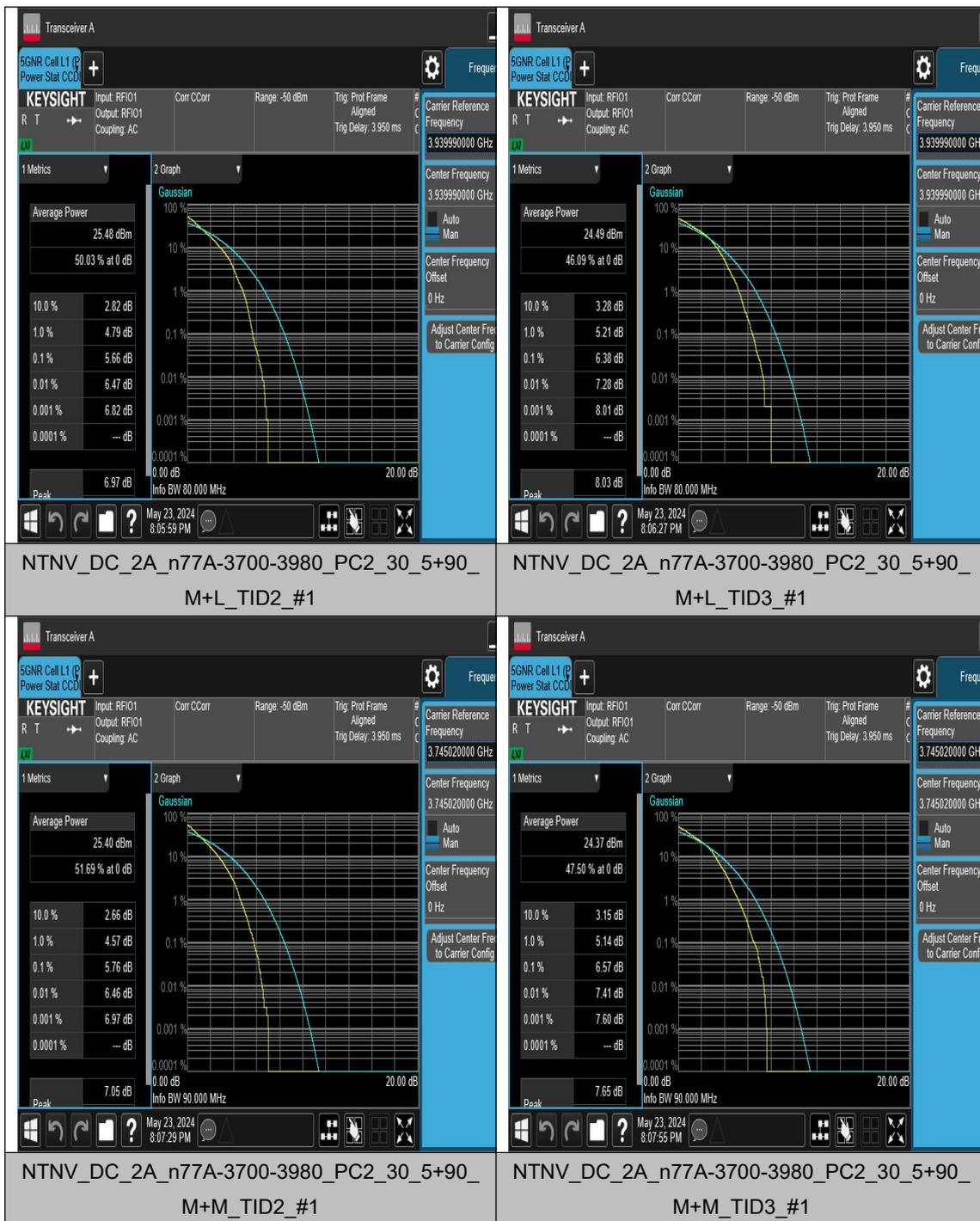


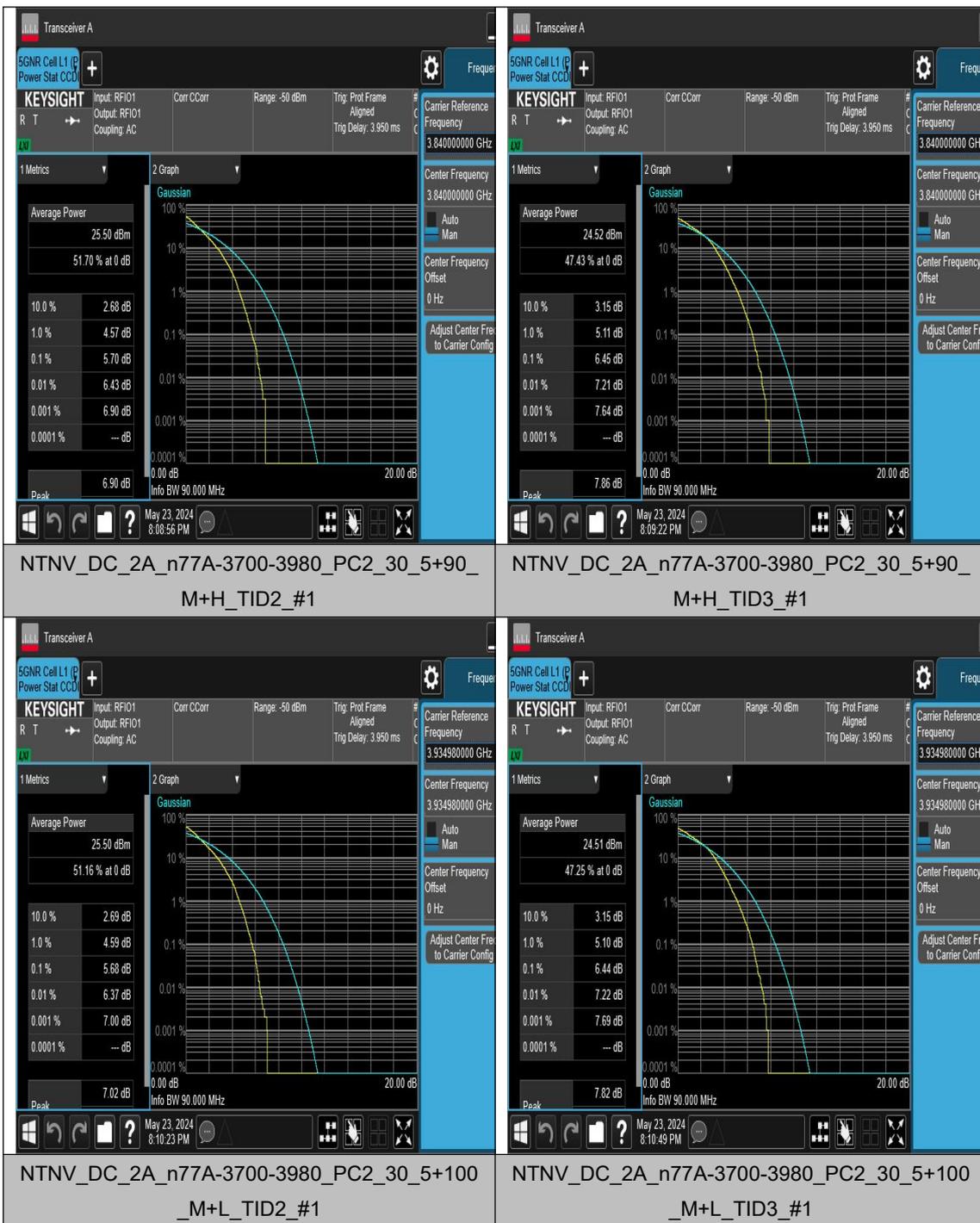


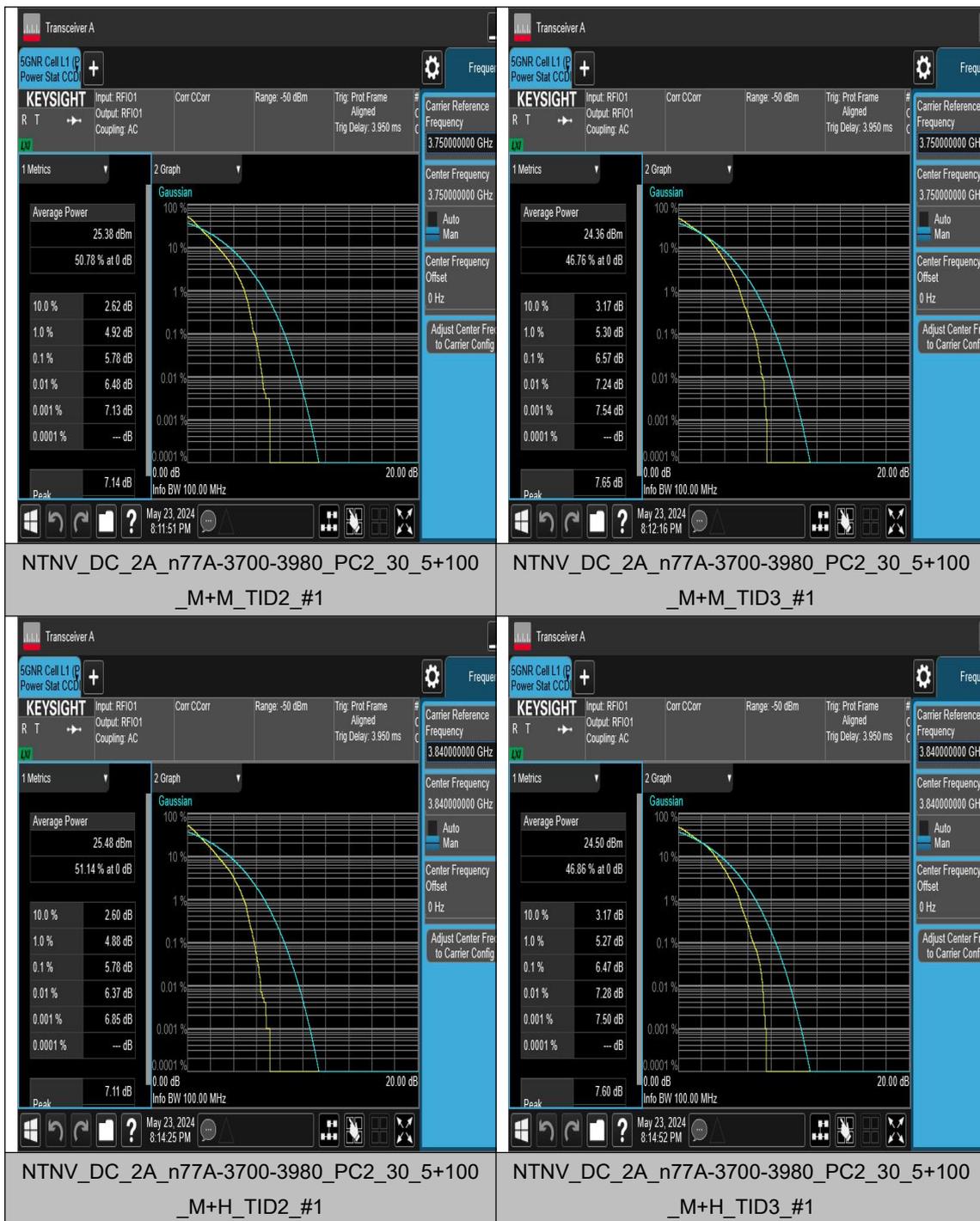


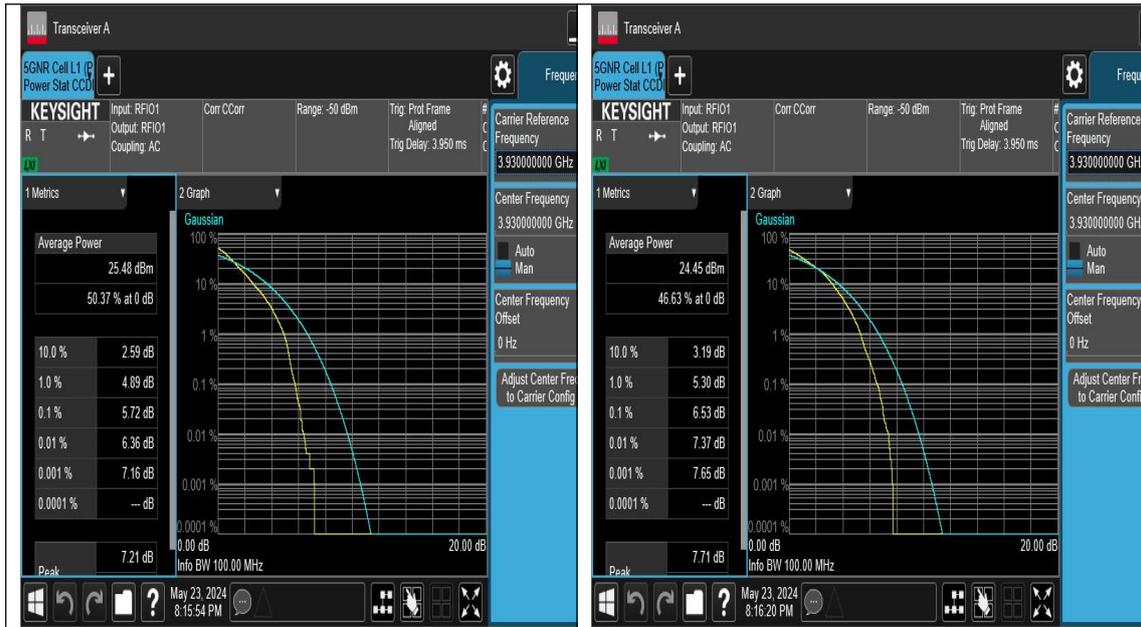












Appendix C: 26dB Bandwidth and Occupied Bandwidth for

NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n77A-3700-39 80	30	5+10	DFT-QPSK	M+L	Outer_Full	8.5878	9.352	PASS
DC_2A_n77A-3700-39 80	30	5+10	DFT-16QAM	M+L	Outer_Full	8.5730	9.291	PASS
DC_2A_n77A-3700-39 80	30	5+10	DFT-QPSK	M+M	Outer_Full	8.5921	9.331	PASS
DC_2A_n77A-3700-39 80	30	5+10	DFT-16QAM	M+M	Outer_Full	8.5840	9.342	PASS
DC_2A_n77A-3700-39 80	30	5+10	DFT-QPSK	M+H	Outer_Full	8.5904	9.350	PASS
DC_2A_n77A-3700-39 80	30	5+10	DFT-16QAM	M+H	Outer_Full	8.5705	9.282	PASS
DC_2A_n77A-3700-39 80	30	5+15	DFT-QPSK	M+L	Outer_Full	13.566	14.48	PASS
DC_2A_n77A-3700-39 80	30	5+15	DFT-16QAM	M+L	Outer_Full	13.578	14.52	PASS
DC_2A_n77A-3700-39 80	30	5+15	DFT-QPSK	M+M	Outer_Full	13.542	14.50	PASS
DC_2A_n77A-3700-39 80	30	5+15	DFT-16QAM	M+M	Outer_Full	13.561	14.40	PASS
DC_2A_n77A-3700-39 80	30	5+15	DFT-QPSK	M+H	Outer_Full	13.564	14.49	PASS
DC_2A_n77A-3700-39 80	30	5+15	DFT-16QAM	M+H	Outer_Full	13.584	14.48	PASS
DC_2A_n77A-3700-39 80	30	5+20	DFT-QPSK	M+L	Outer_Full	18.219	19.25	PASS
DC_2A_n77A-3700-39 80	30	5+20	DFT-16QAM	M+L	Outer_Full	18.251	19.31	PASS
DC_2A_n77A-3700-39 80	30	5+20	DFT-QPSK	M+M	Outer_Full	18.176	19.19	PASS
DC_2A_n77A-3700-39 80	30	5+20	DFT-16QAM	M+M	Outer_Full	18.243	19.16	PASS
DC_2A_n77A-3700-39 80	30	5+20	DFT-QPSK	M+H	Outer_Full	18.205	19.27	PASS

DC_2A_n77A-3700-39 80	30	5+20	DFT-16QAM	M+H	Outer_Full	18.263	19.22	PASS
DC_2A_n77A-3700-39 80	30	5+40	DFT-QPSK	M+L	Outer_Full	37.920	39.67	PASS
DC_2A_n77A-3700-39 80	30	5+40	DFT-16QAM	M+L	Outer_Full	37.813	39.58	PASS
DC_2A_n77A-3700-39 80	30	5+40	DFT-QPSK	M+M	Outer_Full	37.813	39.52	PASS
DC_2A_n77A-3700-39 80	30	5+40	DFT-16QAM	M+M	Outer_Full	37.731	39.46	PASS
DC_2A_n77A-3700-39 80	30	5+40	DFT-QPSK	M+H	Outer_Full	37.856	39.53	PASS
DC_2A_n77A-3700-39 80	30	5+40	DFT-16QAM	M+H	Outer_Full	37.824	39.52	PASS
DC_2A_n77A-3700-39 80	30	5+50	DFT-QPSK	M+L	Outer_Full	47.400	49.51	PASS
DC_2A_n77A-3700-39 80	30	5+50	DFT-16QAM	M+L	Outer_Full	47.393	49.43	PASS
DC_2A_n77A-3700-39 80	30	5+50	DFT-QPSK	M+M	Outer_Full	47.326	49.49	PASS
DC_2A_n77A-3700-39 80	30	5+50	DFT-16QAM	M+M	Outer_Full	47.330	49.38	PASS
DC_2A_n77A-3700-39 80	30	5+50	DFT-QPSK	M+H	Outer_Full	47.353	49.50	PASS
DC_2A_n77A-3700-39 80	30	5+50	DFT-16QAM	M+H	Outer_Full	47.357	49.40	PASS
DC_2A_n77A-3700-39 80	30	5+60	DFT-QPSK	M+L	Outer_Full	57.874	60.02	PASS
DC_2A_n77A-3700-39 80	30	5+60	DFT-16QAM	M+L	Outer_Full	57.736	59.89	PASS
DC_2A_n77A-3700-39 80	30	5+60	DFT-QPSK	M+M	Outer_Full	57.866	59.94	PASS
DC_2A_n77A-3700-39 80	30	5+60	DFT-16QAM	M+M	Outer_Full	57.761	59.88	PASS
DC_2A_n77A-3700-39 80	30	5+60	DFT-QPSK	M+H	Outer_Full	57.878	59.86	PASS
DC_2A_n77A-3700-39 80	30	5+60	DFT-16QAM	M+H	Outer_Full	57.730	59.88	PASS
DC_2A_n77A-3700-39 80	30	5+80	DFT-QPSK	M+L	Outer_Full	77.370	80.29	PASS
DC_2A_n77A-3700-39 80	30	5+80	DFT-16QAM	M+L	Outer_Full	77.472	80.18	PASS
DC_2A_n77A-3700-39	30	5+80	DFT-QPSK	M+M	Outer_Full	77.408	80.10	PASS

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DC_2A_n77A-3700-39 80	30	5+80	DFT-16QAM	M+M	Outer_Full	77.435	80.10	PASS
DC_2A_n77A-3700-39 80	30	5+80	DFT-QPSK	M+H	Outer_Full	77.427	80.28	PASS
DC_2A_n77A-3700-39 80	30	5+80	DFT-16QAM	M+H	Outer_Full	77.541	80.09	PASS
DC_2A_n77A-3700-39 80	30	5+90	DFT-QPSK	M+L	Outer_Full	87.387	90.47	PASS
DC_2A_n77A-3700-39 80	30	5+90	DFT-16QAM	M+L	Outer_Full	87.542	90.41	PASS
DC_2A_n77A-3700-39 80	30	5+90	DFT-QPSK	M+M	Outer_Full	87.399	90.37	PASS
DC_2A_n77A-3700-39 80	30	5+90	DFT-16QAM	M+M	Outer_Full	87.567	90.32	PASS
DC_2A_n77A-3700-39 80	30	5+90	DFT-QPSK	M+H	Outer_Full	87.383	90.31	PASS
DC_2A_n77A-3700-39 80	30	5+90	DFT-16QAM	M+H	Outer_Full	87.576	90.47	PASS
DC_2A_n77A-3700-39 80	30	5+100	DFT-QPSK	M+L	Outer_Full	97.542	100.7	PASS
DC_2A_n77A-3700-39 80	30	5+100	DFT-16QAM	M+L	Outer_Full	97.302	100.7	PASS
DC_2A_n77A-3700-39 80	30	5+100	DFT-QPSK	M+M	Outer_Full	97.538	100.7	PASS
DC_2A_n77A-3700-39 80	30	5+100	DFT-16QAM	M+M	Outer_Full	97.495	100.7	PASS
DC_2A_n77A-3700-39 80	30	5+100	DFT-QPSK	M+H	Outer_Full	97.538	100.7	PASS
DC_2A_n77A-3700-39 80	30	5+100	DFT-16QAM	M+H	Outer_Full	97.337	100.7	PASS