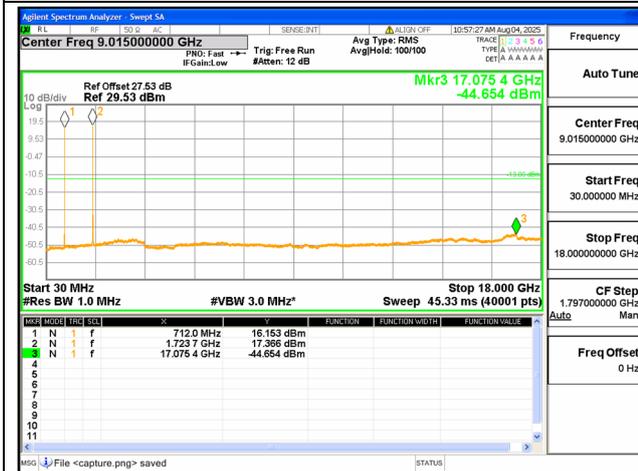




4A-12A / 20+10MHz / QPSK / Mid+Low CH /
1@99-1@0



4A-12A / 20+10MHz / QPSK / Mid+Low CH /
100@0-50@0



4A-12A / 20+10MHz / QPSK / Mid+Mid CH /
1@0-1@49



4A-12A / 20+10MHz / QPSK / Mid+Mid CH /
1@99-1@0



4A-12A / 20+10MHz / QPSK / Mid+Mid CH /
100@0-50@0



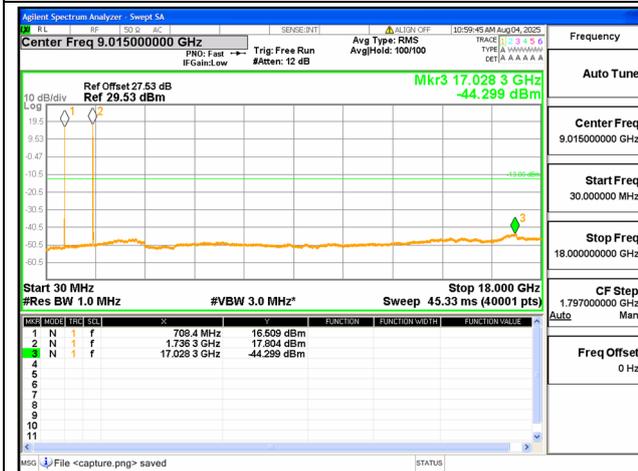
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1@0-1@49



4A-12A / 20+10MHz / QPSK / Mid+High CH /
1@99-1@0



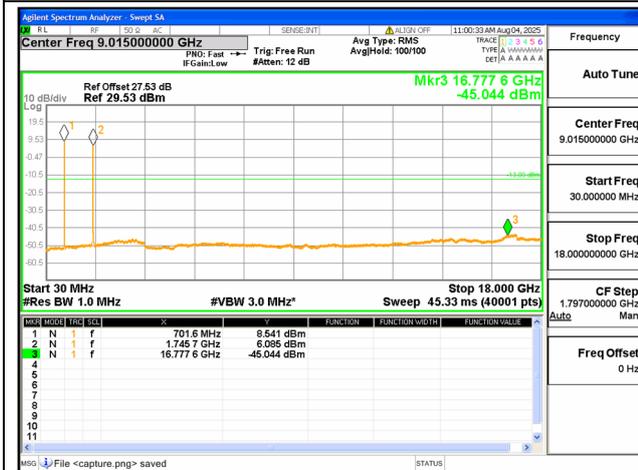
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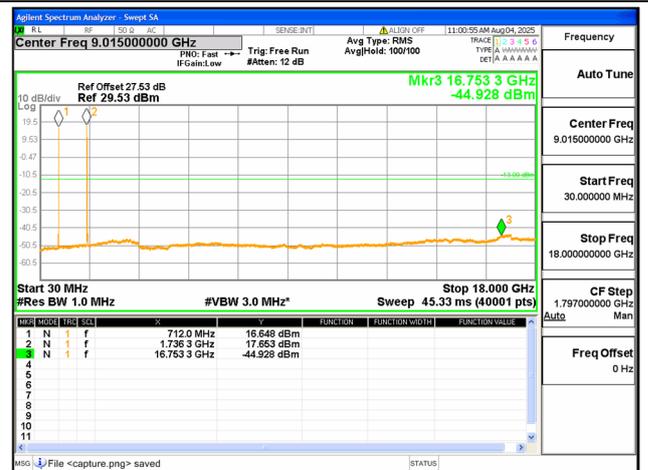
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1@0-1@49



4A-12A / 20+10MHz / QPSK / High+Low CH /
1@99-1@0



4A-12A / 20+10MHz / QPSK / High+Low CH /
100@0-50@0



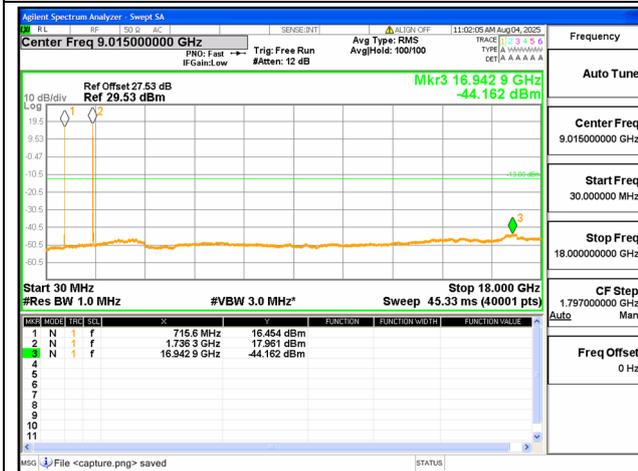
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1@0-1@49



4A-12A / 20+10MHz / QPSK / High+Mid CH / 1@99-1@0



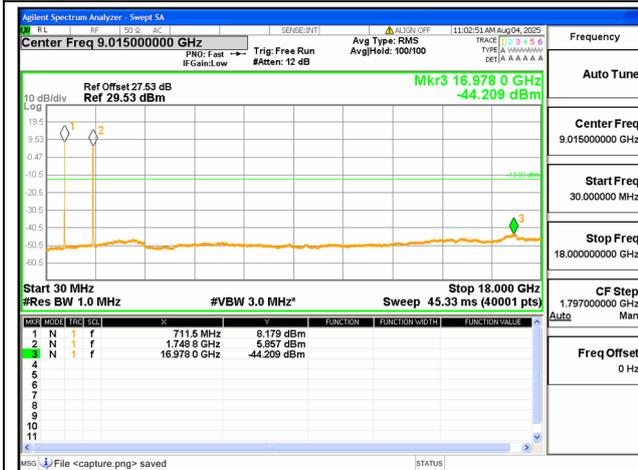
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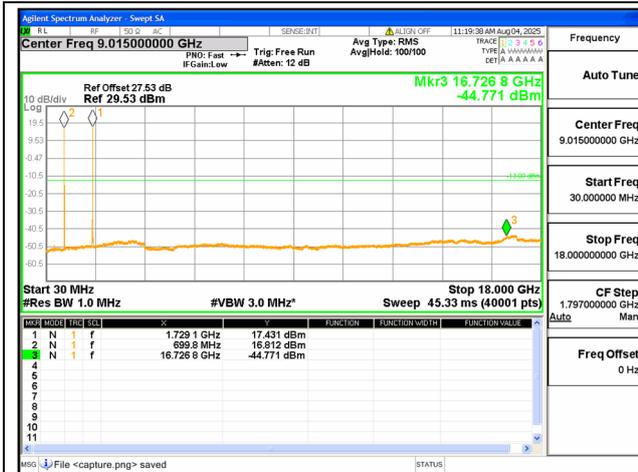
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4A-12A / 20+10MHz / QPSK / High+High CH / 1@99-1@0



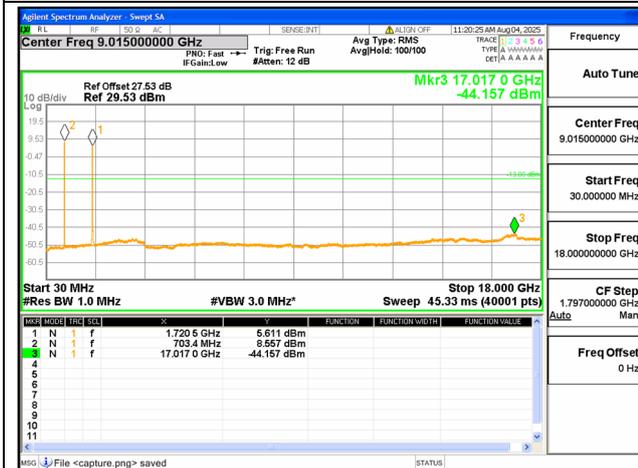
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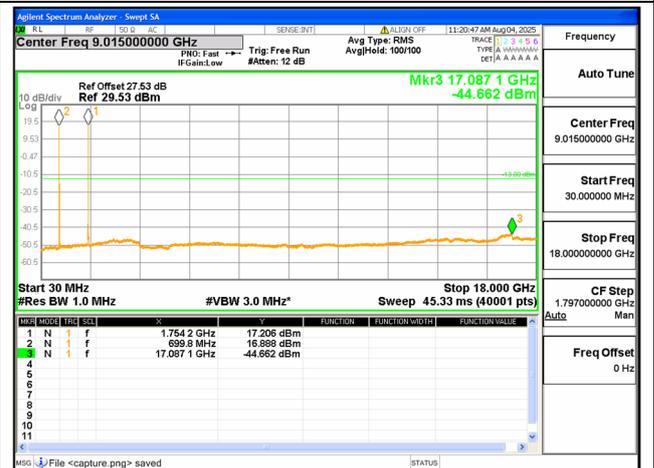
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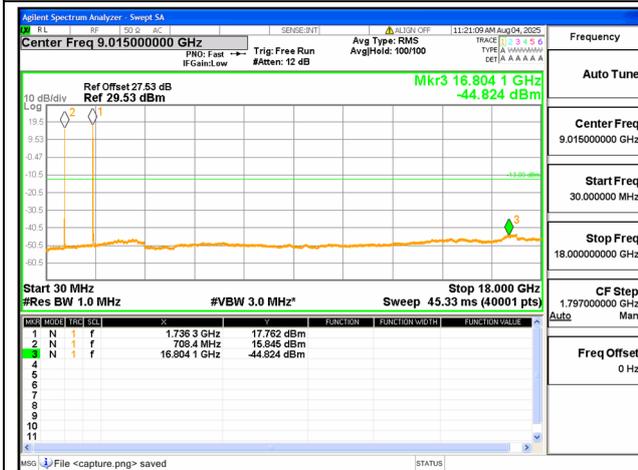
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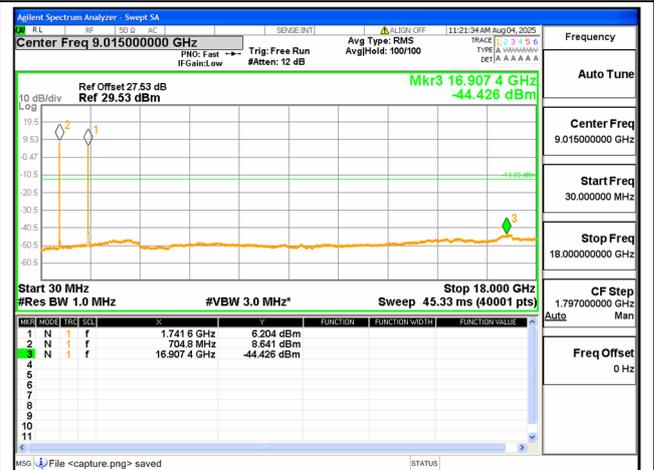
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12A-66A / 10+20MHz / QPSK / Low+Mid CH / 1@0-1@99



12A-66A / 10+20MHz / QPSK / Low+Mid CH / 1@49-1@0



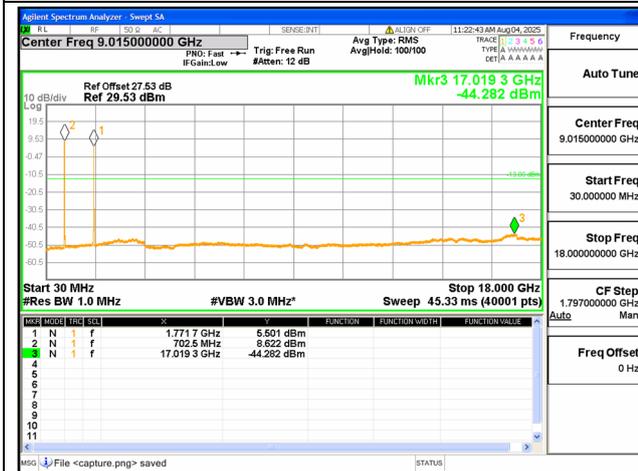
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12A-66A / 10+20MHz / QPSK / Low+High CH / 1@0-1@99



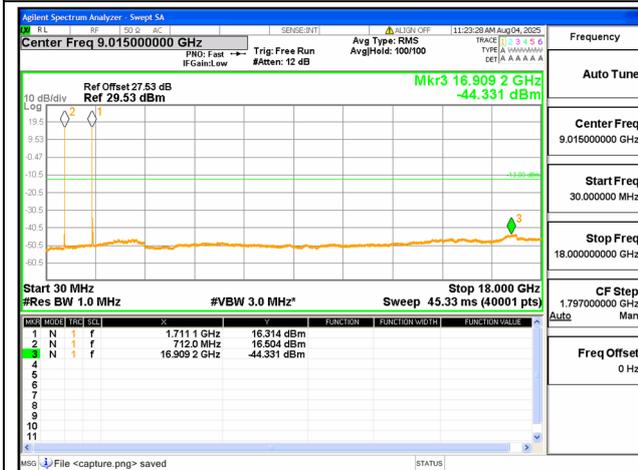
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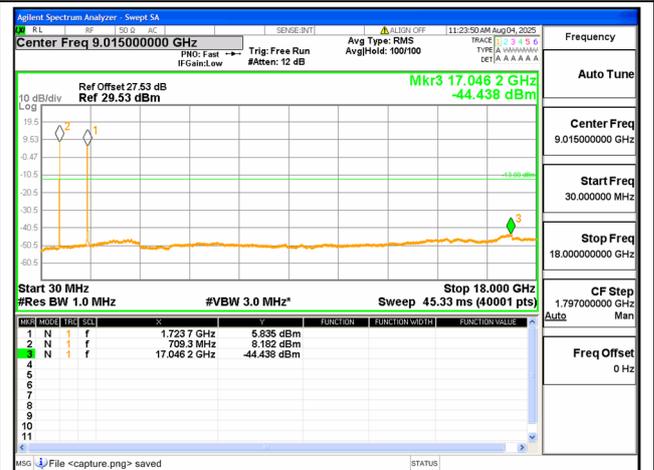
12A-66A / 10+20MHz / QPSK / Low+High CH / 50@0-100@0



12A-66A / 10+20MHz / QPSK / Mid+Low CH / 1@0-1@99



12A-66A / 10+20MHz / QPSK / Mid+Low CH / 1@49-1@0



12A-66A / 10+20MHz / QPSK / Mid+Low CH / 50@0-100@0