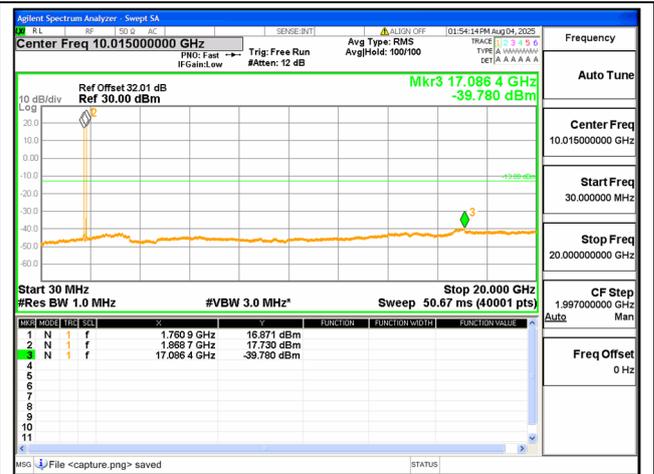
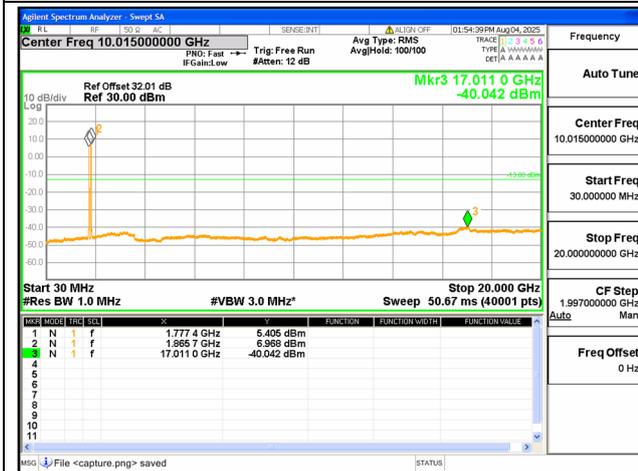




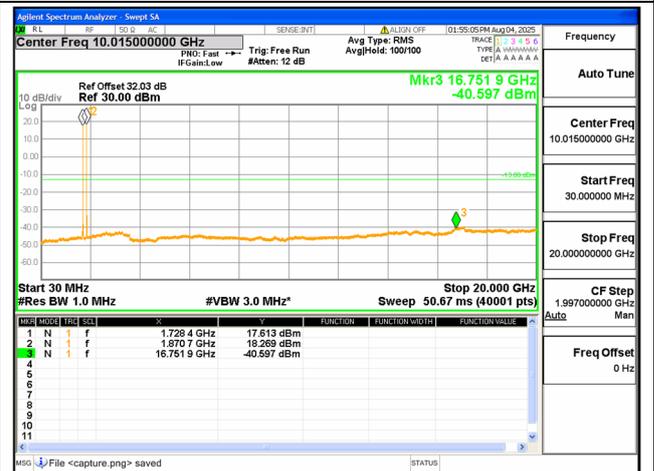
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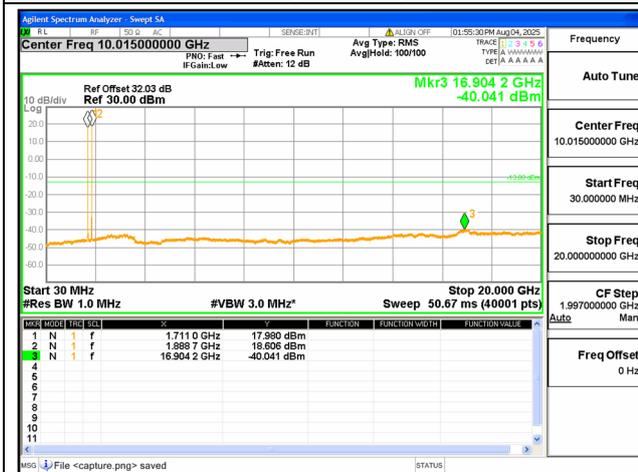
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2A-66A / 20+20MHz / QPSK / Low+High CH / 100@0-100@0



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2A-66A / 20+20MHz / QPSK / Mid+Low CH / 1@99-1@0



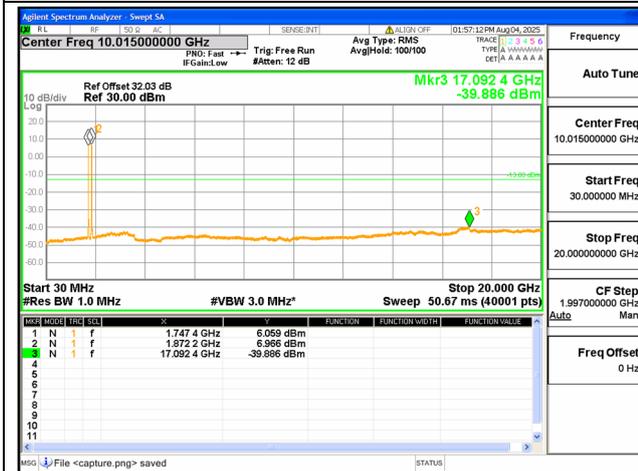
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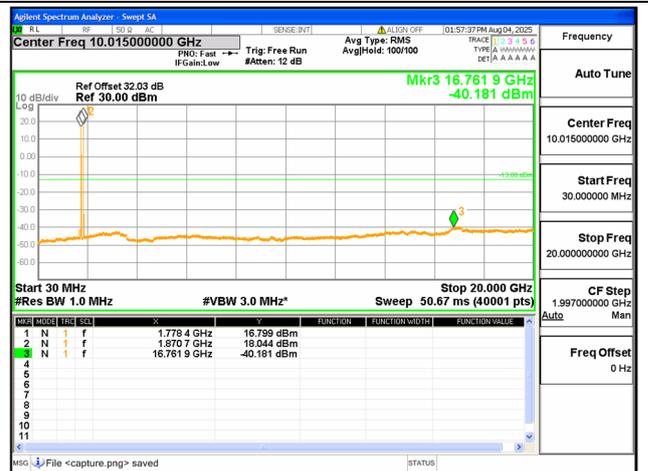
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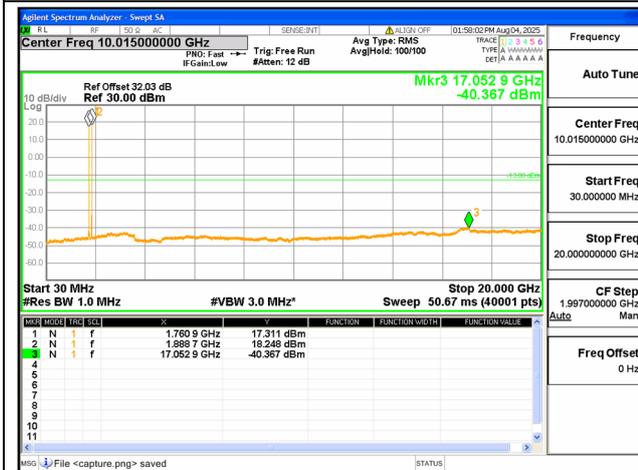
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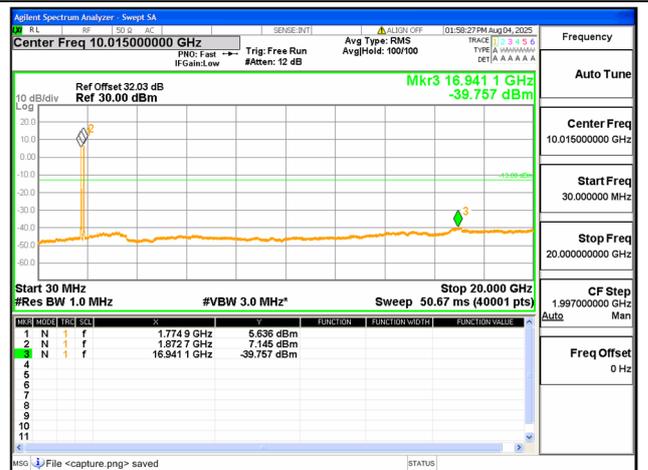
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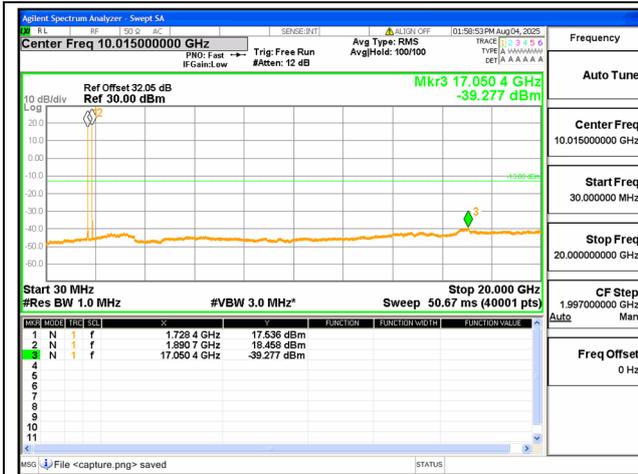
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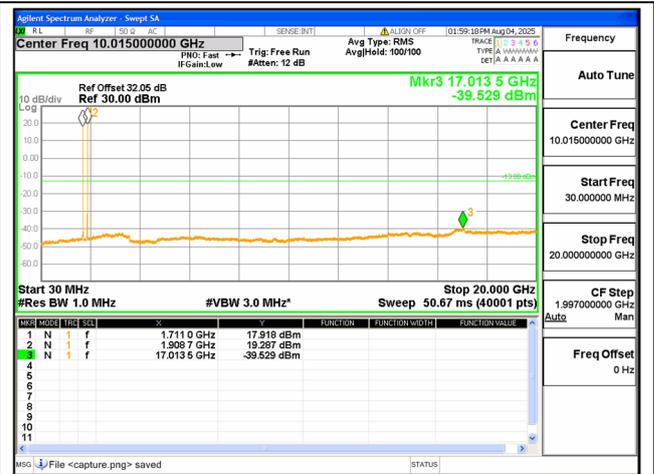
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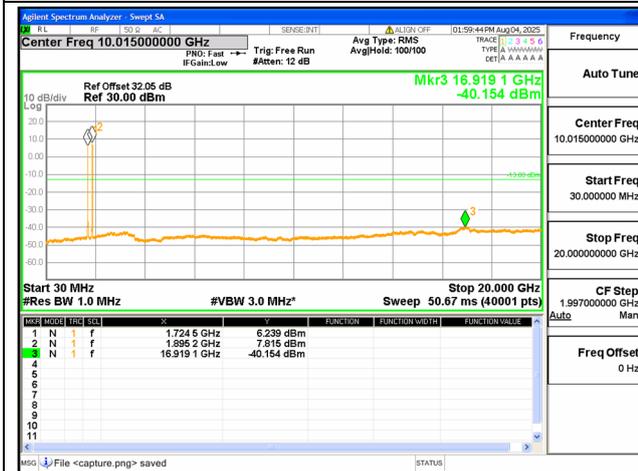
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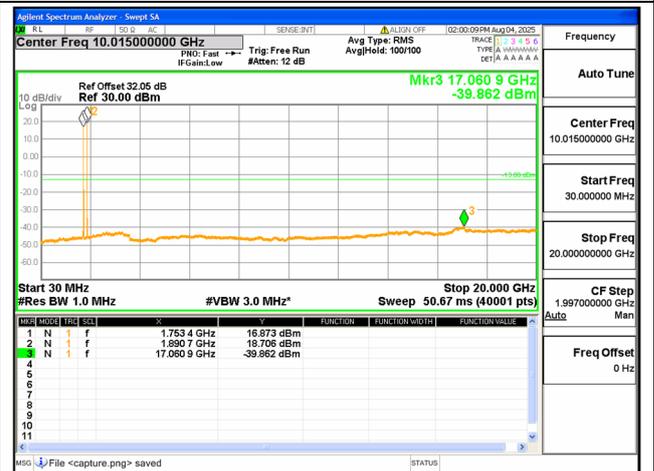
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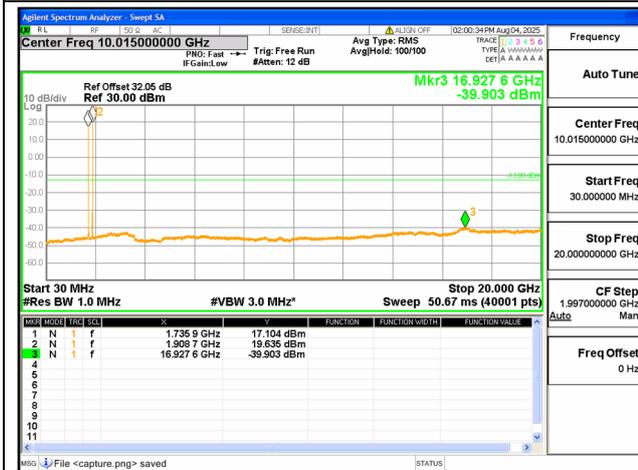
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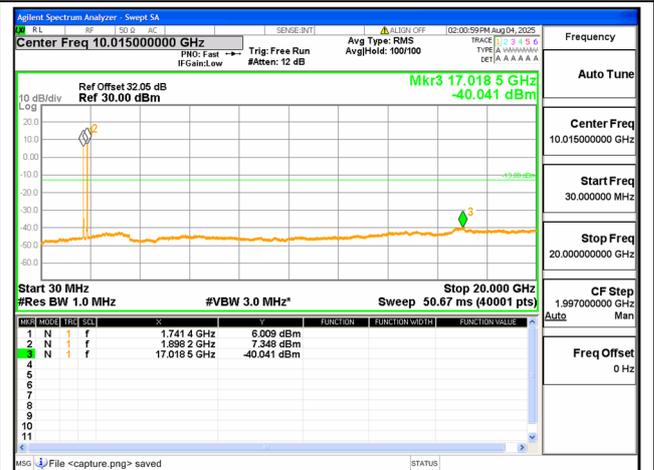
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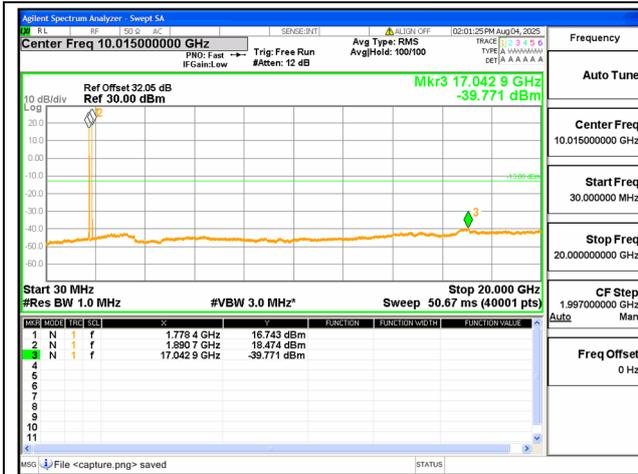
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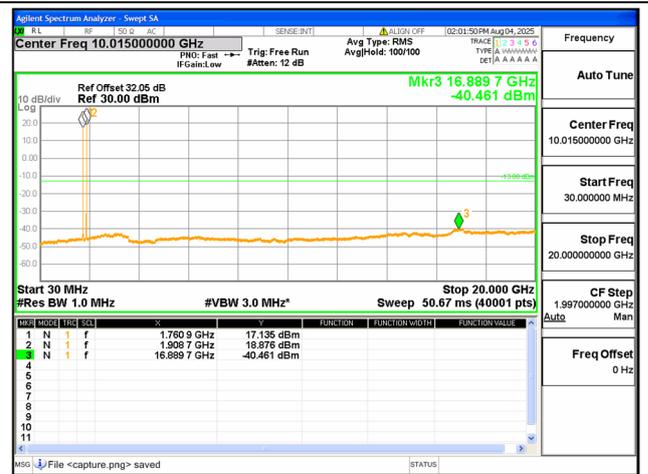
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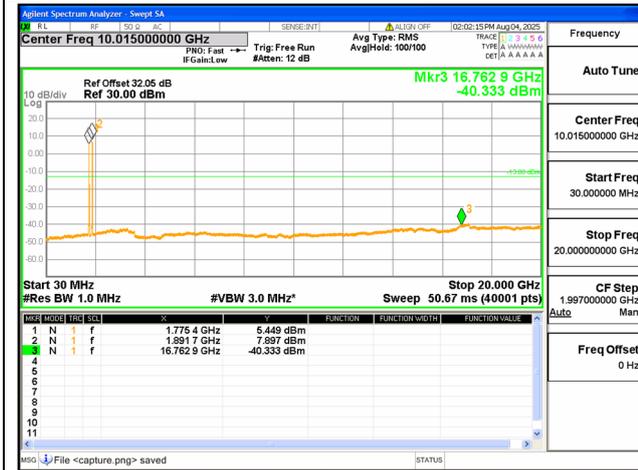
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2A-66A / 20+20MHz / QPSK / High+High CH /  
1@0-1@99



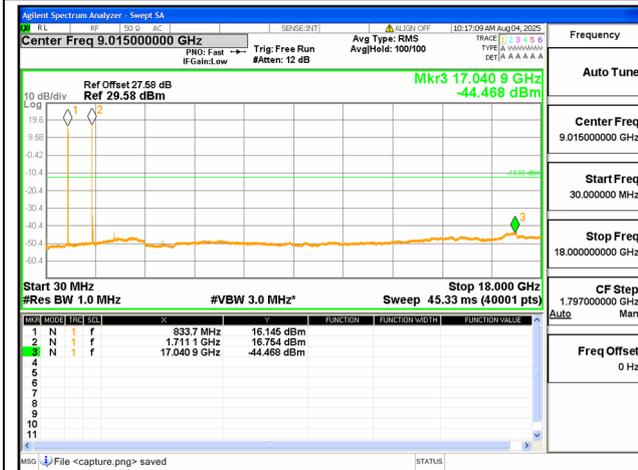
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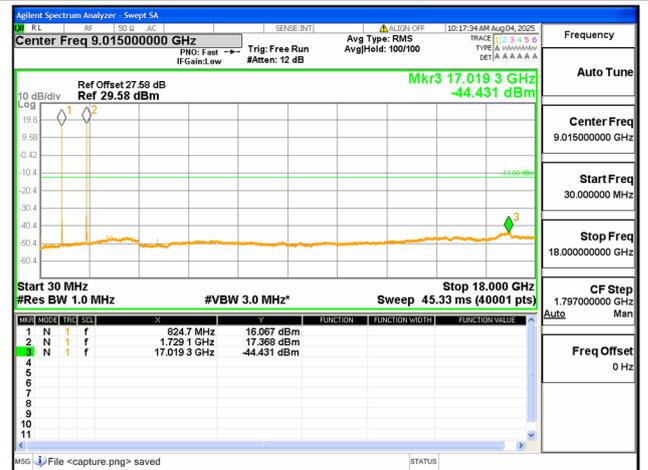
2A-66A / 20+20MHz / QPSK / High+High CH /  
100@0-100@0



4A-5A / 20+10MHz / QPSK / Low+Low CH /  
1@99-1@0

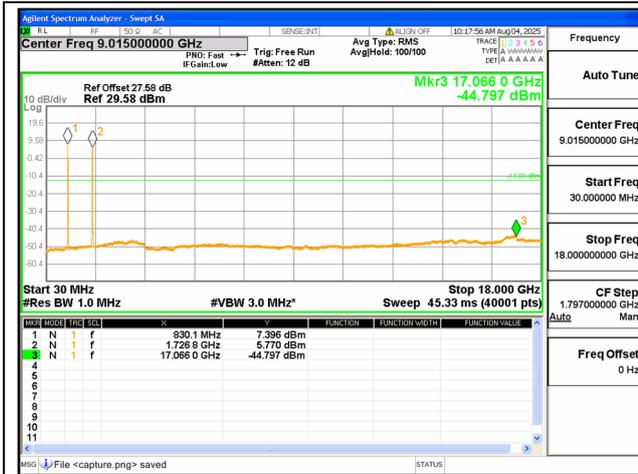


4A-5A / 20+10MHz / QPSK / Low+Low CH /  
1@0-1@49



4A-5A / 20+10MHz / QPSK / Low+Low CH /  
1@99-1@0





4A-5A / 20+10MHz / QPSK / Low+Low CH / 100@0-50@0



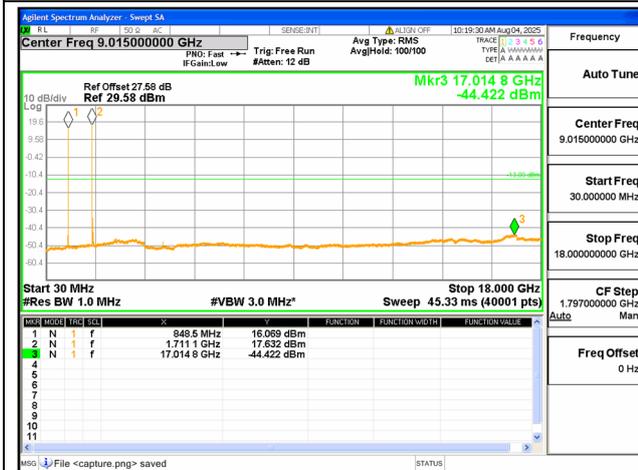
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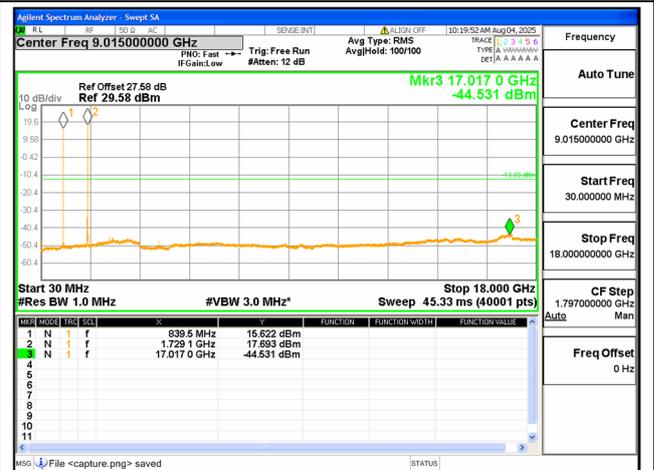
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4A-5A / 20+10MHz / QPSK / Low+Mid CH / 100@0-50@0



4A-5A / 20+10MHz / QPSK / Low+High CH / 1@0-1@49



4A-5A / 20+10MHz / QPSK / Low+High CH / 1@99-1@0