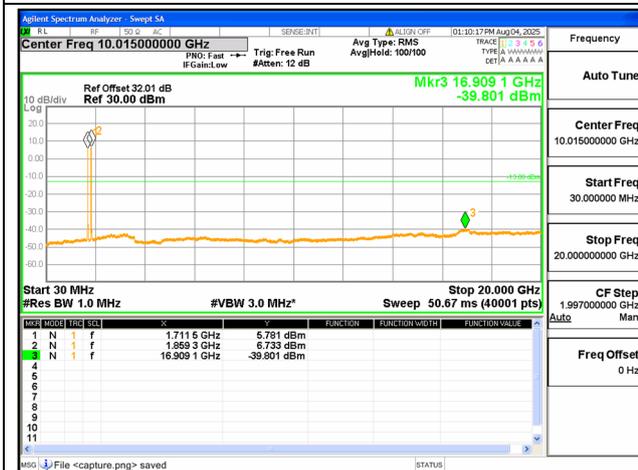


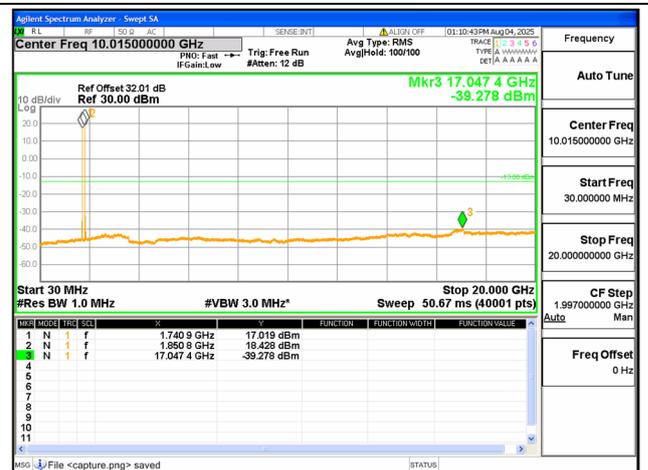
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1@0-1@99



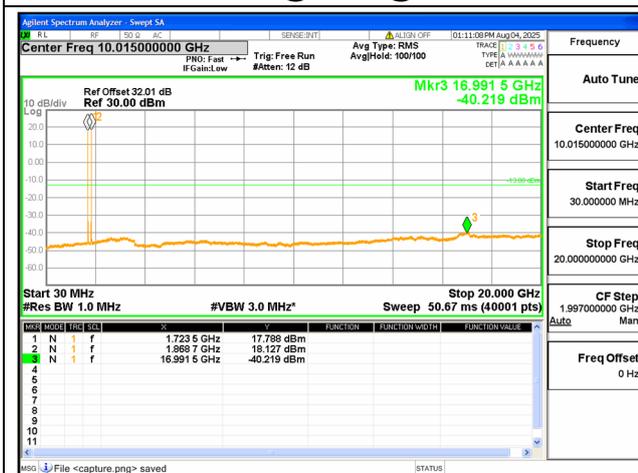
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1@99-1@0



2A-4A / 20+20MHz / QPSK / Low+Low CH /
100@0-100@0



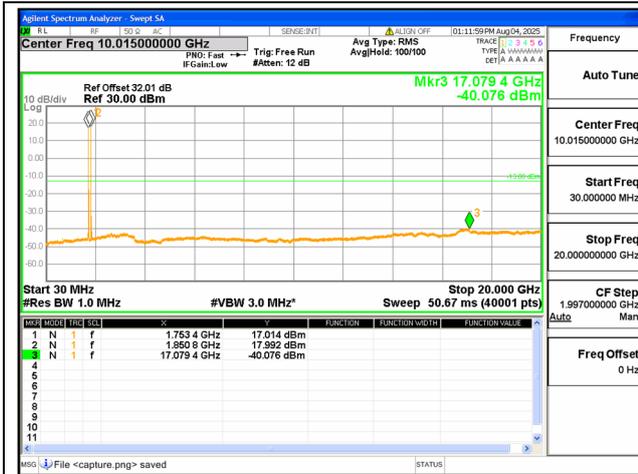
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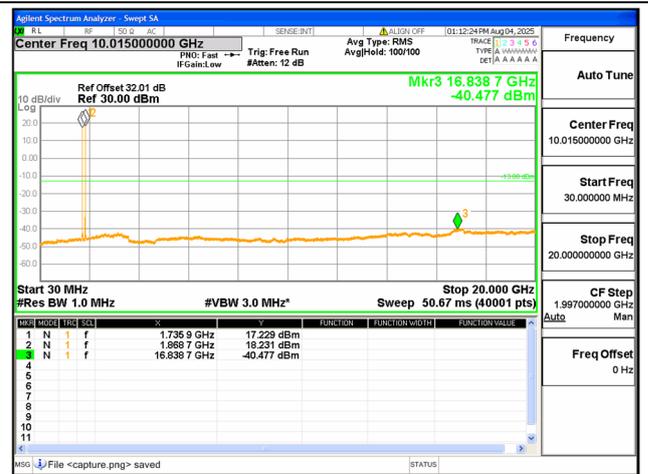
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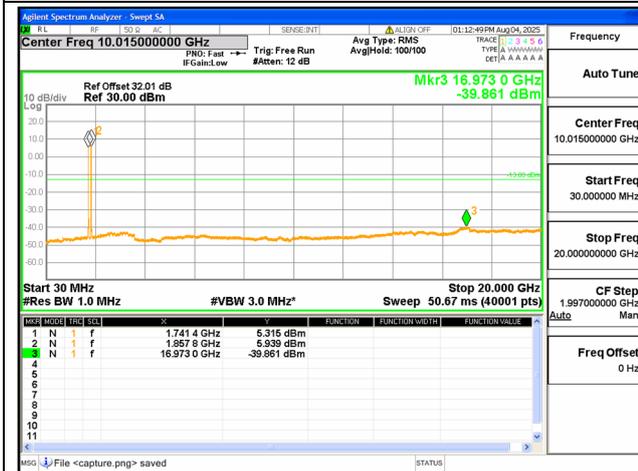
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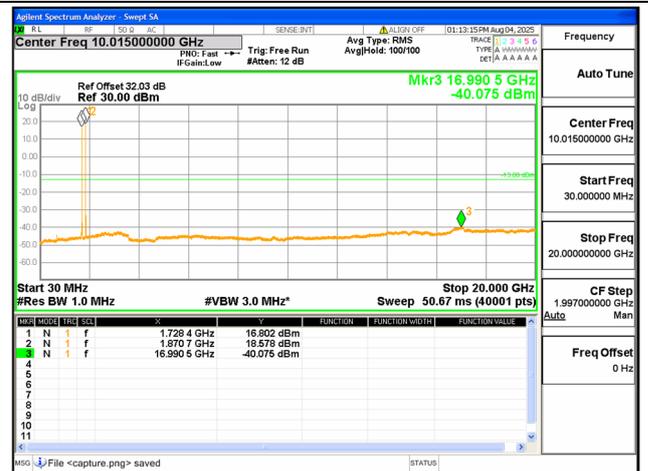
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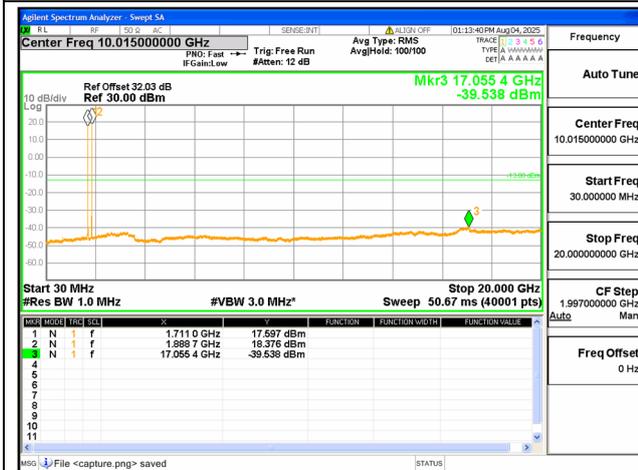
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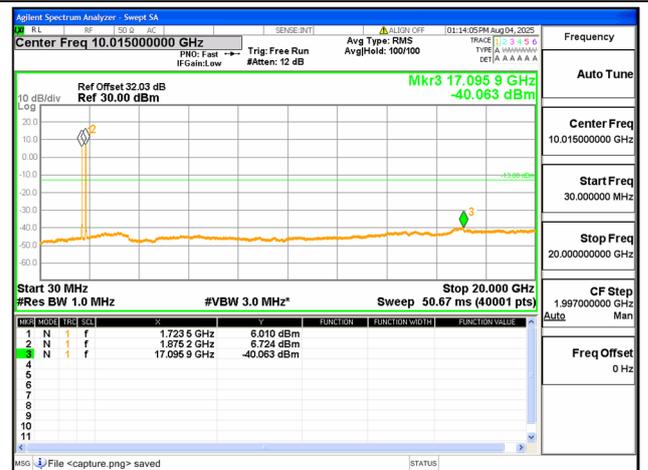
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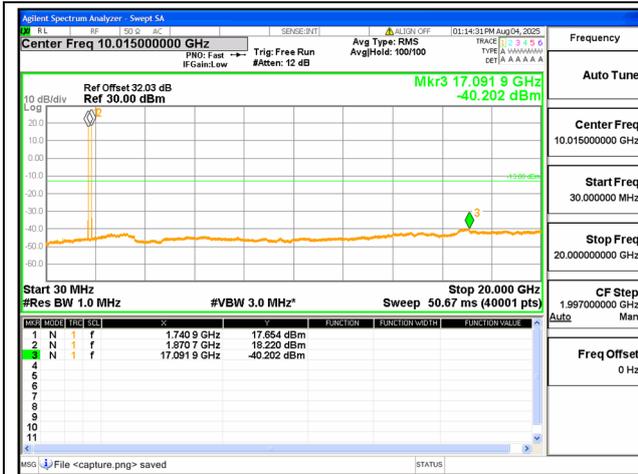
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2A-4A / 20+20MHz / QPSK / Mid+Low CH / 1@99-1@0



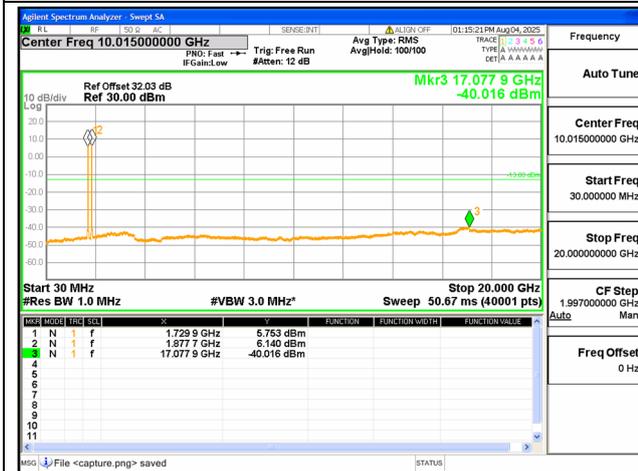
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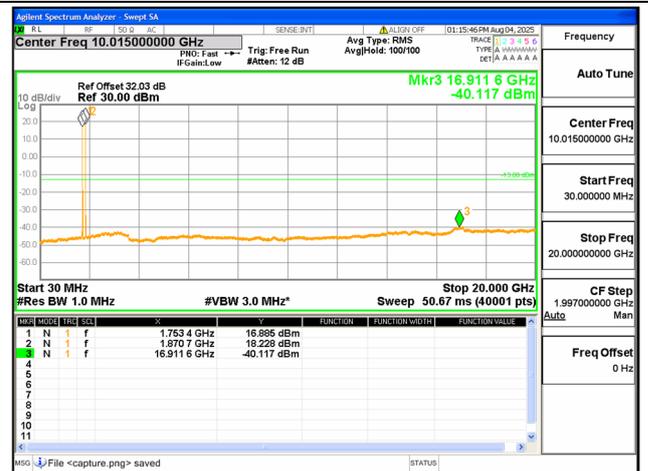
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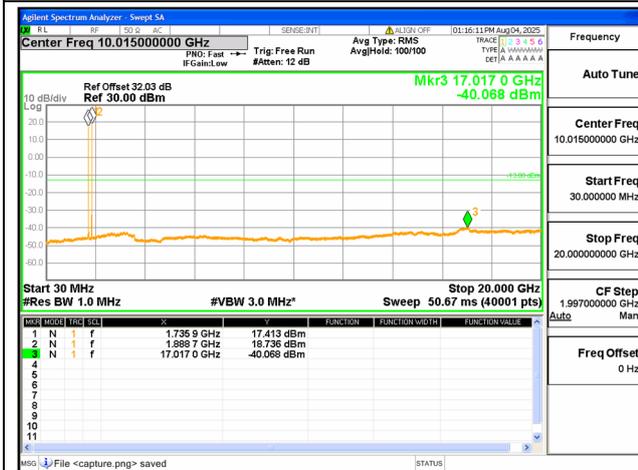
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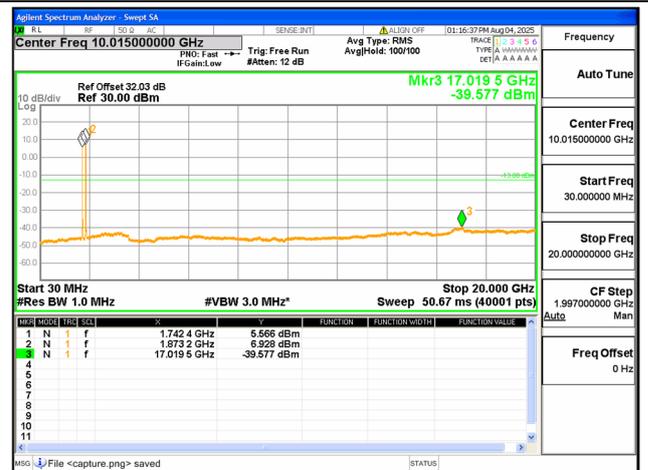
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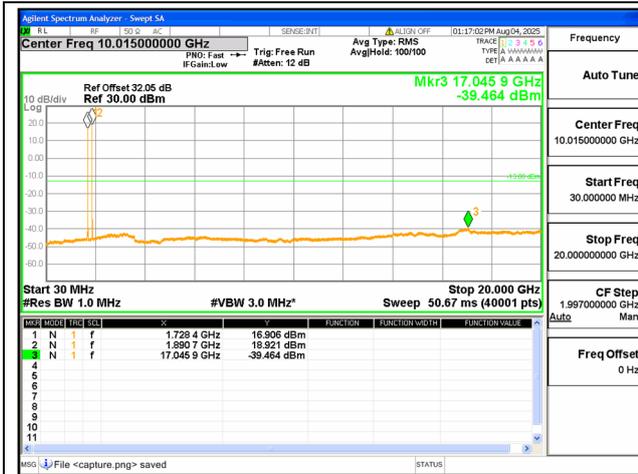
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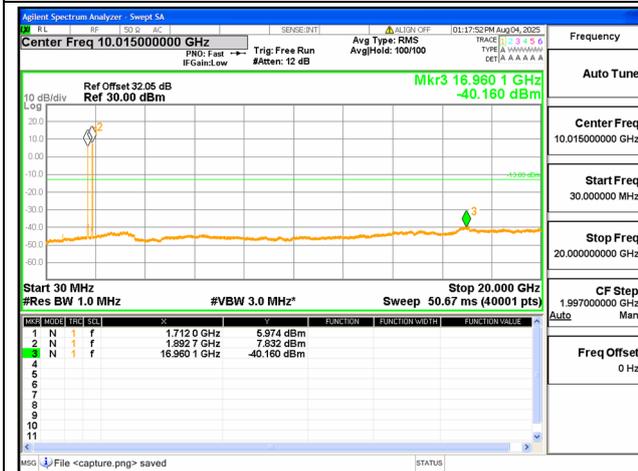
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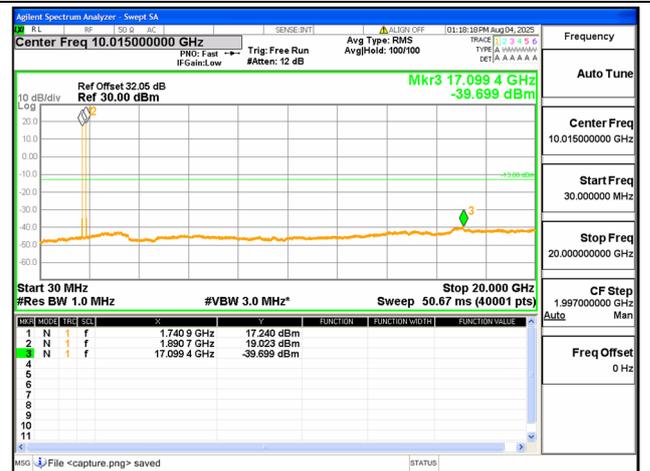
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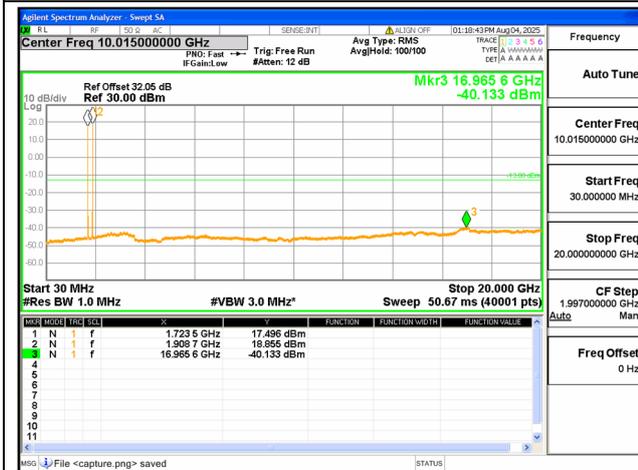
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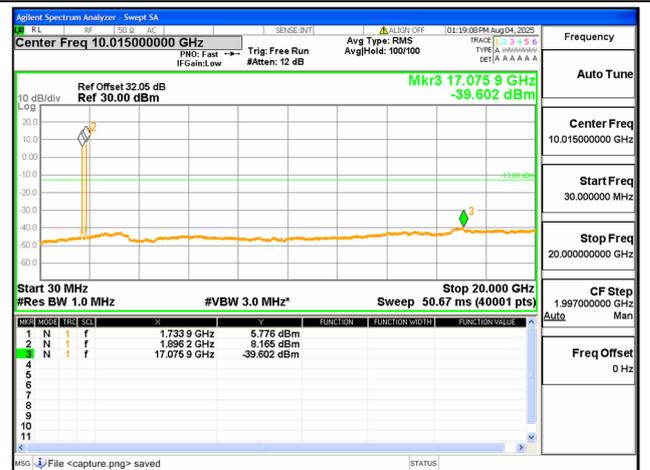
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2A-4A / 20+20MHz / QPSK / High+Mid CH / 1@0-1@99

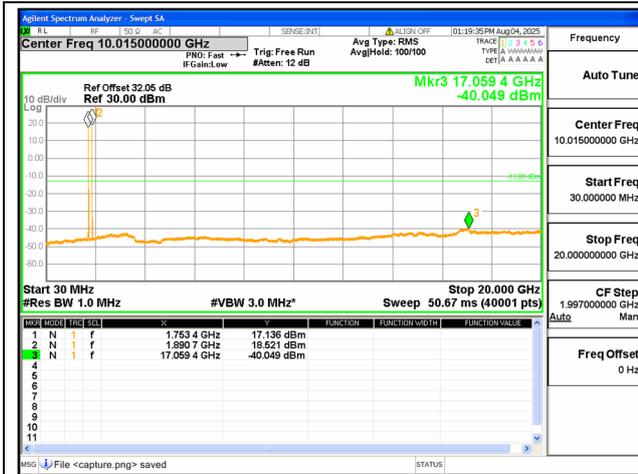


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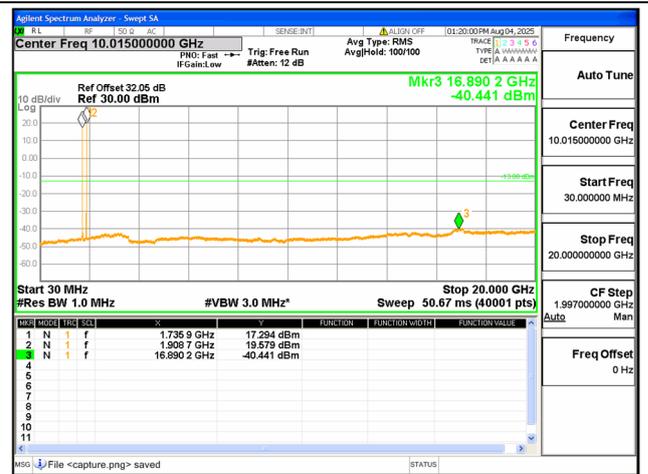


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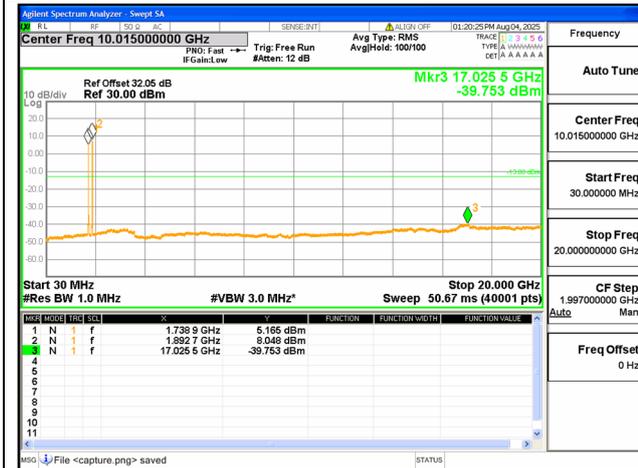




2A-4A / 20+20MHz / QPSK / High+High CH /
1@0-1@99



2A-4A / 20+20MHz / QPSK / High+High CH /
1@99-1@0



2A-4A / 20+20MHz / QPSK / High+High CH /
100@0-100@0



2A-5A / 20+10MHz / QPSK / Low+Low CH /
1@0-1@49



2A-5A / 20+10MHz / QPSK / Low+Low CH /
1@99-1@0