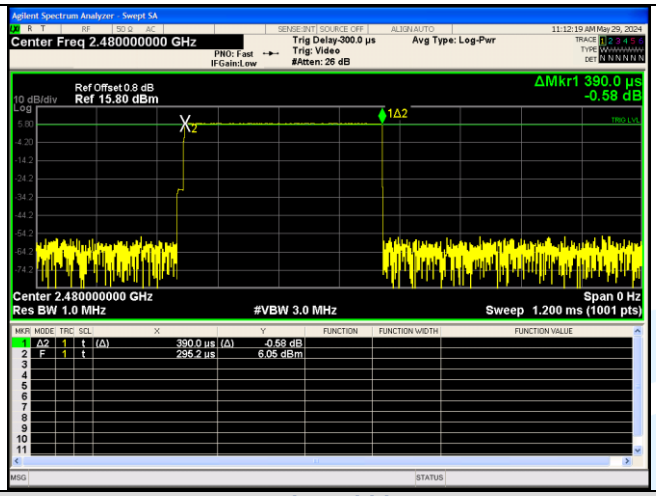
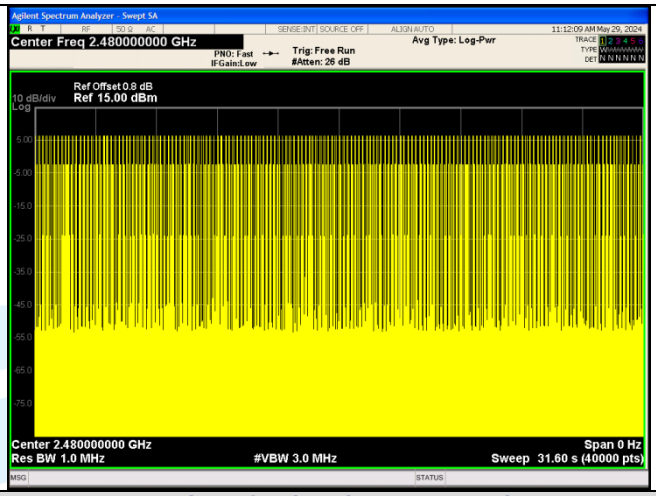
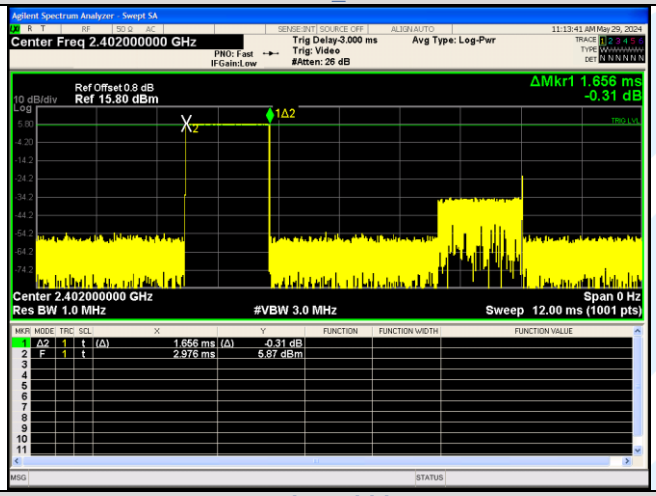


Pulse Width
GFSK_DH1



Number of Pulses in 31.6 seconds
GFSK_DH1



Pulse Width
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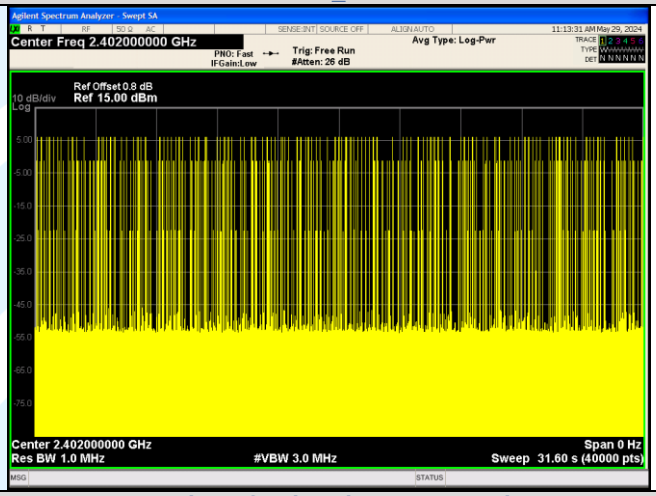
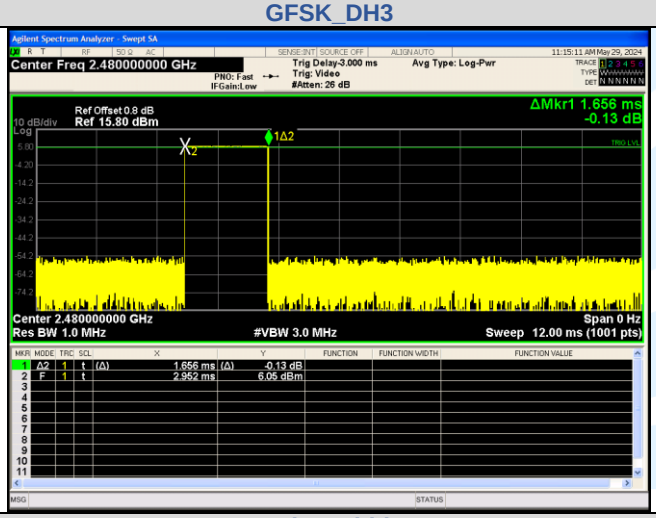
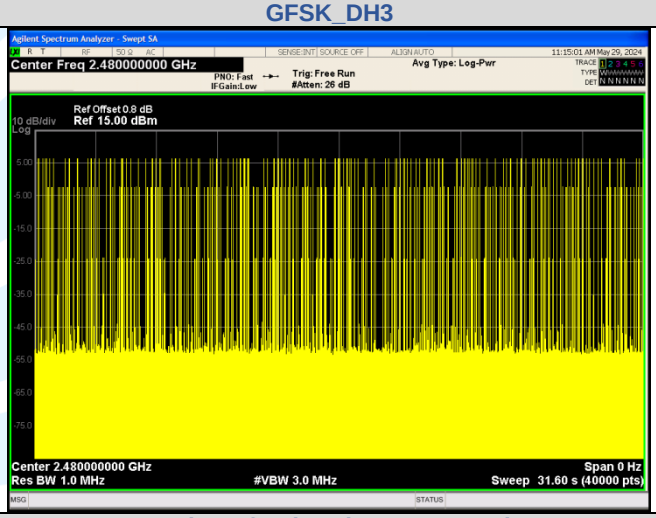


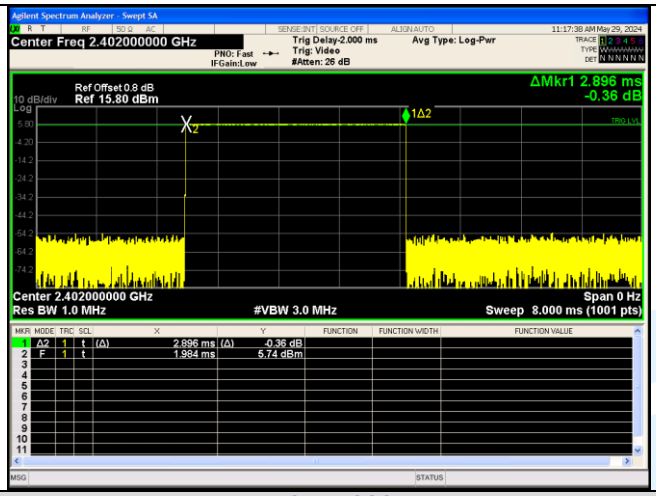
Number of Pulses in 31.6 seconds
GFSK_DH3



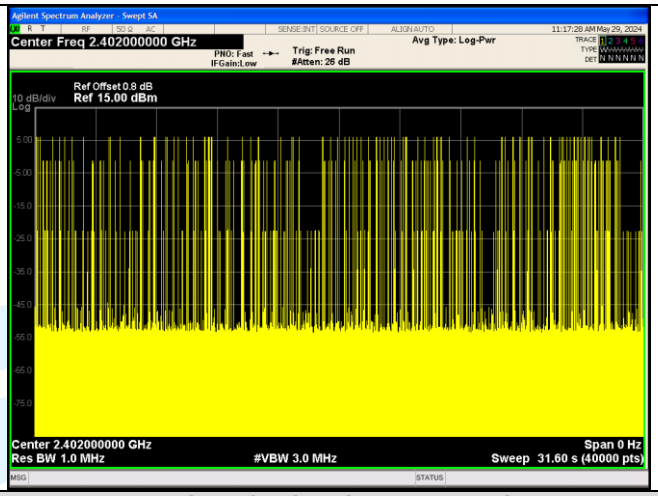
Pulse Width
GFSK_DH3



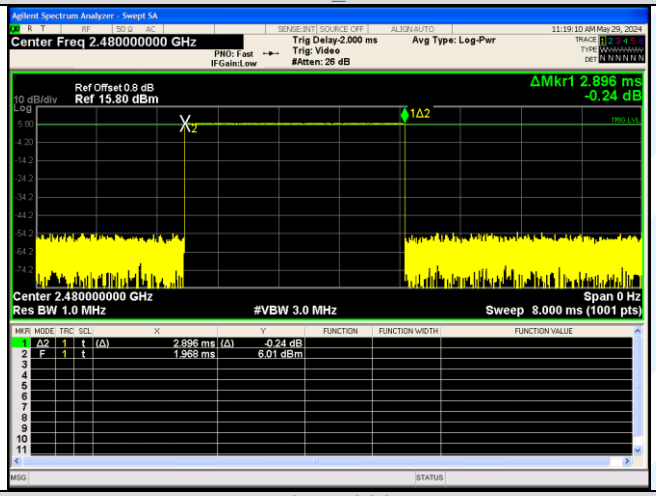
Number of Pulses in 31.6 seconds
GFSK_DH3



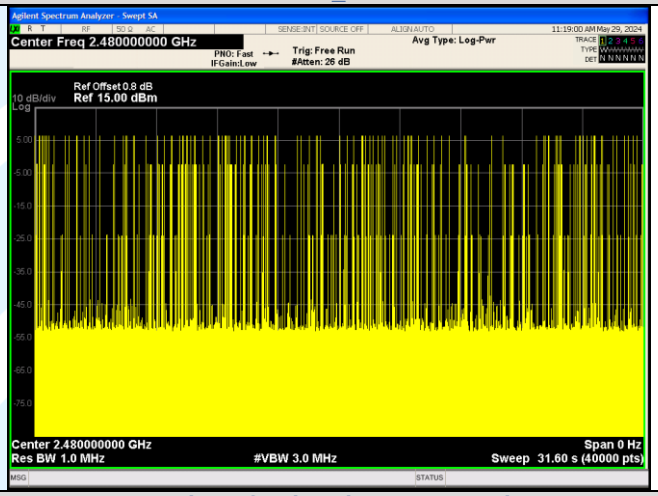
Pulse Width
GFSK_DH5



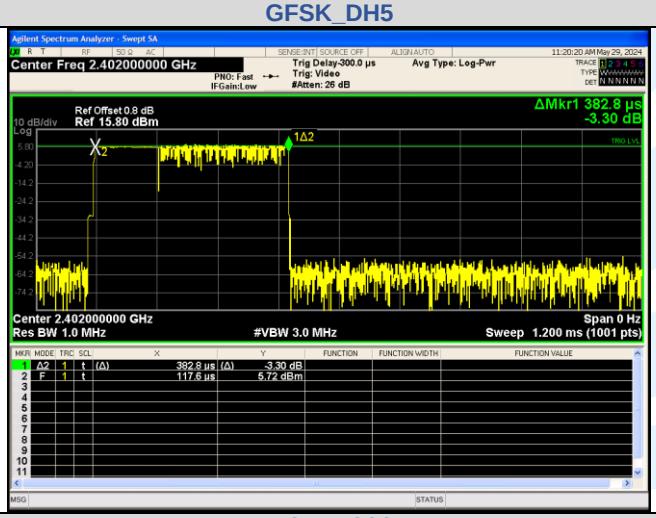
Number of Pulses in 31.6 seconds
GFSK_DH5



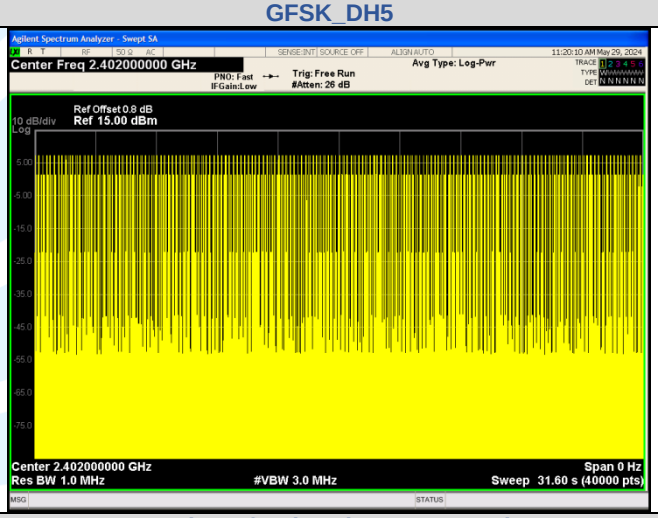
Pulse Width
GFSK_DH5



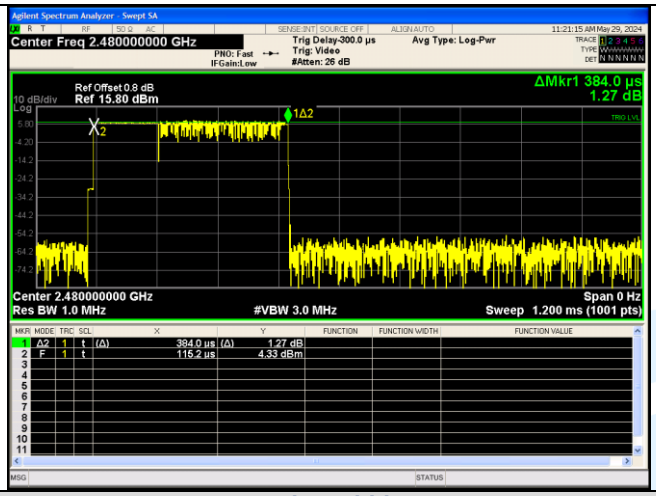
Number of Pulses in 31.6 seconds
GFSK_DH5



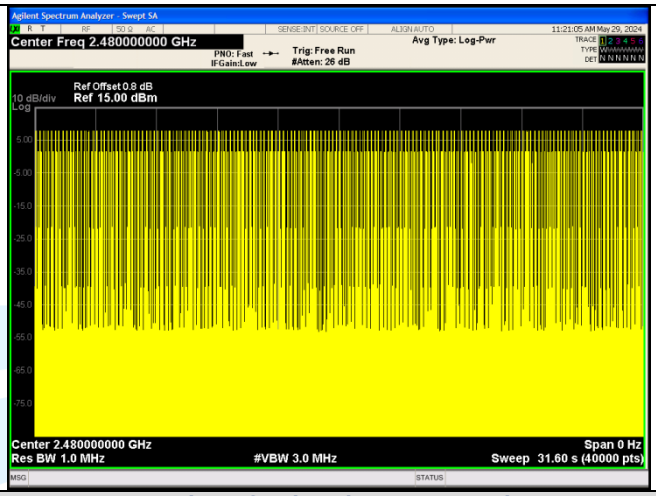
Pulse Width
π/4DQPSK_2-DH1



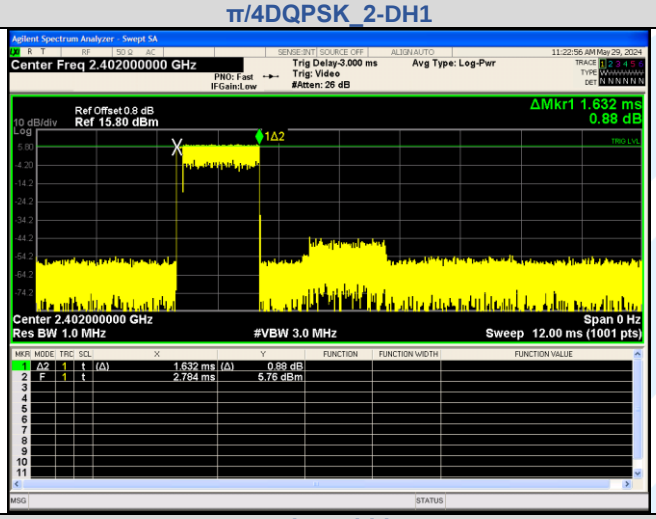
Number of Pulses in 31.6 seconds
π/4DQPSK_2-DH1



Pulse Width
π/4DQPSK_2-DH1



Number of Pulses in 31.6 seconds
π/4DQPSK_2-DH1



Pulse Width
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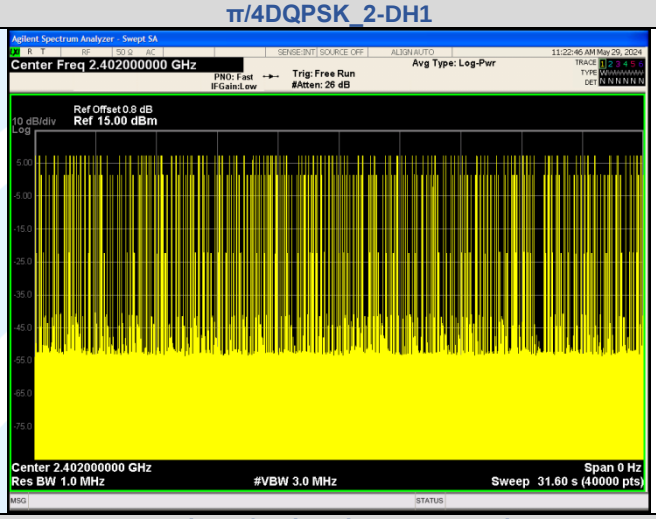
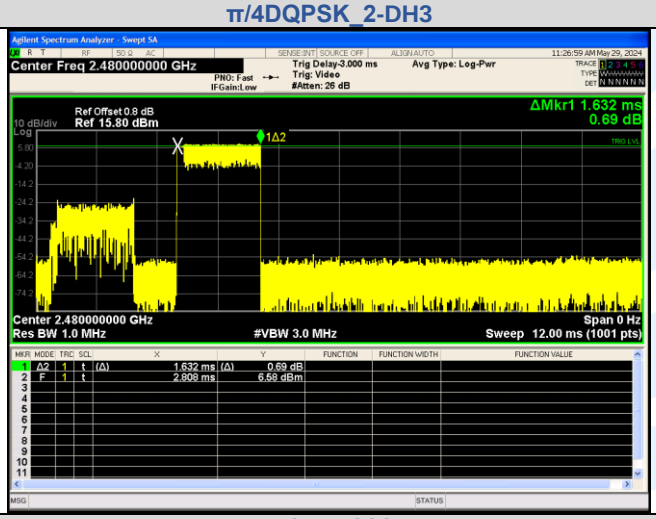
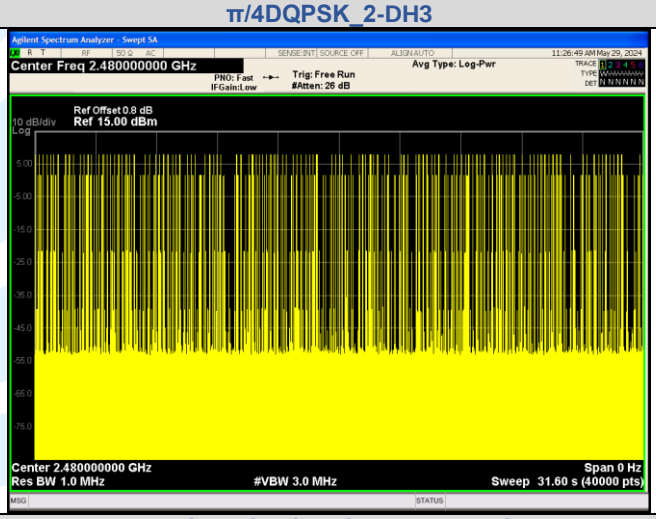


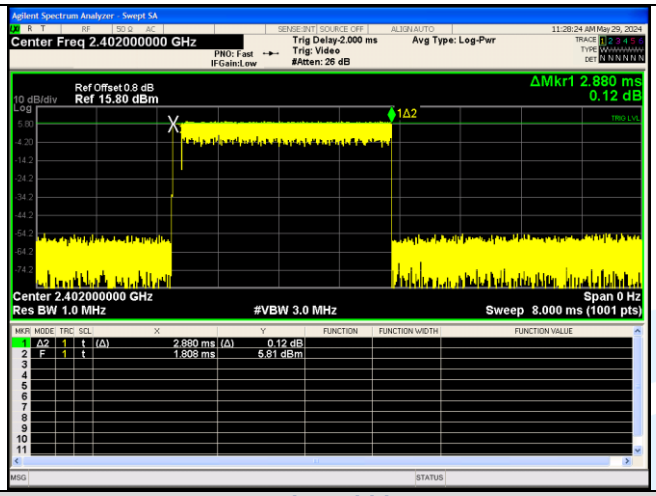
Number of Pulses in 31.6 seconds
π/4DQPSK_2-DH3



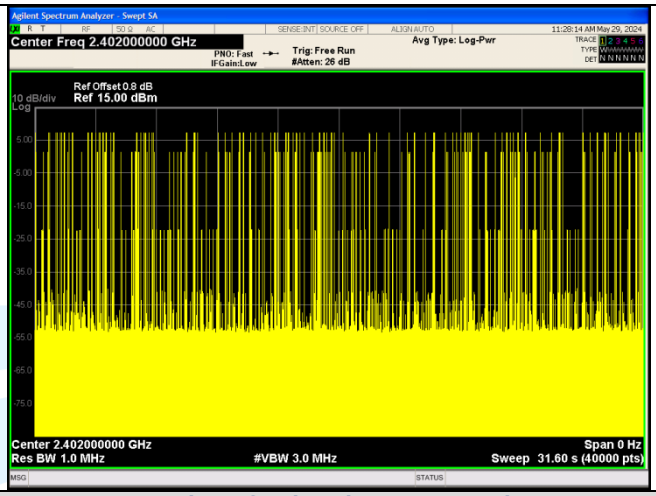
Pulse Width
π/4DQPSK_2-DH3



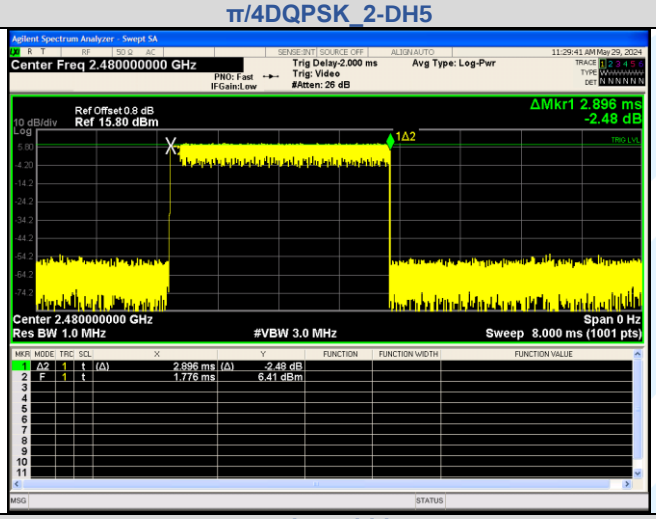
Number of Pulses in 31.6 seconds
π/4DQPSK_2-DH3



Pulse Width
π/4DQPSK_2-DH5



Number of Pulses in 31.6 seconds
π/4DQPSK_2-DH5



Pulse Width
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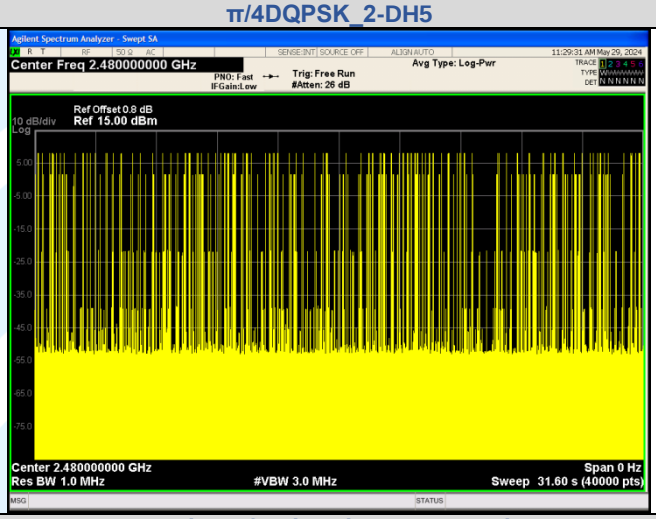

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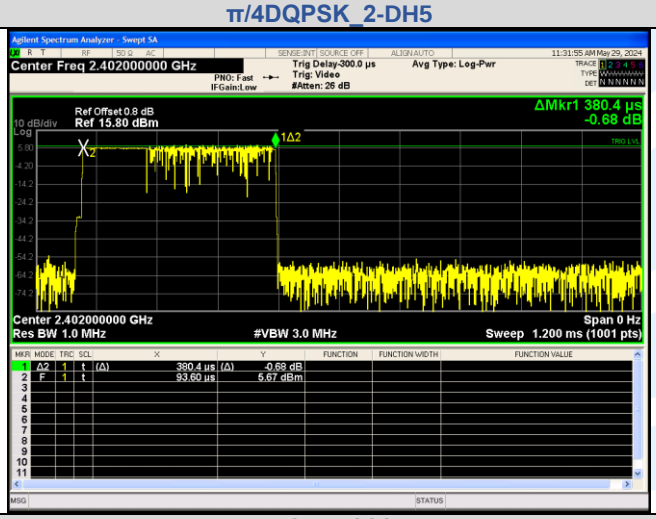
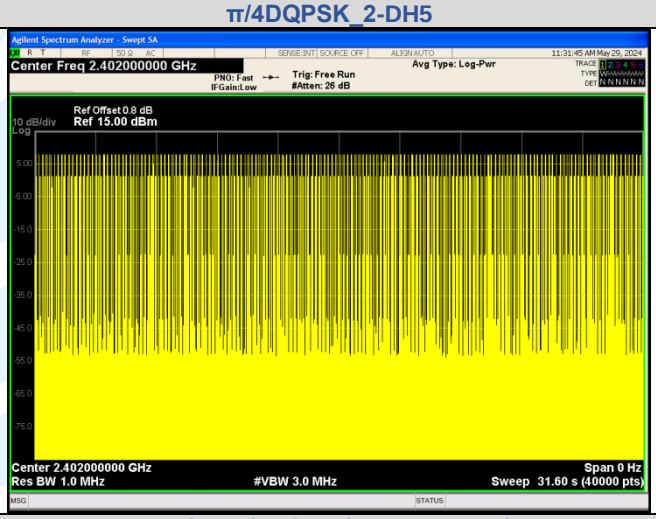
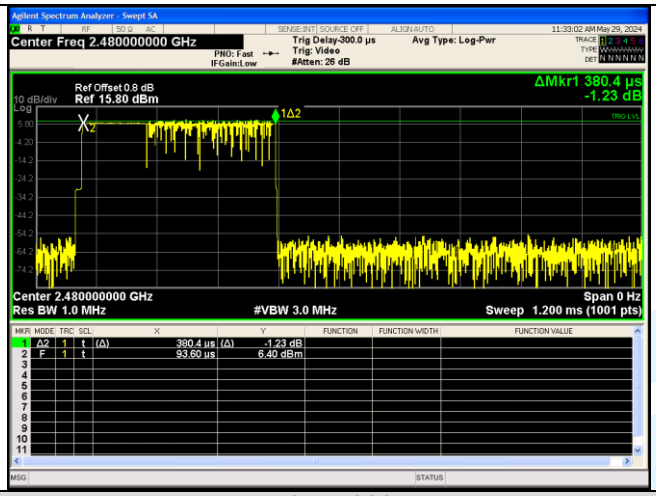


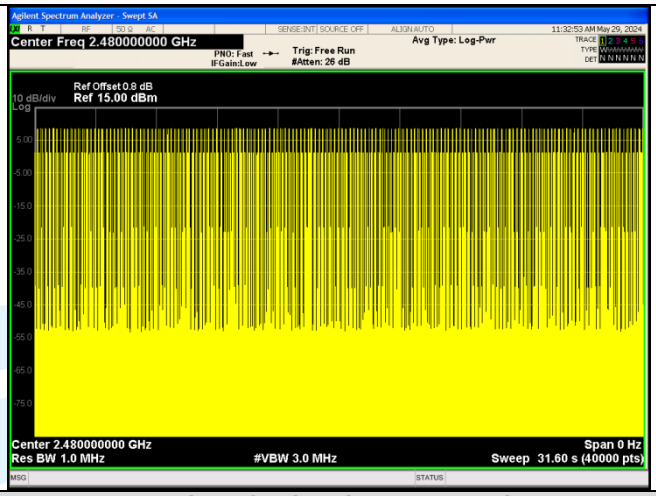
Pulse Width
8DPSK_3-DH1



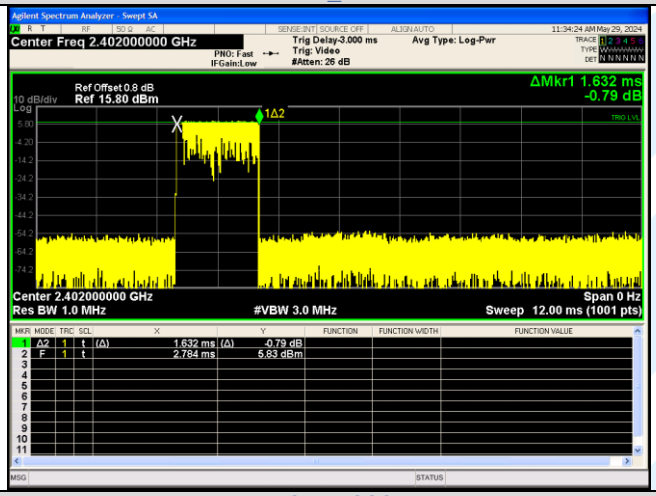
Number of Pulses in 31.6 seconds
8DPSK_3-DH1



Pulse Width
8DPSK_3-DH1



Number of Pulses in 31.6 seconds
8DPSK_3-DH1



Pulse Width
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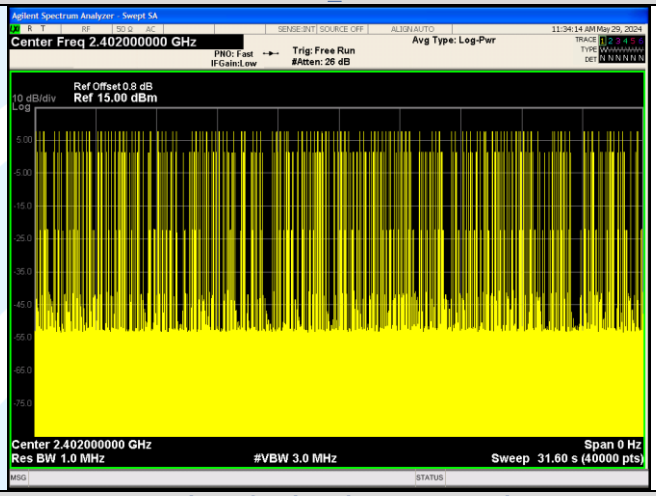
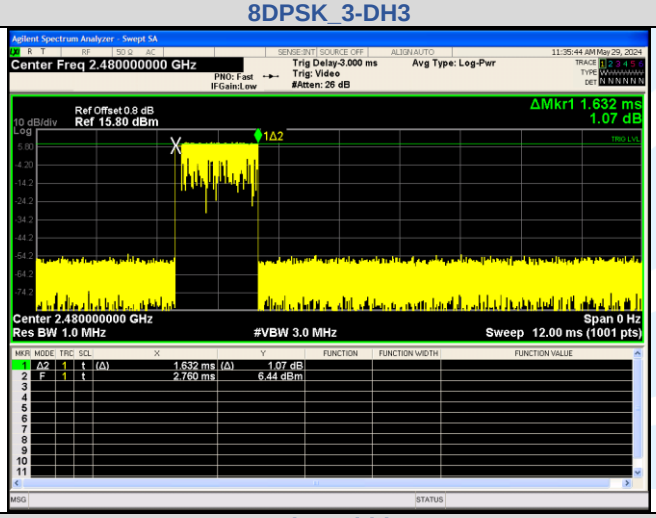


Number of Pulses in 31.6 seconds
8DPSK_3-DH3



Pulse Width
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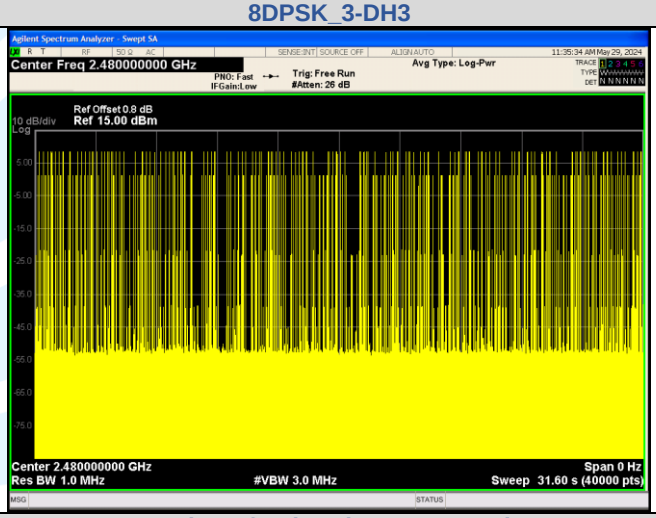
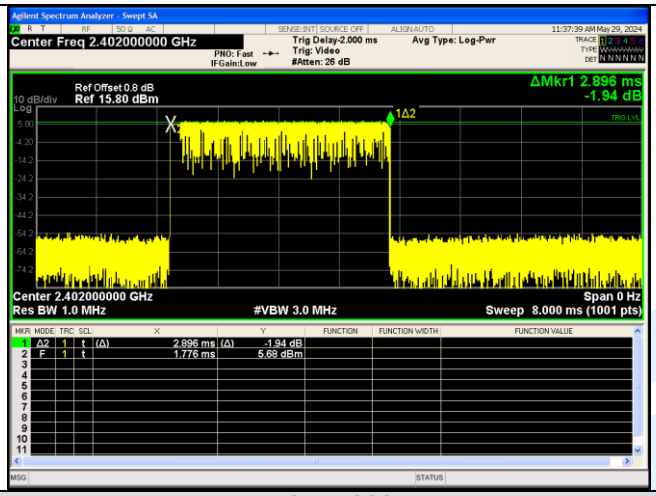
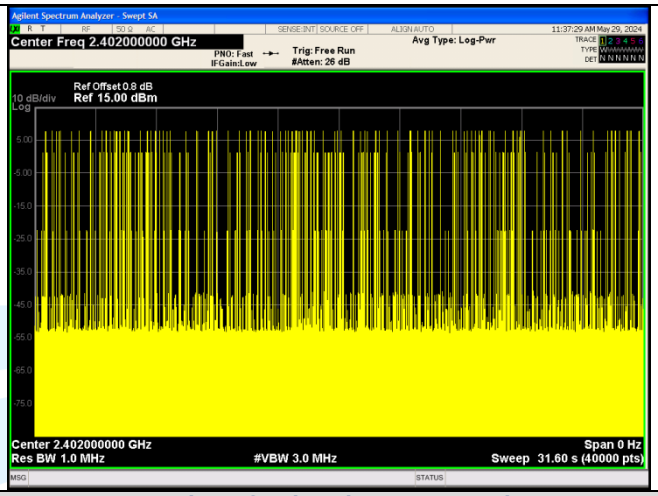


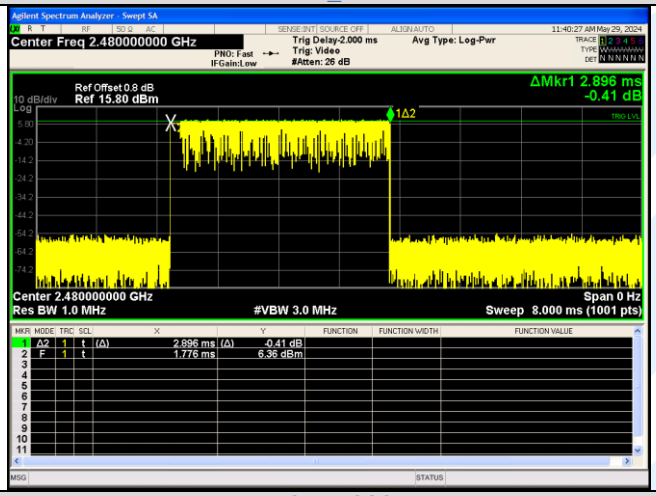
Number of Pulses in 31.6 seconds
8DPSK_3-DH3



Pulse Width
8DPSK_3-DH5



Number of Pulses in 31.6 seconds
8DPSK_3-DH5



Pulse Width
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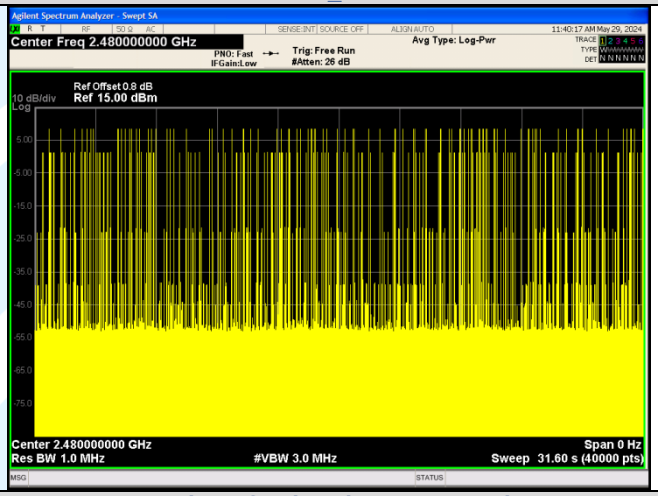
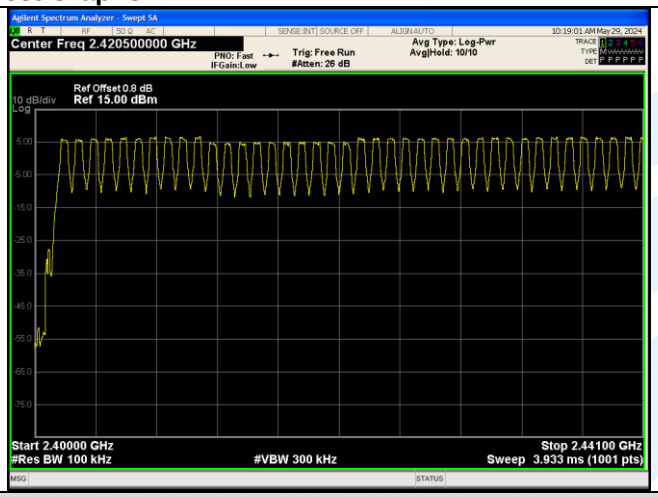


Number of Pulses in 31.6 seconds
8DPSK_3-DH5

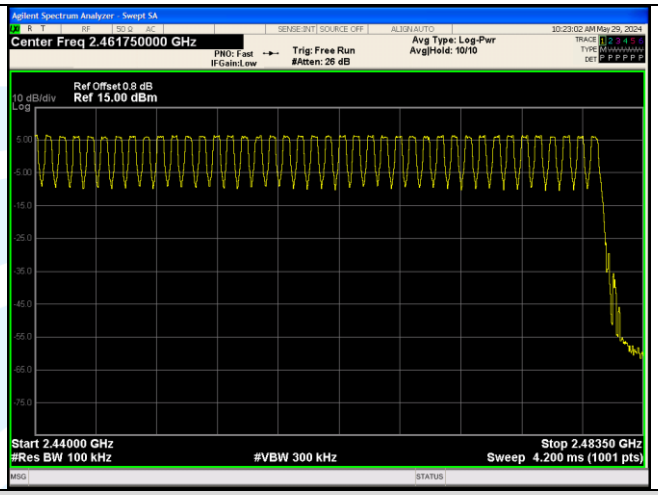
A.6 NUMBER OF HOPPING CHANNEL

Modulation	Packet	Number of Hopping Channel	Limit	Result
GFSK	DH5	79	15	PASS
π/4DQPSK	2-DH5	79	15	PASS
8DPSK	3-DH5	79	15	PASS

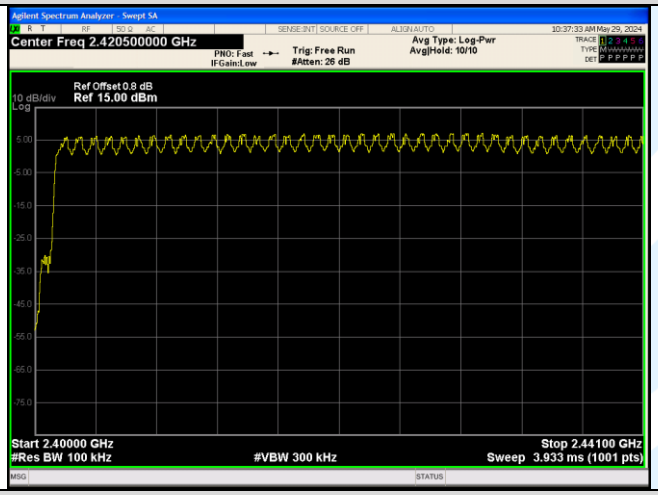
Test Graphs



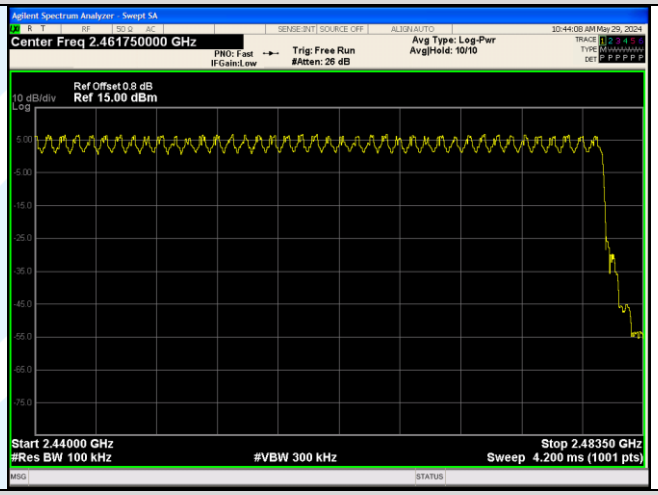
Low End Spectrum Channel Hopping Plot
GFSK



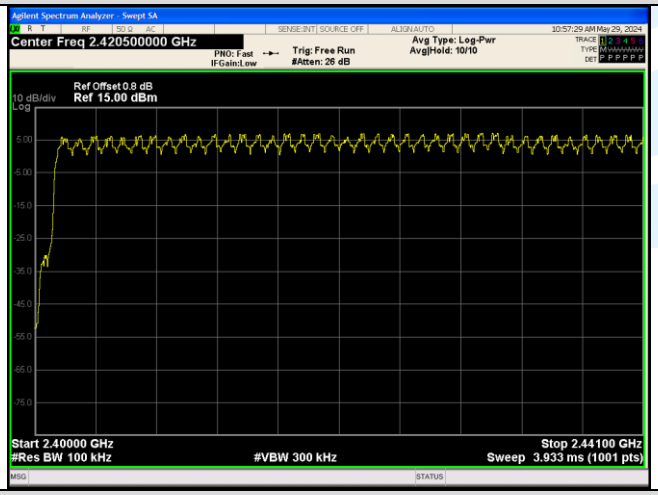
High End Spectrum Channel Hopping Plot
GFSK



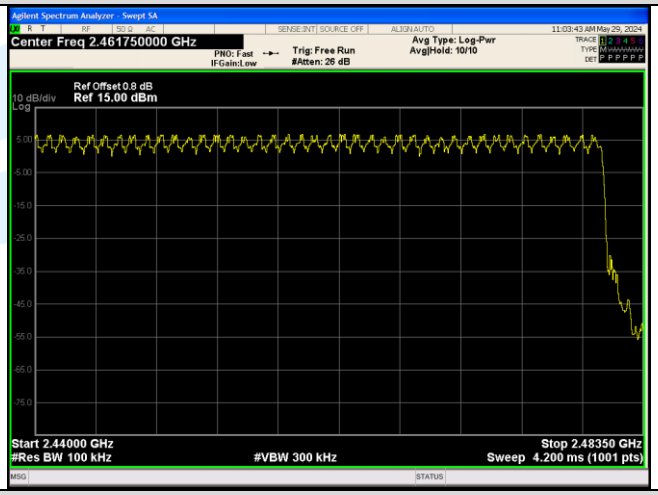
Low End Spectrum Channel Hopping Plot
π/4DQPSK



High End Spectrum Channel Hopping Plot
π/4DQPSK



Low End Spectrum Channel Hopping Plot
8DPSK



High End Spectrum Channel Hopping Plot
8DPSK

APPENDIX 1 PHOTOS OF TEST SETUP

See test photos attached in Appendix 1 for the actual connections between Product and support equipment.

APPENDIX 2 PHOTOS OF EUT CONSTRUCTIONAL DETAILS

Refer to Appendix 2 for EUT external and internal photos.

*** End of Report ***

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