Overview, Connector-Interface

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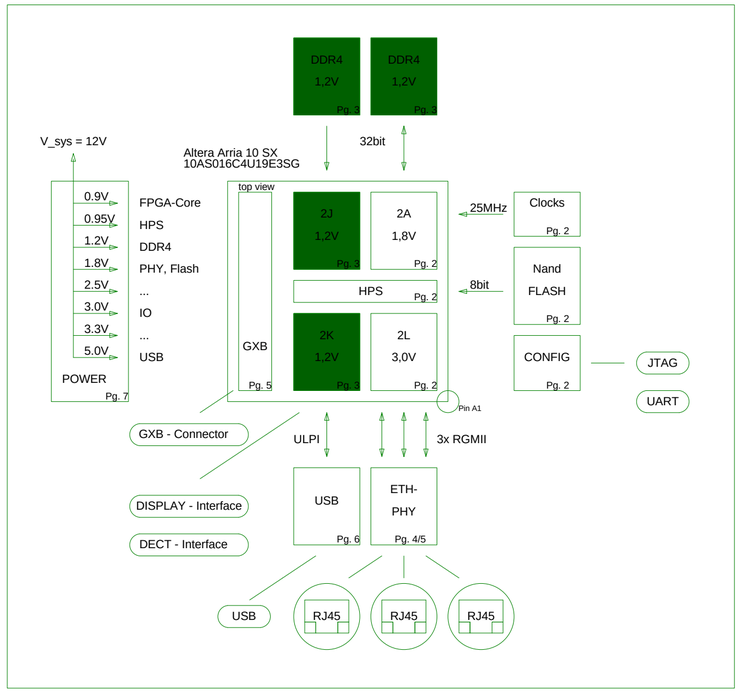
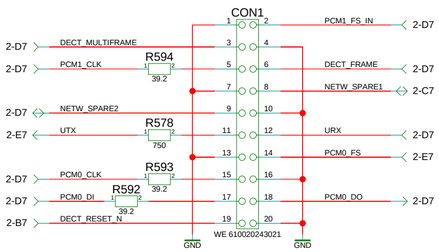
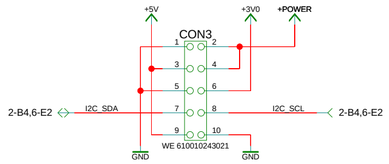


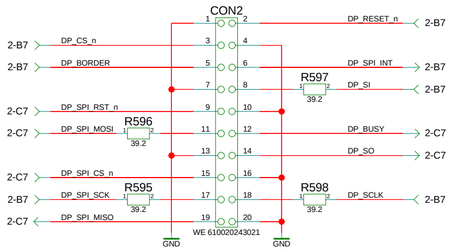
DECT-Interface (1V8)



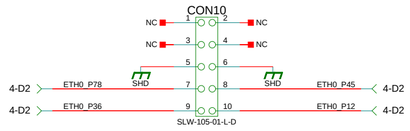
Local Power



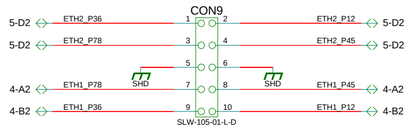
Display - Interface (3V0)



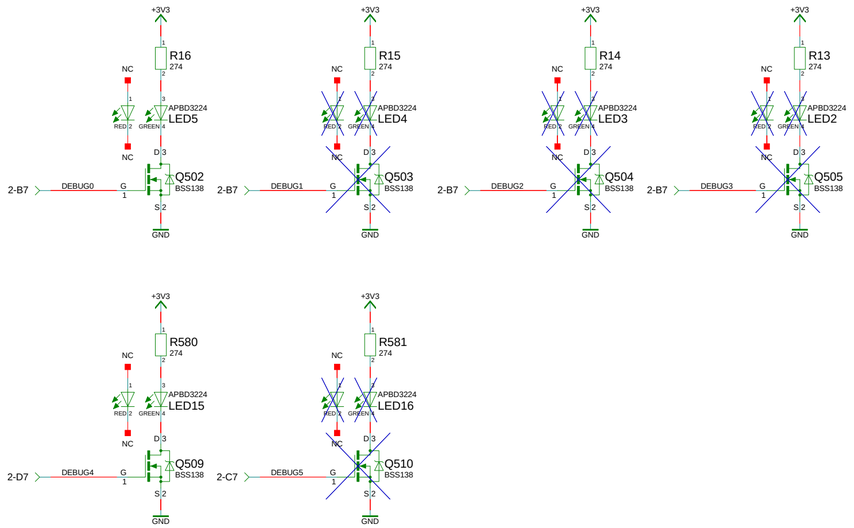
PoE



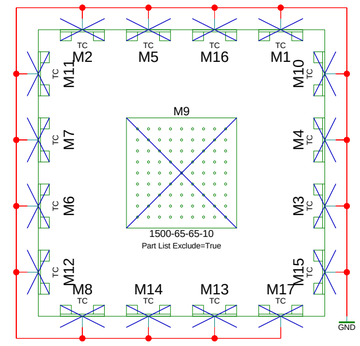
PoU (remote Power)



Debug LEDs



EMI - shield



TLB500
Datamatrix

MH1
M3-Testing-Slotted

MH3
M3-Testing

MH4
M3-Testing

Overview, Connector-Interface				
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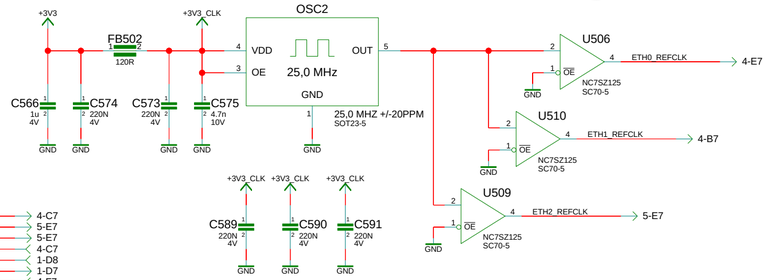
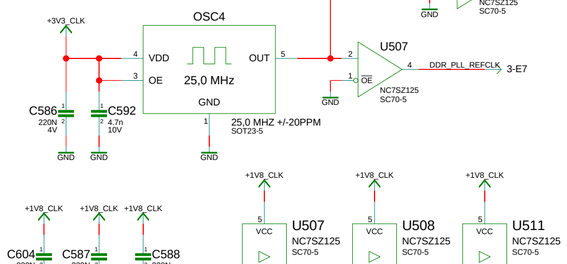
Clocking

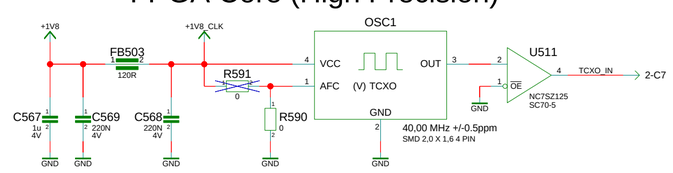
ETH Phys



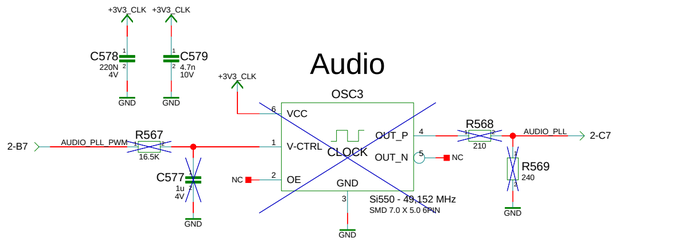
HPS & DDR



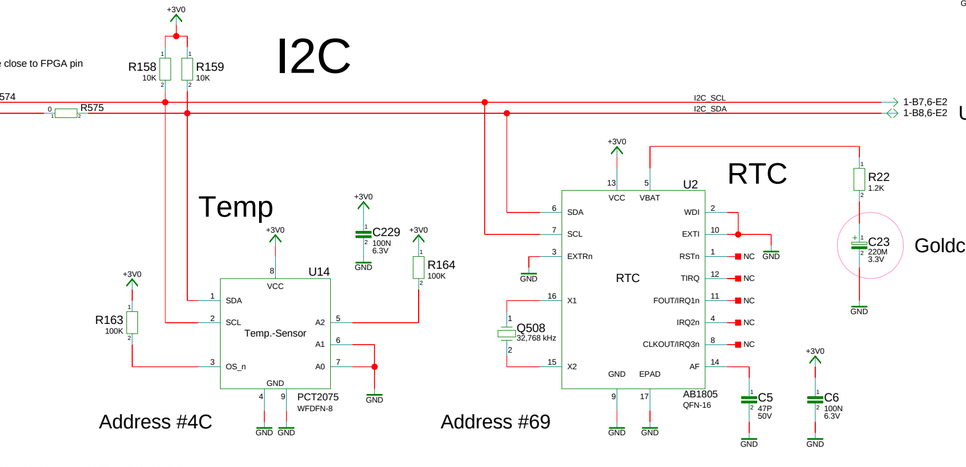
FPGA Core (High Precision)



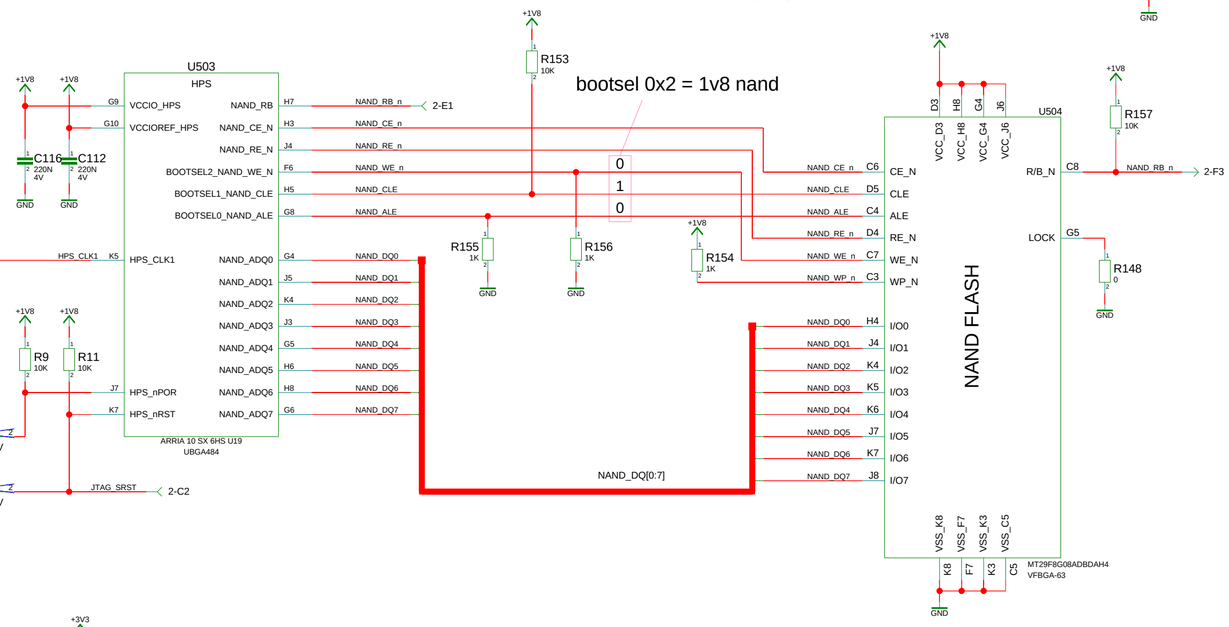
Audio



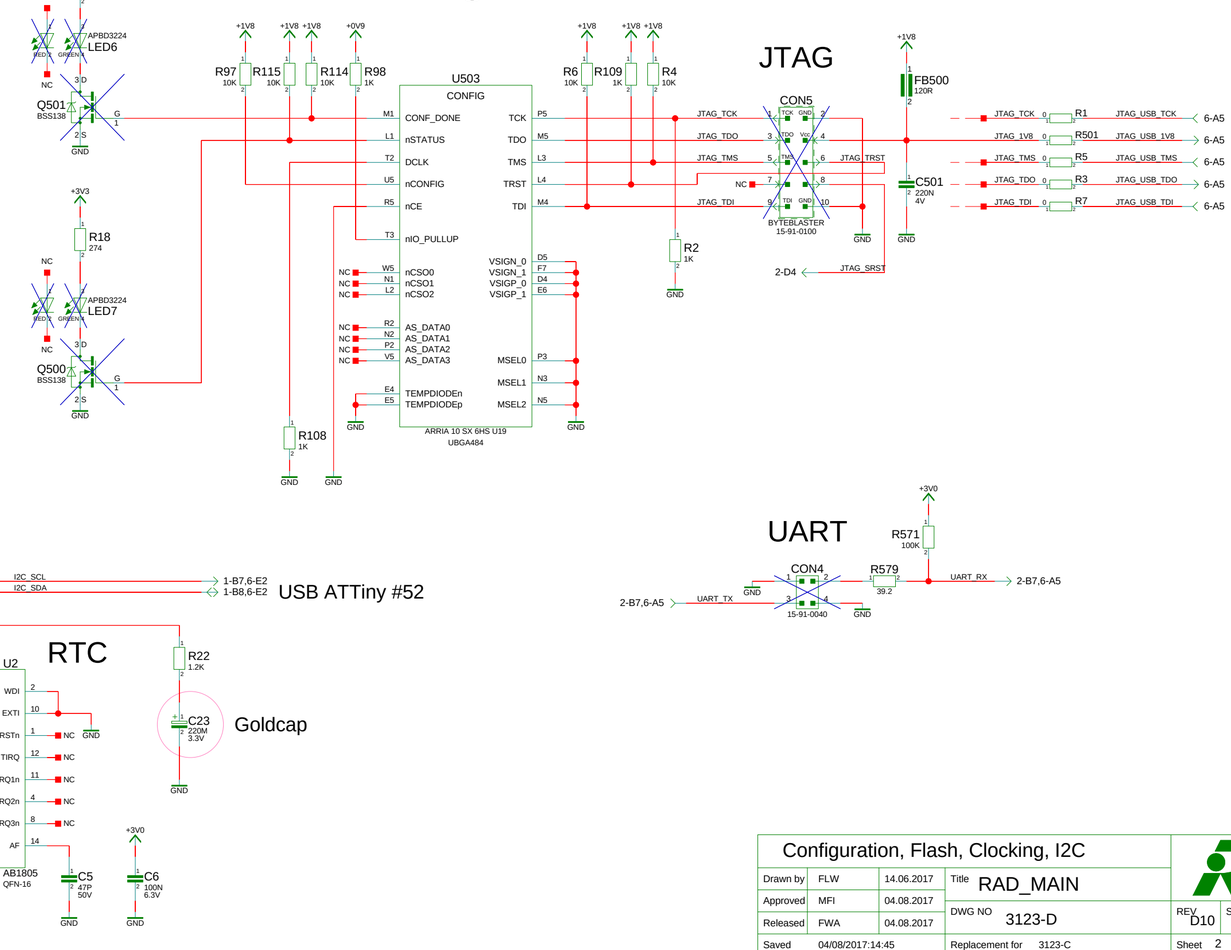
I2C



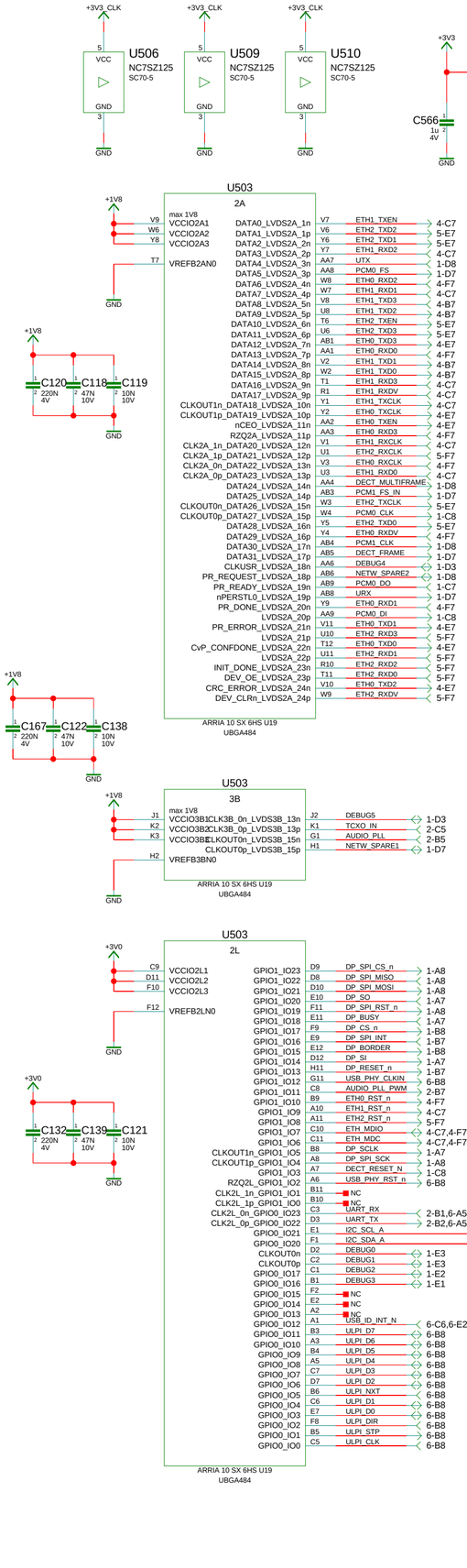
Flash



Configuration



Power



Configuration, Flash, Clocking, I2C			
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DDR4 SDRAM

DDR_DQ[0:31]

DDR_A[0:16]

DDR Commands

U503 — ARRIA 10 SX 6HS U19, UBGA484 (2J)

U502 — MT40A256M16, FBGA-96

U501 — MT40A256M16, FBGA-96

U4 — NCP51200MNTXG, SON-10 (DDR Termination Regulator)

DDR4 SDRAM					
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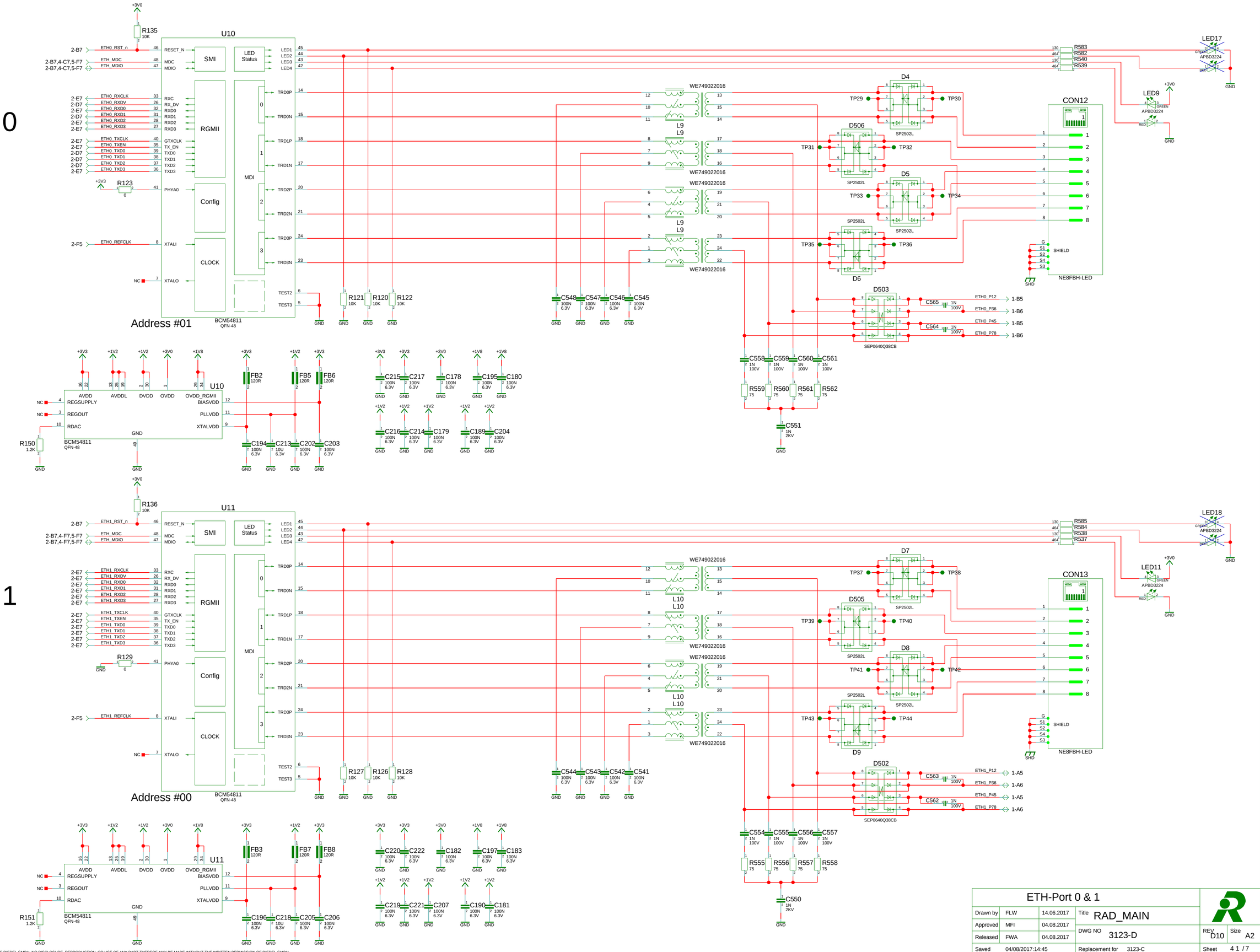
ETH-Port 0
IP



ETH-Port 1
ULLI A

ETH-Port 0 & 1				
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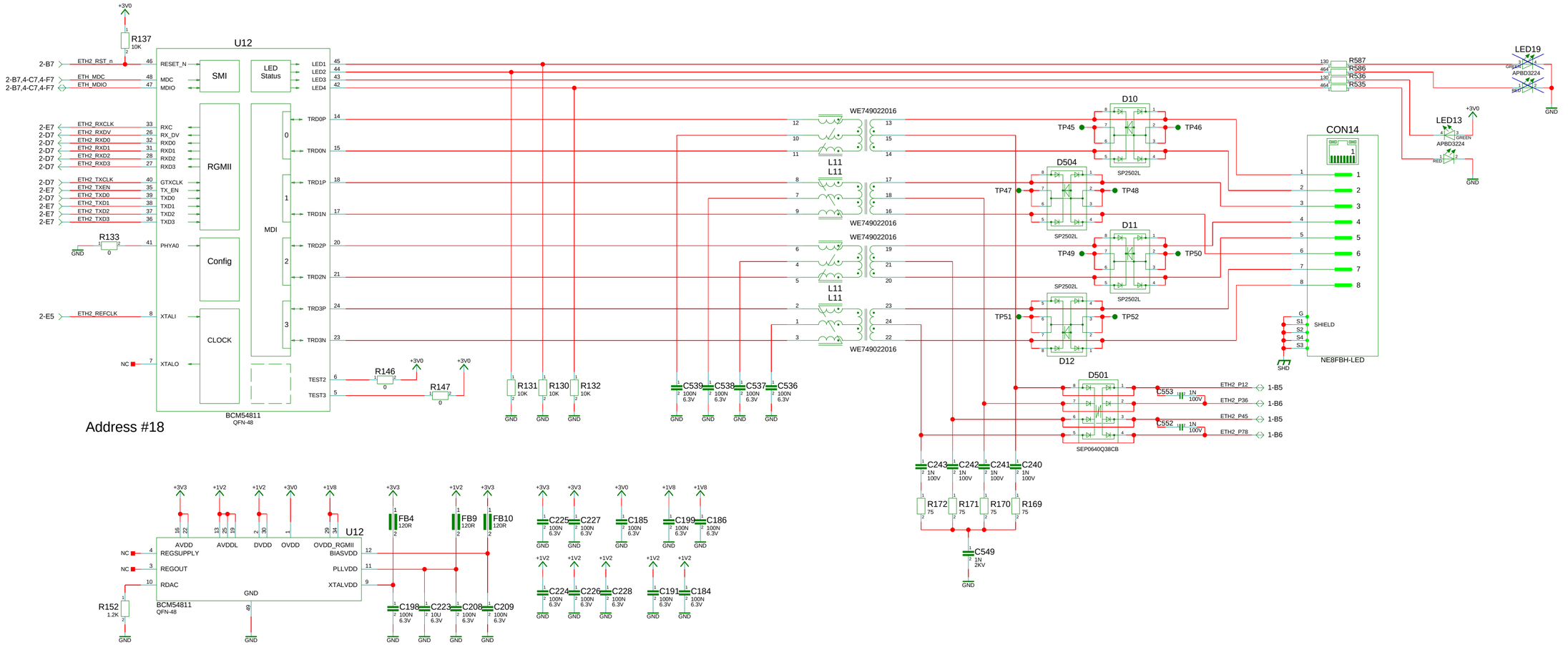
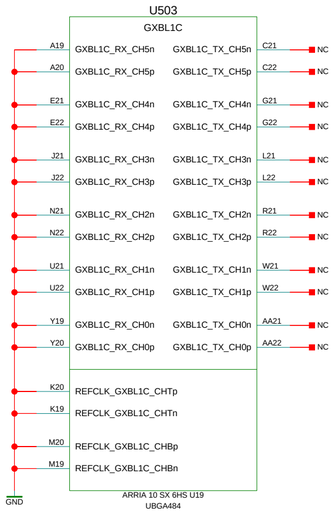
ETH-Port 2
ULLI B



GXB



ETH-Port 2, GXBs				
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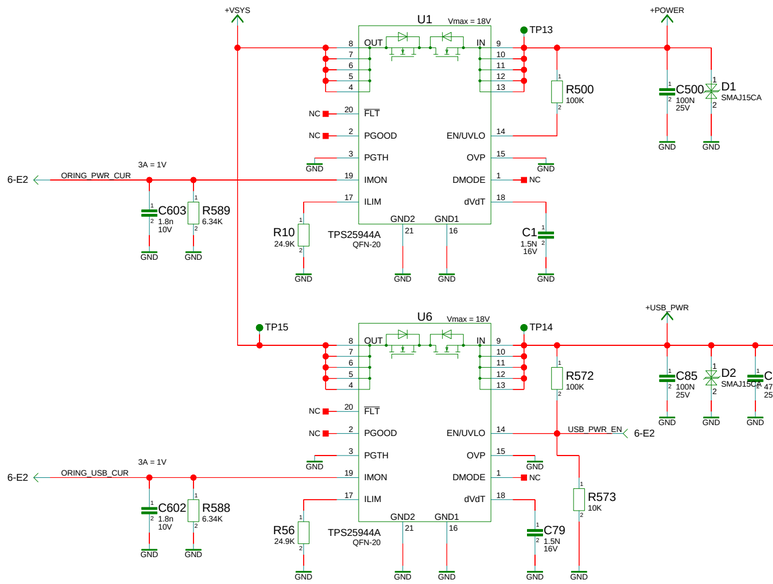
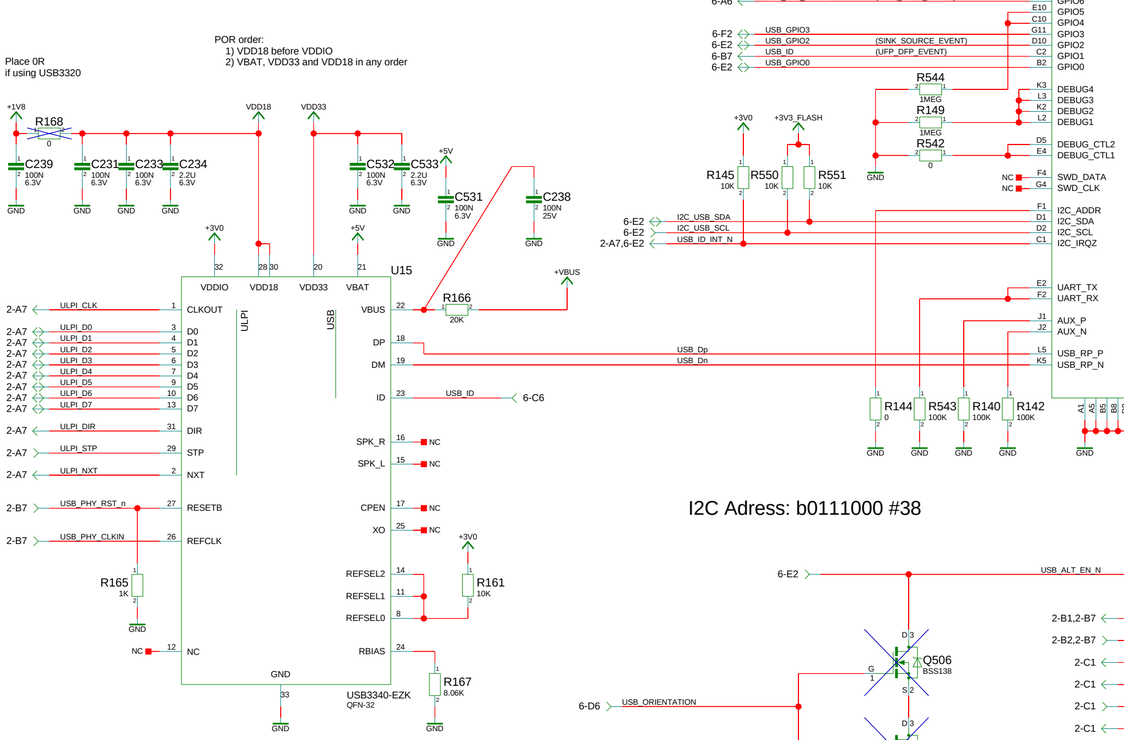
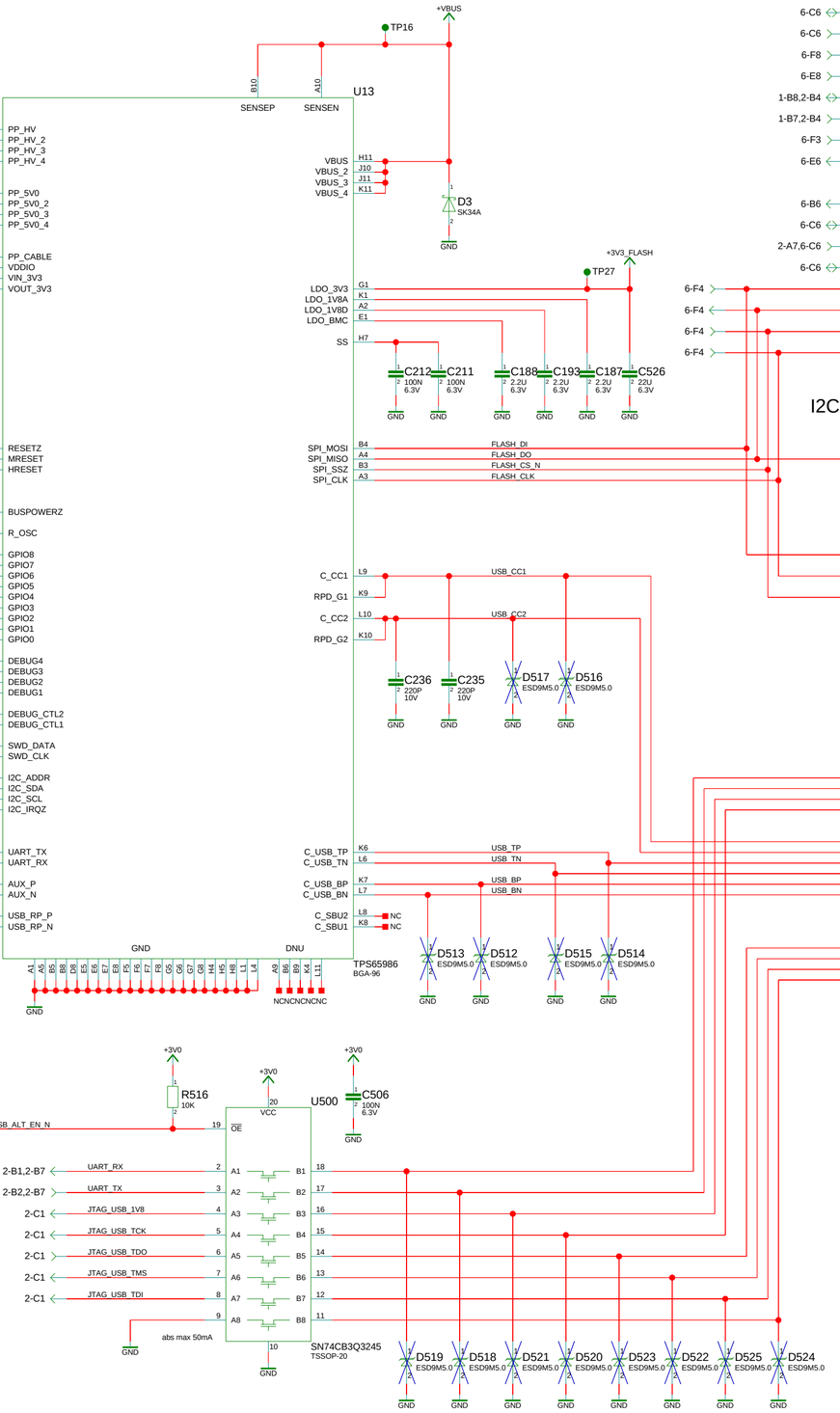
ORing



USB - PHY

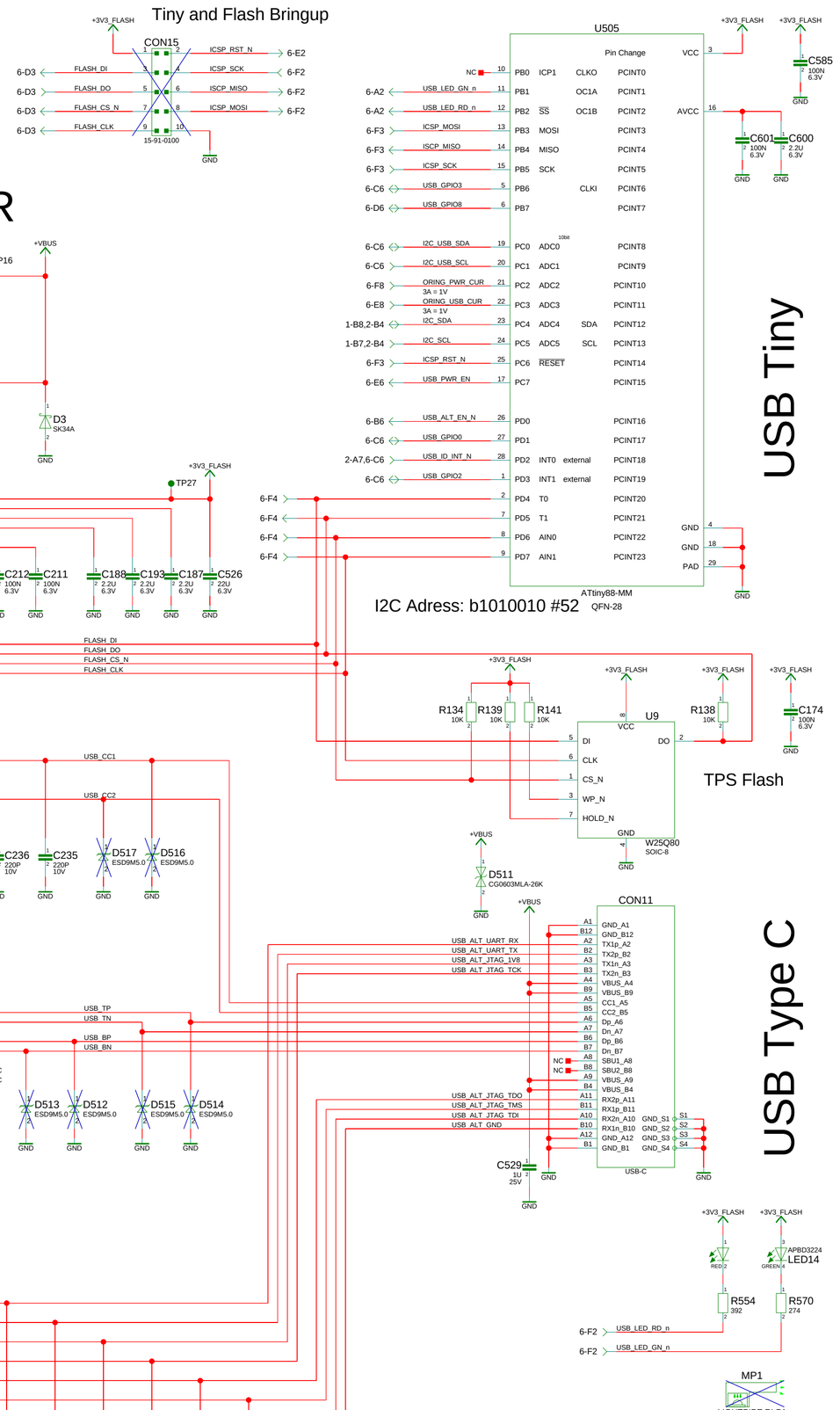


USB - POWER



I2C Adress: b0111000 #38

USB Tiny



USB Type C

USB					
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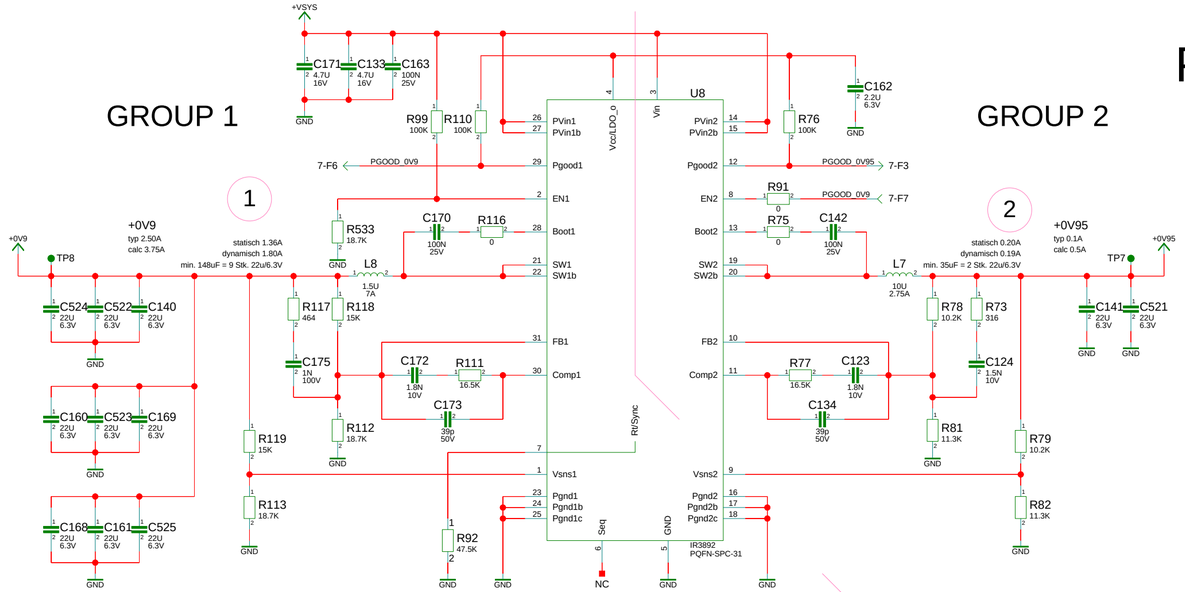
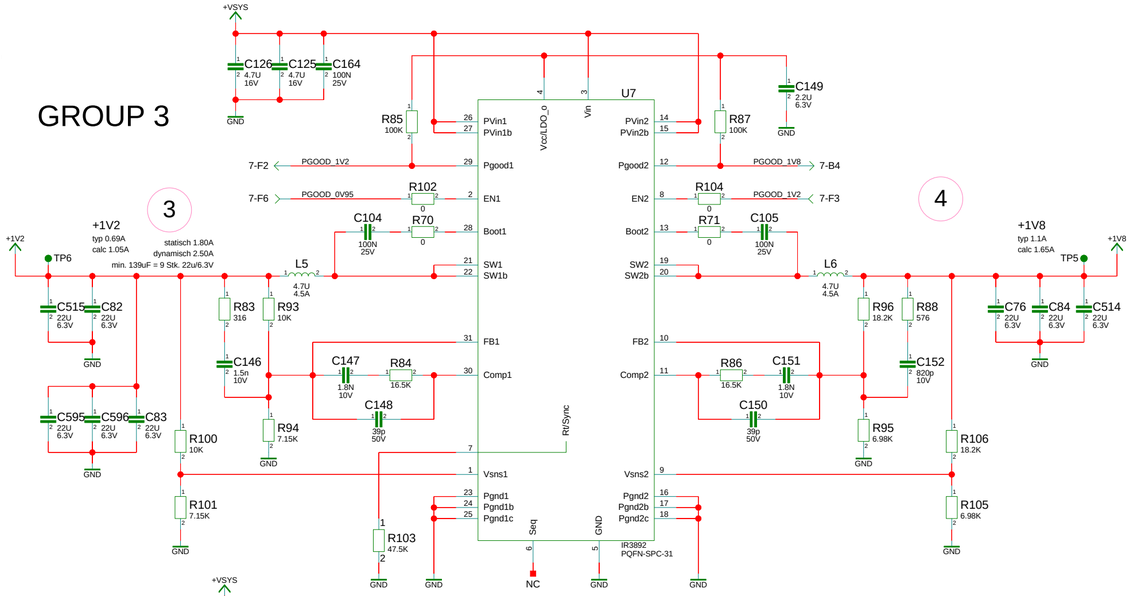
Power

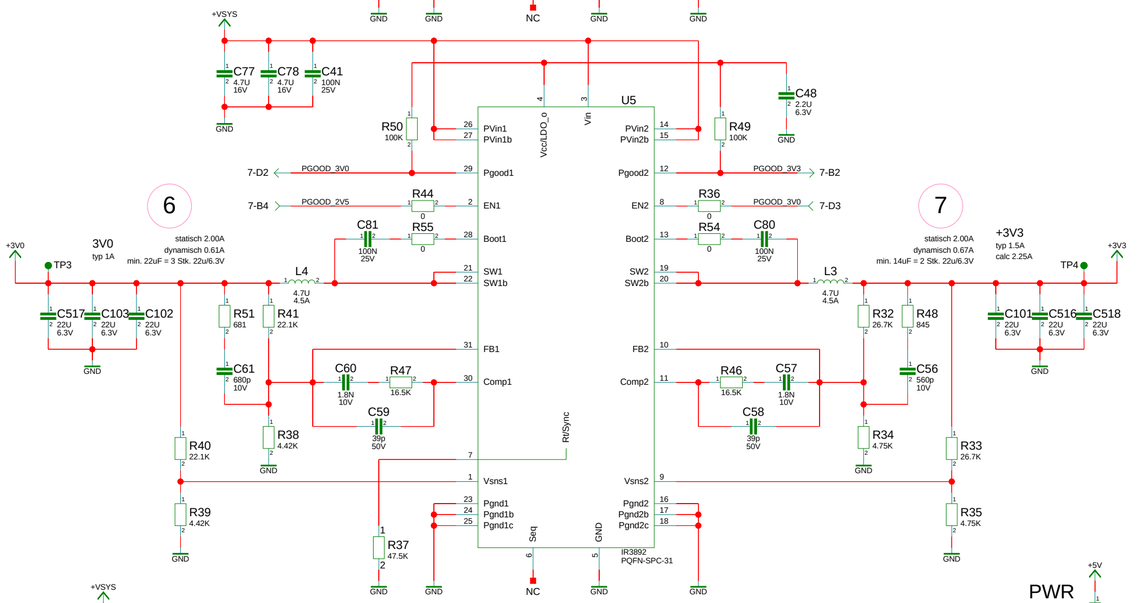
GROUP 1

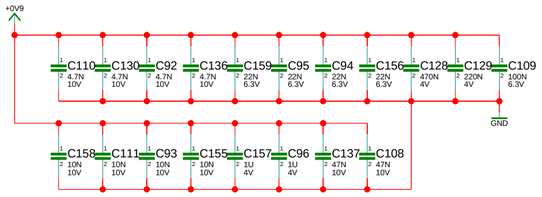
GROUP 2

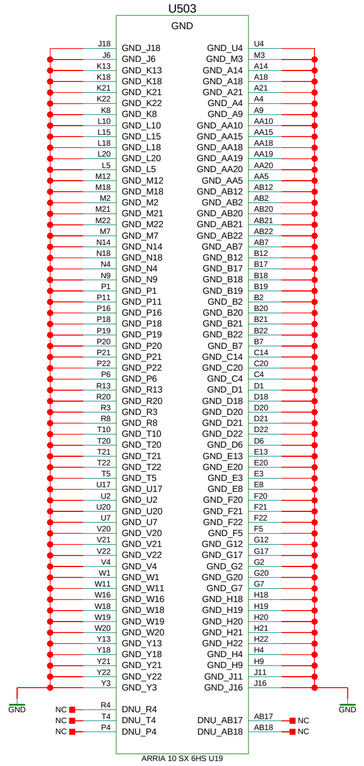
GROUP 3

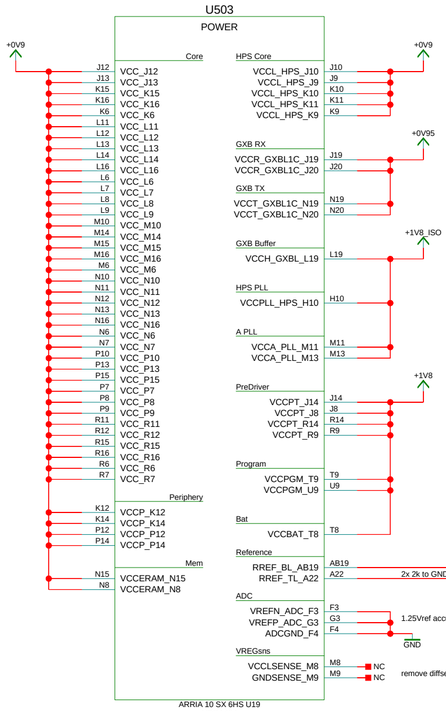


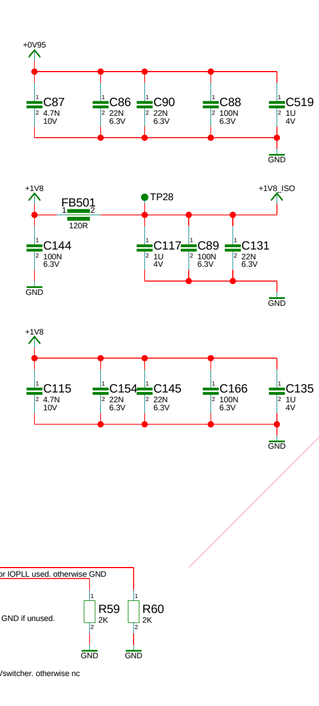












Power				
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