

Circuit Description

1. The Marvell 88AP166-A0-BJD2C008 chip is master micro controller, the oscillator component Y102 generated a frequency of 26MHz as base clock frequency, it is a center processing unit which the master frequency of running speed is 333MHz, with 1GB DDR memory, and SD card socket, one USB port for external USB disk.
2. The AW-GH321 WiFi module base on 802.11b/g WIFI wireless technolgy, use the high-frequency from 2412MHz to 2462MHz as carrier frequency for 802.11b/g, the CCK, OFDM modulation type be used.
3. The USB disk and SD card all external signal and data input to MCU as program source, playing and display on LCD screen.
4. The work voltage is DC 5V, supply by external power adaptor.