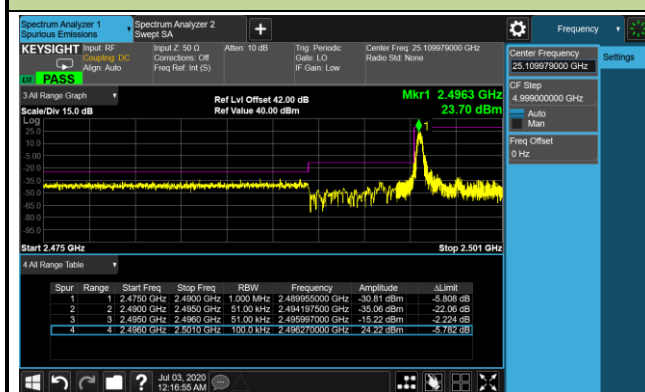


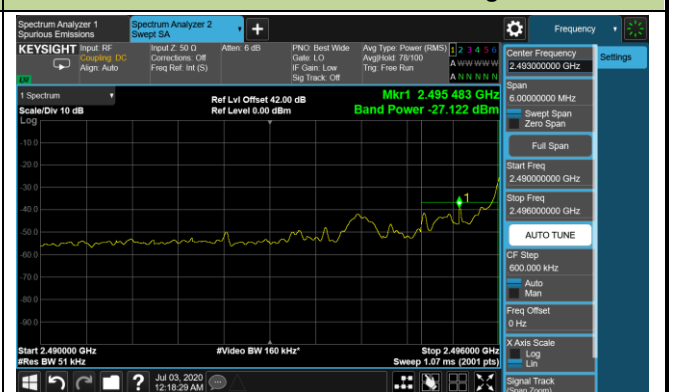
Product	LTE-A Cat 12 M.2 Module	Test Engineer	Gordon Qi
Test Date	2020/07/02	Test Site	SR6
Test Band	Band 41	Test Result	Pass

5MHz Channel Bandwidth - 1RB

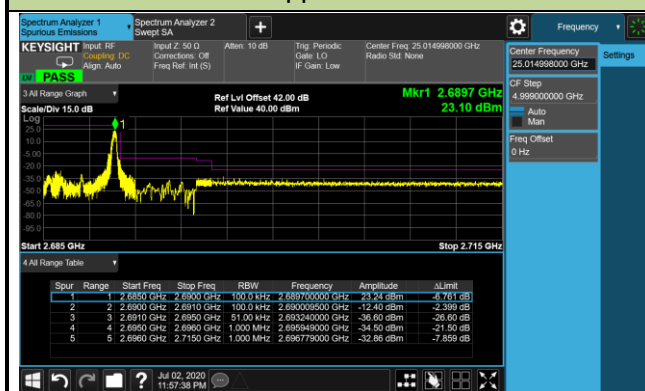
Lower ACP



Lower Extended Band Edge



Upper ACP

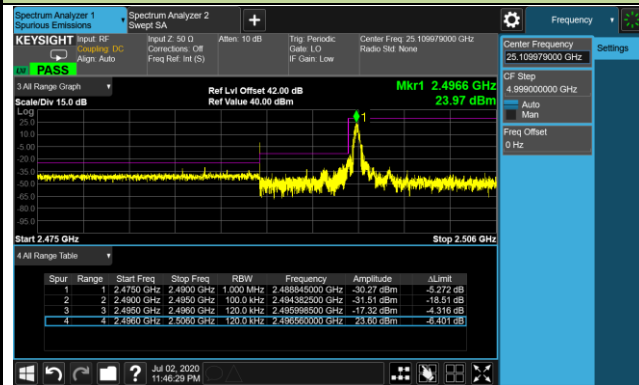


Upper Extended Band Edge



10MHz Channel Bandwidth - 1RB

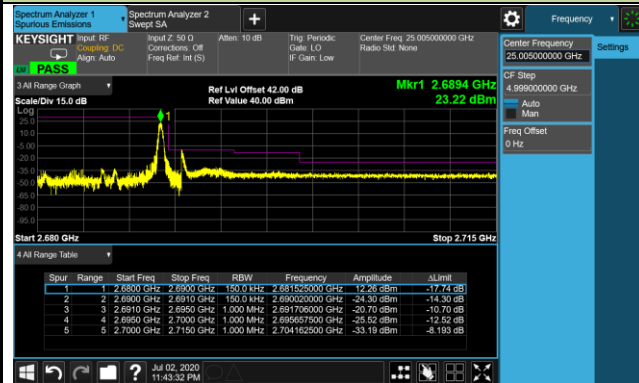
Lower ACP



Lower Extended Band Edge

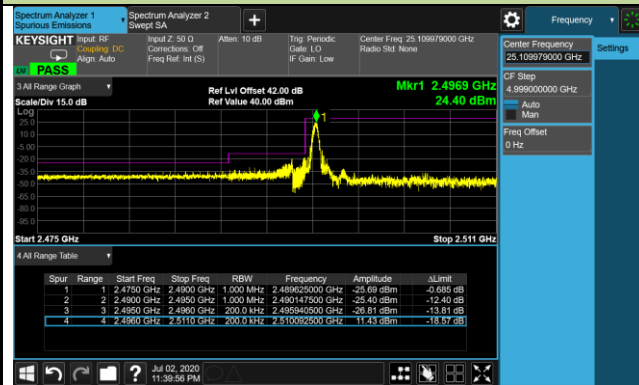


Upper ACP

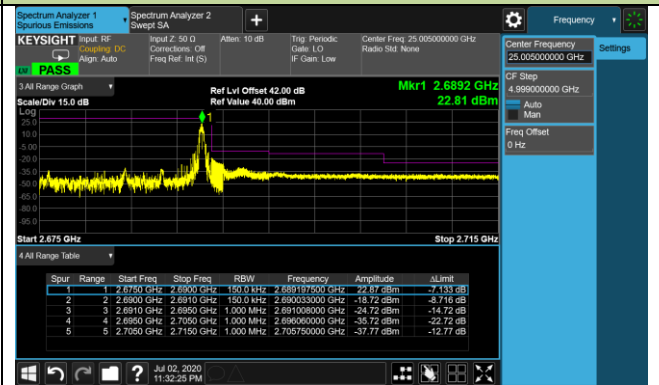


15MHz Channel Bandwidth - 1RB

Lower ACP

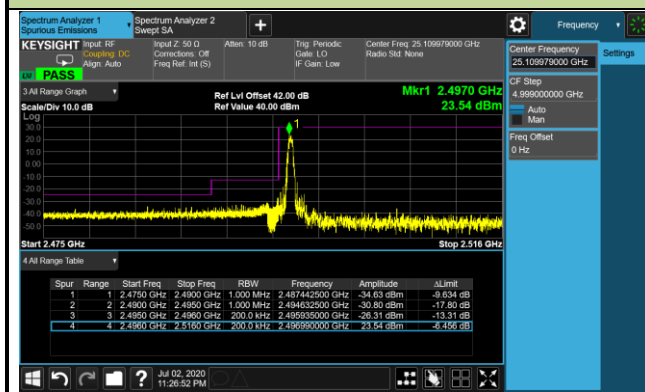


Upper ACP

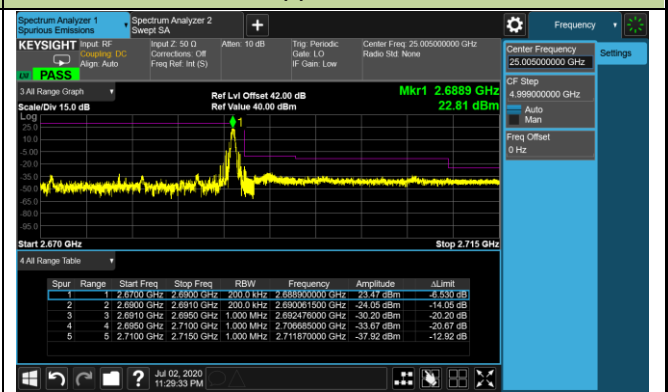


20MHz Channel Bandwidth - 1RB

Lower ACP

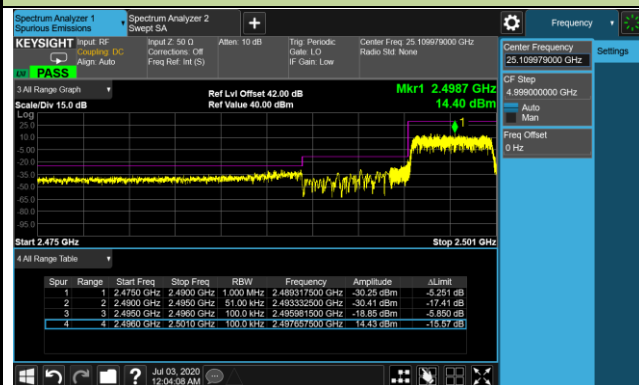


Upper ACP

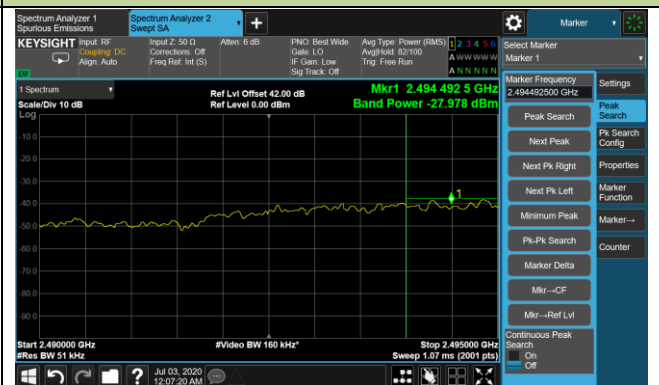


5MHz Channel Bandwidth - Full RB

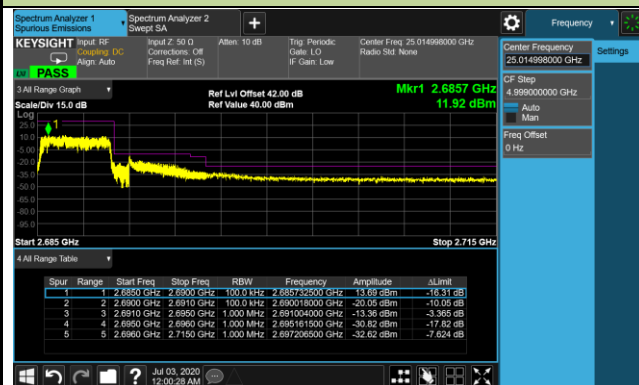
Lower ACP



Lower Extended Band Edge

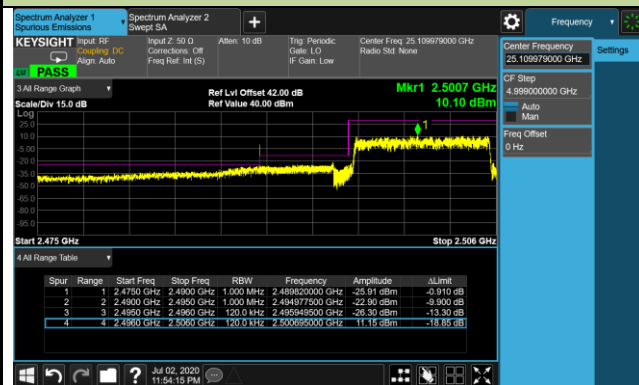


Upper ACP



10MHz Channel Bandwidth - Full RB

Lower ACP

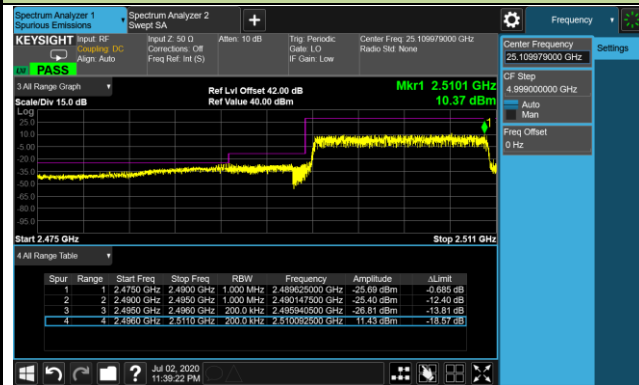


Upper ACP

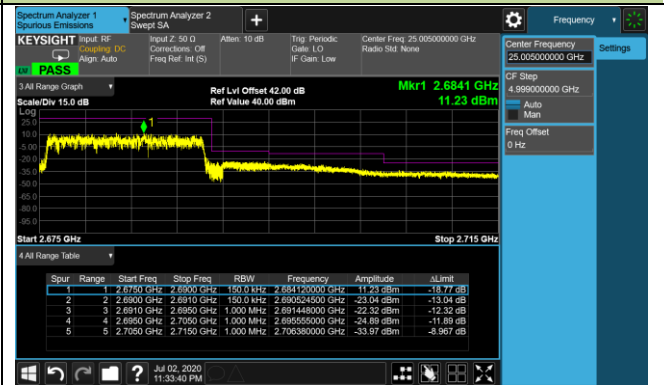


15MHz Channel Bandwidth - Full RB

Lower ACP

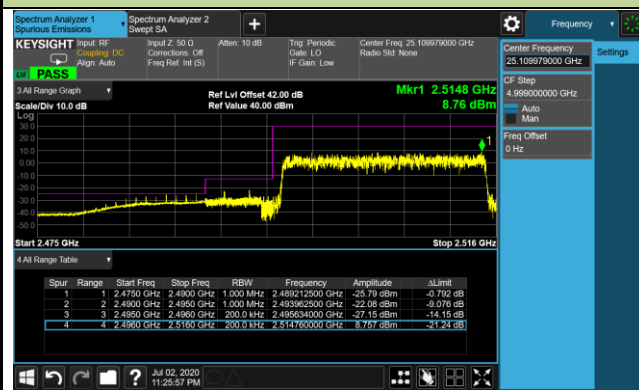


Upper ACP

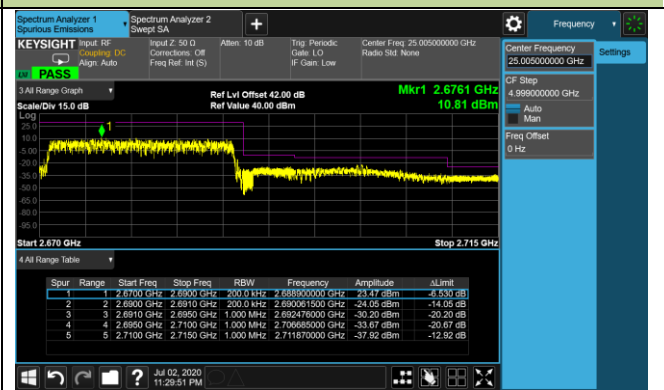


20MHz Channel Bandwidth - Full RB

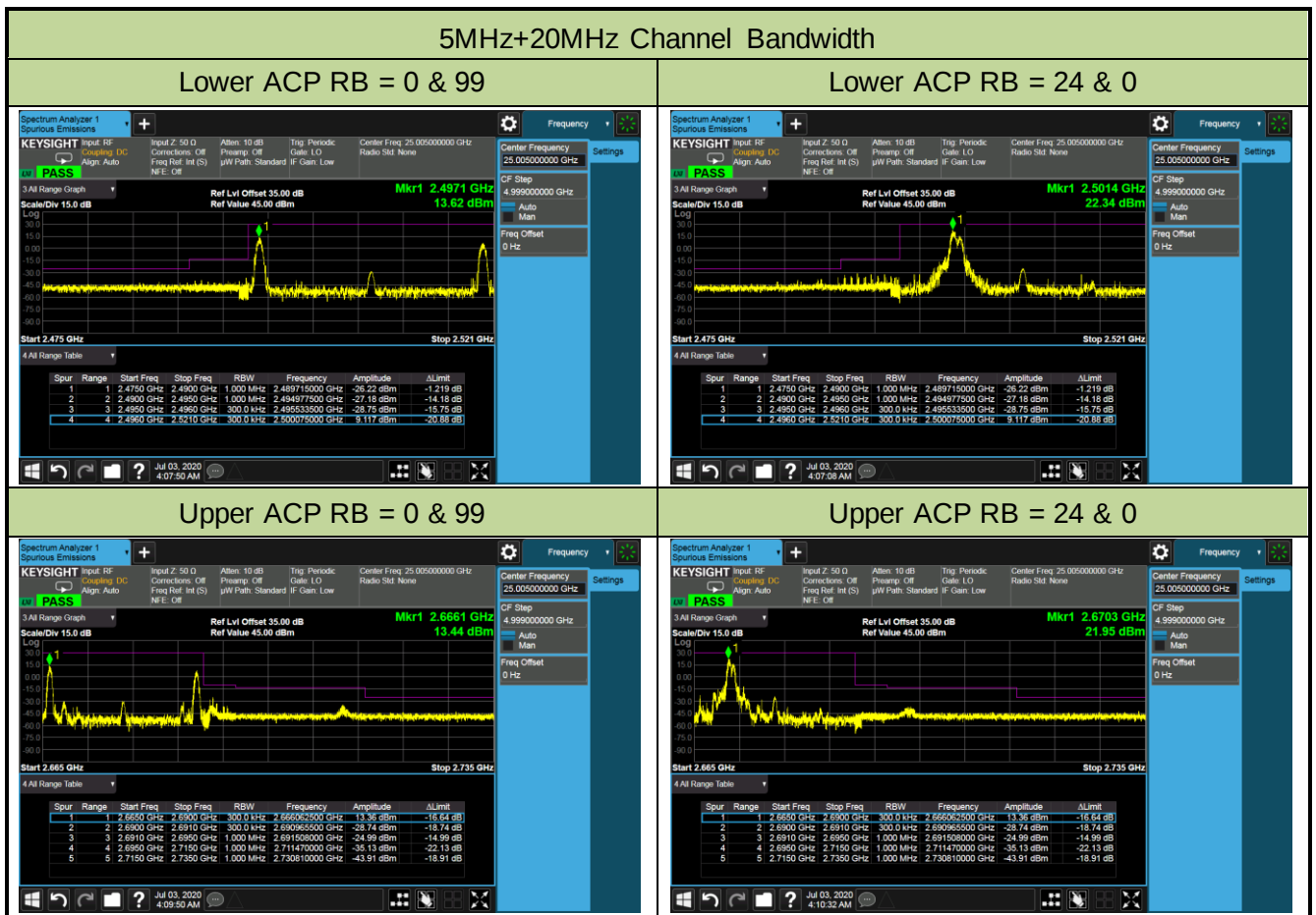
Lower ACP



Upper ACP

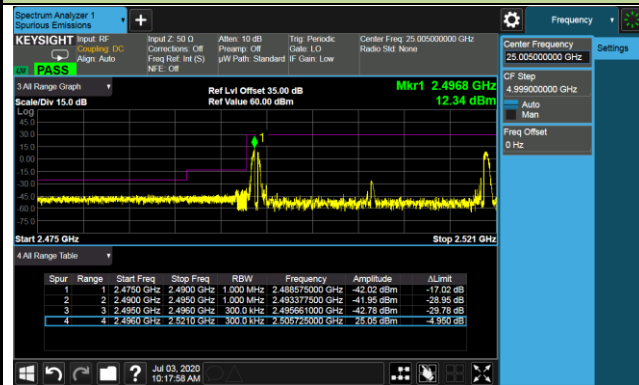


Product	LTE-A Cat 12 M.2 Module	Test Engineer	Candy Luo
Test Date	2020/07/03	Test Site	SR6
Test Band	Intra-Band CA_41C	Test Result	Pass

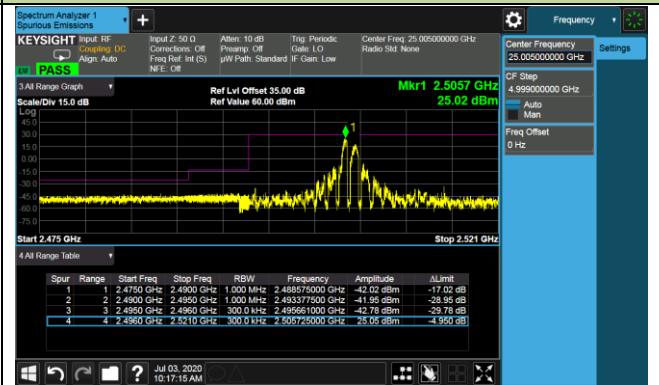


10MHz+15MHz Channel Bandwidth

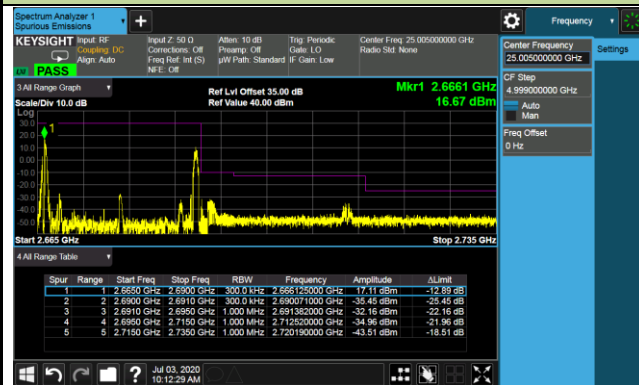
Lower ACP RB = 0 & 74



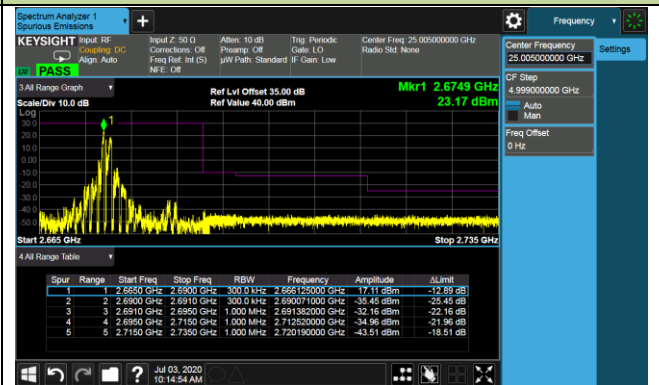
Lower ACP RB = 49 & 0



Upper ACP RB= 0 & 74

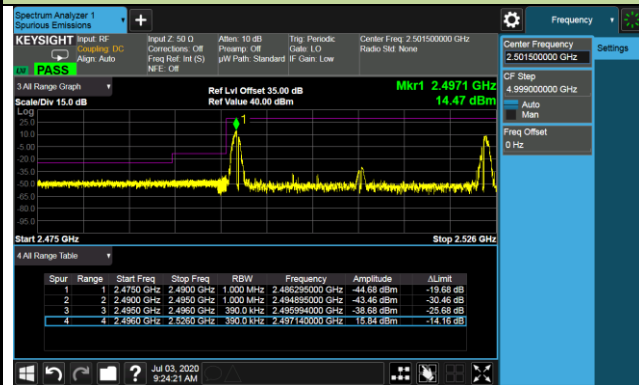


Upper ACP RB = 49 & 0

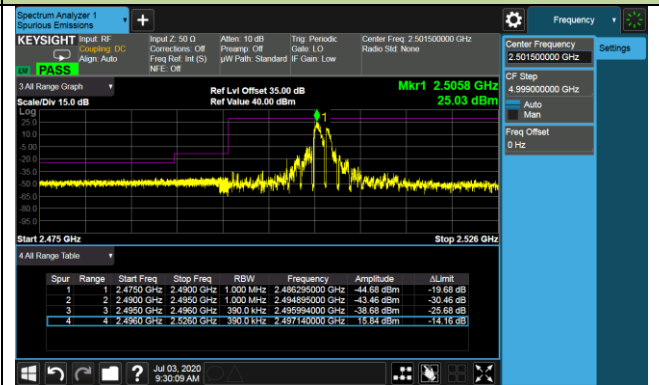


10MHz+20MHz Channel Bandwidth

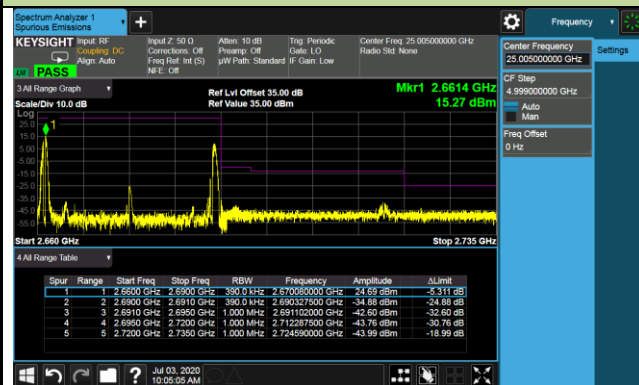
Lower ACP RB = 0 & 99



Lower ACP RB= 49 & 0



Upper ACP RB= 0 & 99

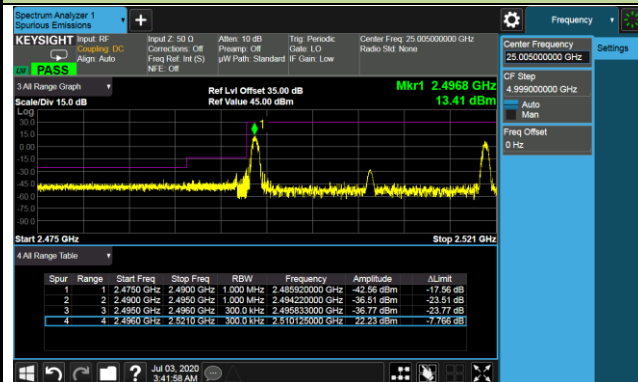


Upper ACP RB = 49 & 0

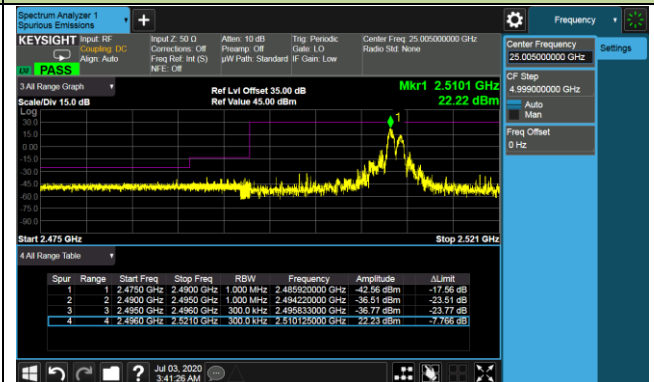


15MHz+10MHz Channel Bandwidth

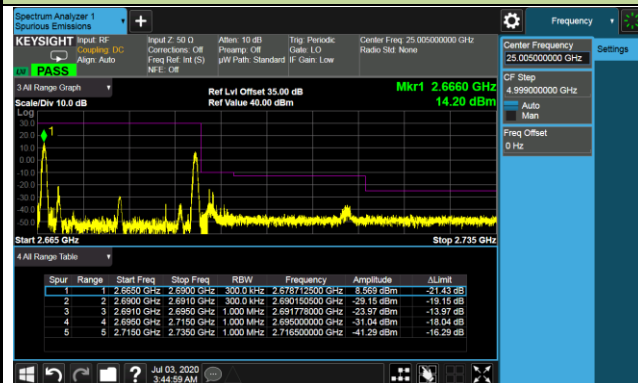
Lower ACP RB = 0 & 49



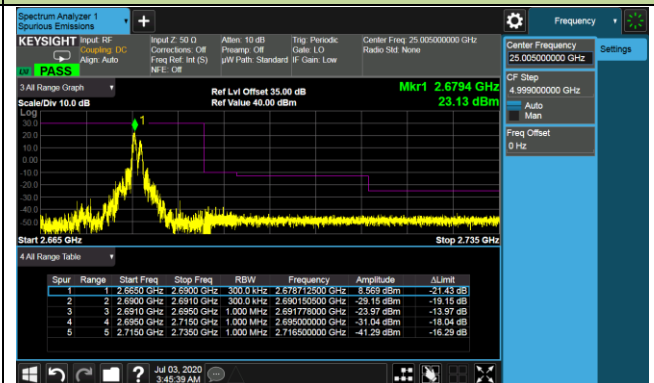
Lower ACP RB = 74 & 0



Upper ACP RB = 0 & 49

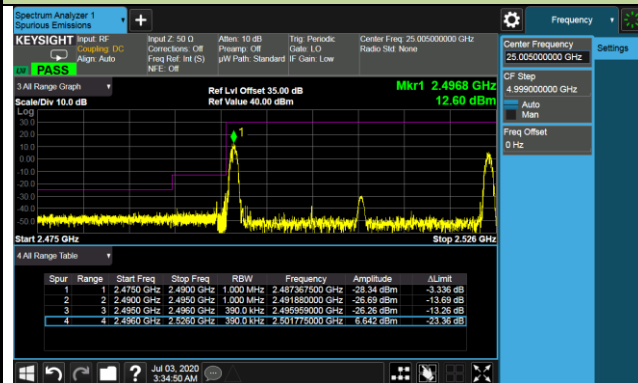


Upper ACP RB = 74 & 0

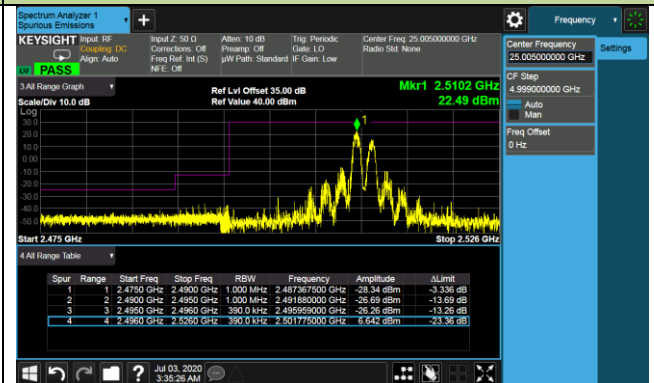


15MHz+15MHz Channel Bandwidth

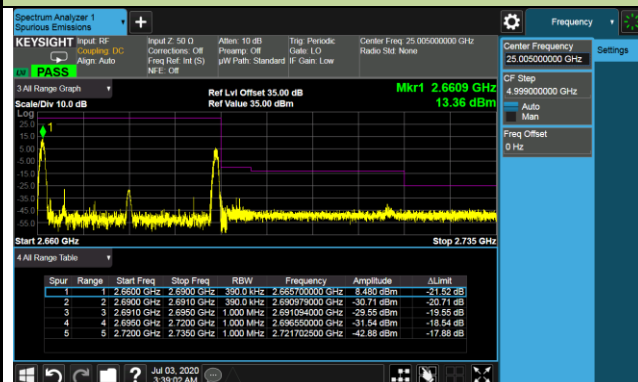
Lower ACP RB = 0 & 74



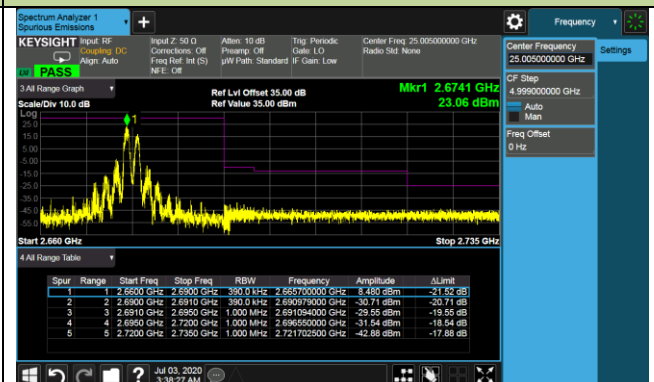
Lower ACP RB= 74 & 0



Upper ACP RB = 0 & 74

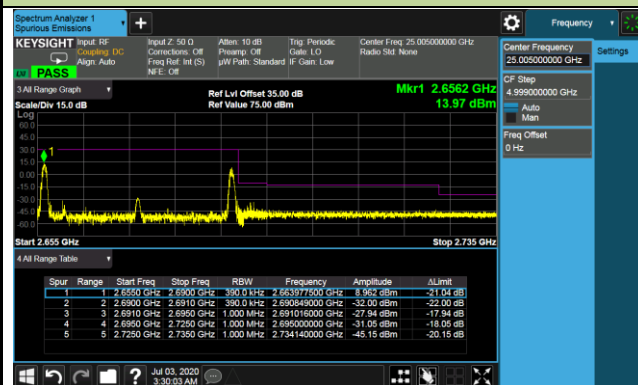


Upper ACP RB = 74 & 0

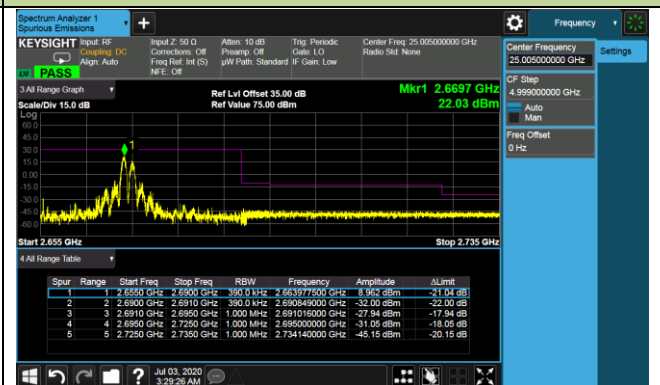


15MHz+20MHz Channel Bandwidth

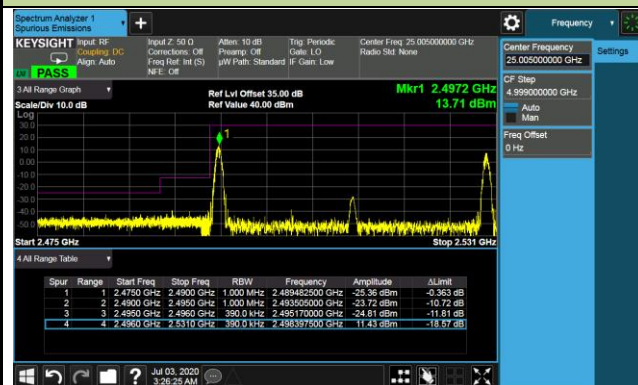
Lower ACP RB = 0 & 99



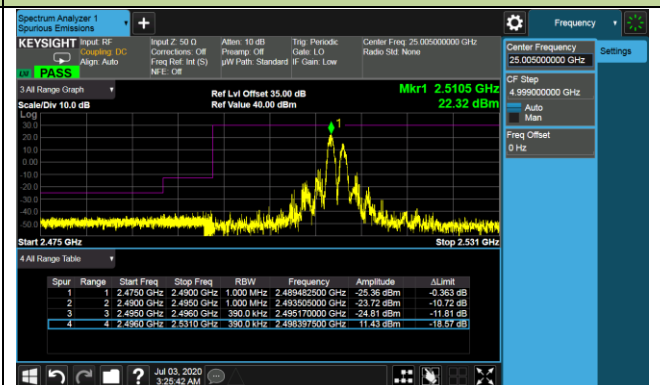
Lower ACP RB = 74 & 0



Upper ACP RB = 0 & 99

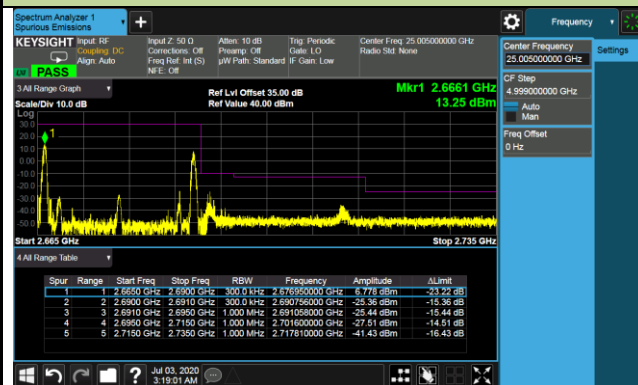


Upper ACP RB = 74 & 0

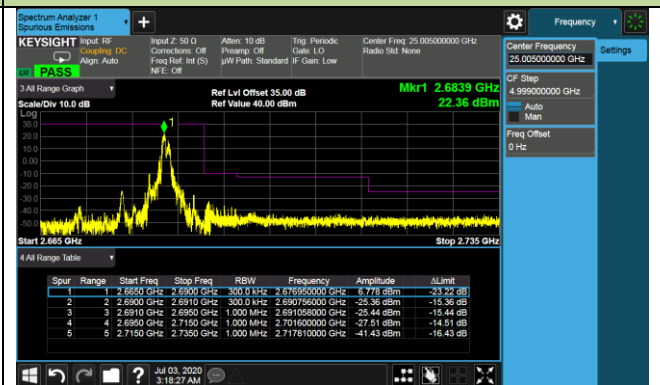


20MHz+5MHz Channel Bandwidth

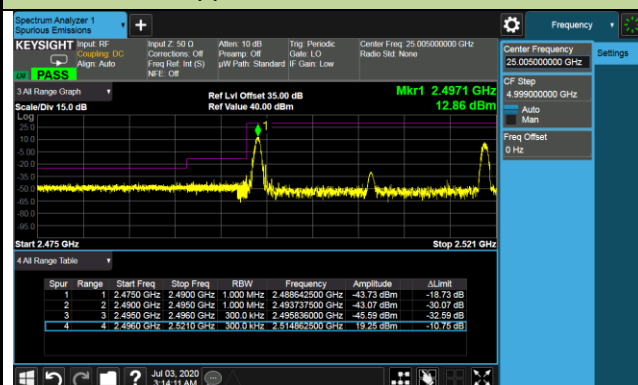
Lower ACP RB = 0 & 24



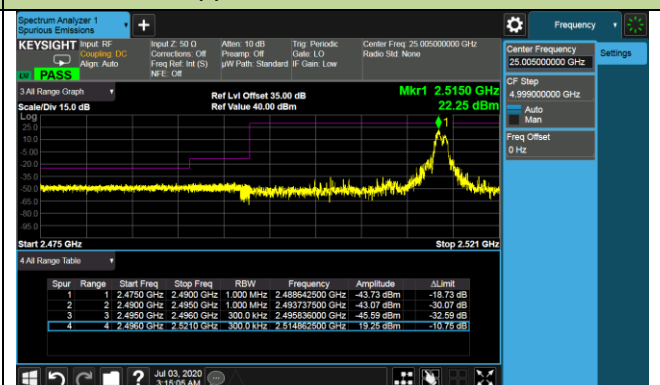
Lower ACP RB = 99 & 0



Upper ACP RB = 0 & 24

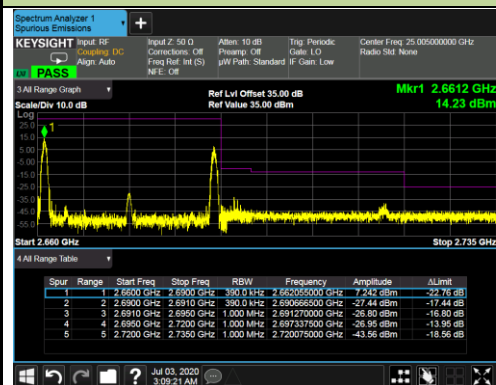


Upper ACP RB = 99 & 0

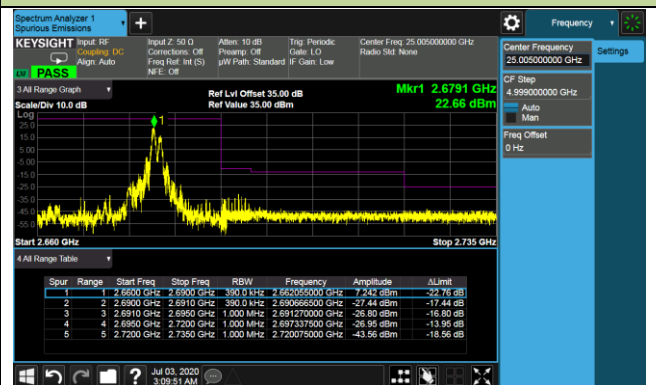


20MHz+10MHz Channel Bandwidth

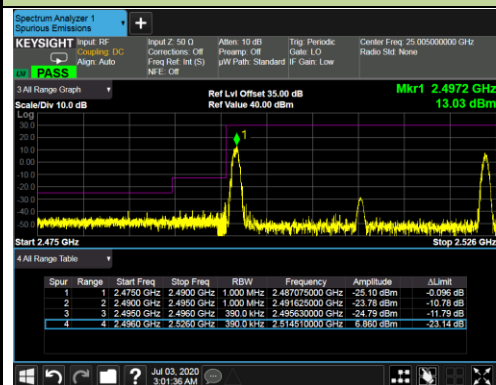
Lower ACP RB = 0 & 49



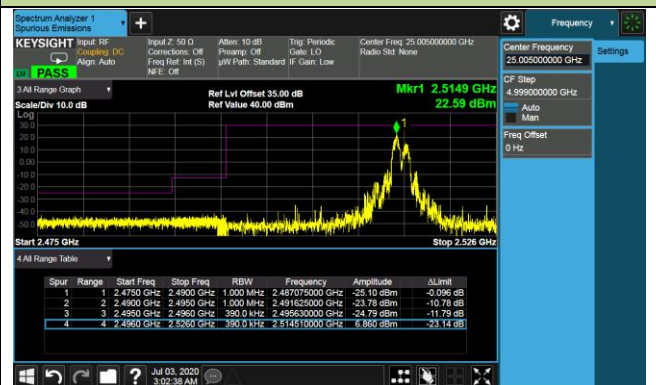
Lower ACP RB = 99 & 0



Upper ACP RB = 0 & 49

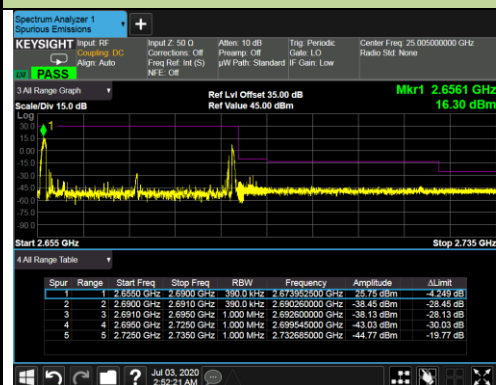


Upper ACP RB = 99 & 0

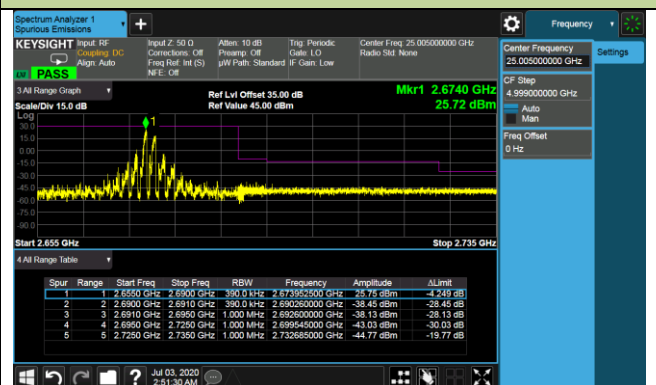


20MHz+15MHz Channel Bandwidth

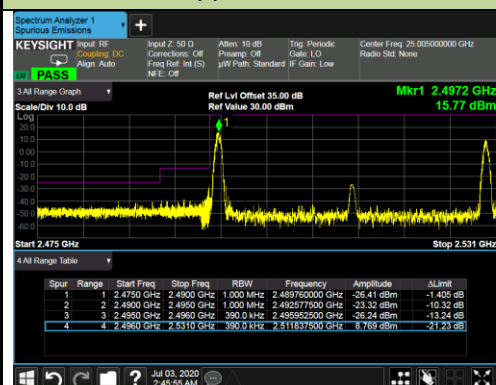
Lower ACP RB = 0 & 74



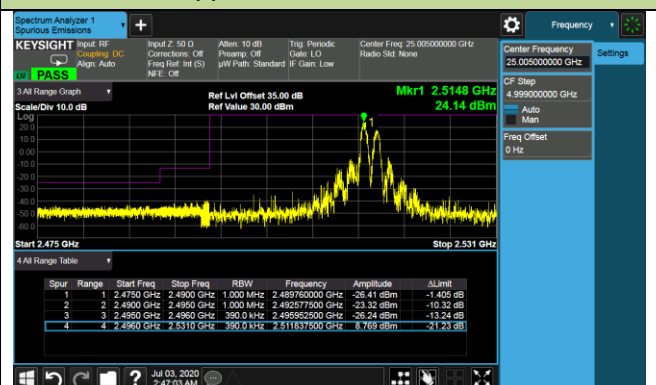
Lower ACP RB = 99 & 0



Upper ACP RB = 0 & 74

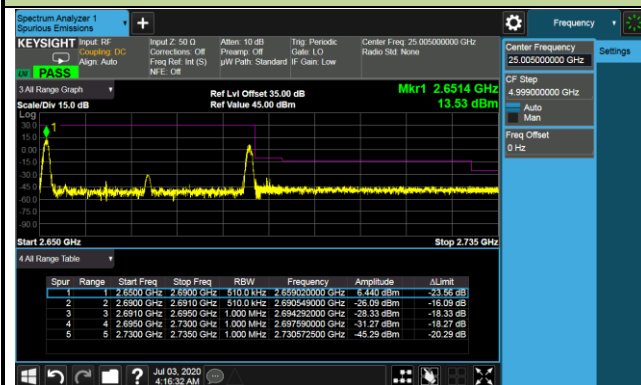


Upper ACP RB = 99 & 0

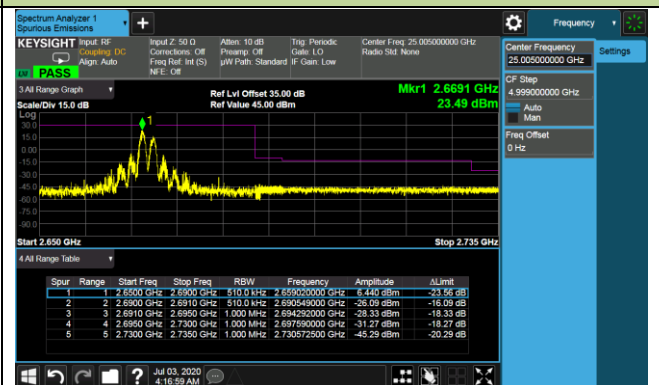


20MHz+20MHz Channel Bandwidth

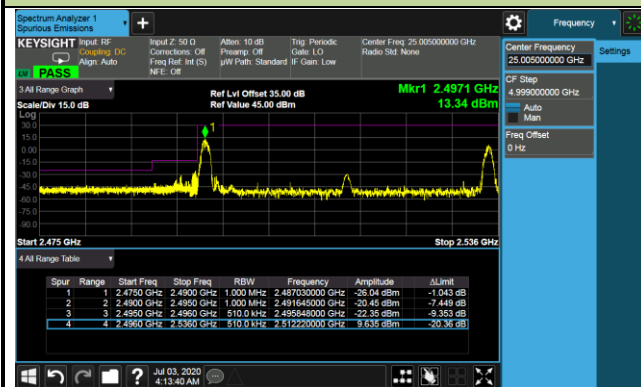
Lower ACP RB = 0 & 99



Lower ACP RB = 99 & 0



Upper ACP RB = 0 & 99



Upper ACP RB = 99 & 0

