

Effective (Isotropic) Radiated Power Output Data

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power (dBm)	Gain (dBi)	E(I)RP (dBm)	Limit (dBm)	Verdict
DC_7A_n77A	30	5+10	DFT-BPSK	M+L	Inner_1RB_Left	26.23	1.88	28.11	30	PASS
DC_7A_n77A	30	5+10	DFT-BPSK	M+L	Inner_1RB_Right	26.12	1.88	28	30	PASS
DC_7A_n77A	30	5+10	DFT-BPSK	M+L	Inner_Full	26.18	1.88	28.06	30	PASS
DC_7A_n77A	30	5+10	DFT-BPSK	M+L	Outer_Full	25.67	1.88	27.55	30	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+L	Inner_1RB_Left	26.36	1.88	28.24	30	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+L	Inner_1RB_Right	26.14	1.88	28.02	30	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+L	Inner_Full	26.12	1.88	28	30	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+L	Outer_Full	25.19	1.88	27.07	30	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+L	Inner_1RB_Left	24.87	1.88	26.75	30	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+L	Inner_1RB_Right	24.77	1.88	26.65	30	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+L	Inner_Full	25.05	1.88	26.93	30	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+L	Outer_Full	24.11	1.88	25.99	30	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+L	Inner_1RB_Left	23.37	1.88	25.25	30	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+L	Inner_1RB_Right	23.25	1.88	25.13	30	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+L	Inner_Full	23.62	1.88	25.5	30	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+L	Outer_Full	23.63	1.88	25.51	30	PASS
DC_7A_n77A	30	5+10	DFT-256QAM	M+L	Inner_1RB_Left	21.58	1.88	23.46	30	PASS
DC_7A_n77A	30	5+10	DFT-256QAM	M+L	Inner_1RB_Right	21.37	1.88	23.25	30	PASS
DC_7A_n77A	30	5+10	DFT-256QAM	M+L	Inner_Full	21.71	1.88	23.59	30	PASS
DC_7A_n77A	30	5+10	DFT-256QAM	M+L	Outer_Full	21.61	1.88	23.49	30	PASS
DC_7A_n77A	30	5+10	DFT-BPSK	M+M	Inner_1RB_Left	25.4	1.88	27.28	30	PASS
DC_7A_n77A	30	5+10	DFT-BPSK	M+M	Inner_1RB_Right	25.21	1.88	27.09	30	PASS
DC_7A_n77A	30	5+10	DFT-BPSK	M+M	Inner_Full	25.38	1.88	27.26	30	PASS
DC_7A_n77A	30	5+10	DFT-BPSK	M+M	Outer_Full	25.58	1.88	27.46	30	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+M	Inner_1RB_Left	25.29	1.88	27.17	30	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+M	Inner_1RB_Right	25.17	1.88	27.05	30	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+M	Inner_Full	25.32	1.88	27.2	30	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+M	Outer_Full	25.37	1.88	27.25	30	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+M	Inner_1RB_Left	24.55	1.88	26.43	30	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+M	Inner_1RB_Right	24.43	1.88	26.31	30	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+M	Inner_Full	24.35	1.88	26.23	30	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+M	Outer_Full	23.34	1.88	25.22	30	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+M	Inner_1RB_Left	22.78	1.88	24.66	30	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+M	Inner_1RB_Right	22.64	1.88	24.52	30	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+M	Inner_Full	22.83	1.88	24.71	30	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+M	Outer_Full	22.75	1.88	24.63	30	PASS

DC_7A_n77A	30	5+10	DFT-256QAM	M+M	Inner_1RB_Left	20.84	1.88	22.72	30	PASS
DC_7A_n77A	30	5+10	DFT-256QAM	M+M	Inner_1RB_Right	20.68	1.88	22.56	30	PASS
DC_7A_n77A	30	5+10	DFT-256QAM	M+M	Inner_Full	20.84	1.88	22.72	30	PASS
DC_7A_n77A	30	5+10	DFT-256QAM	M+M	Outer_Full	20.86	1.88	22.74	30	PASS
DC_7A_n77A	30	5+10	DFT-BPSK	M+H	Inner_1RB_Left	24.96	1.88	26.84	30	PASS
DC_7A_n77A	30	5+10	DFT-BPSK	M+H	Inner_1RB_Right	25.1	1.88	26.98	30	PASS
DC_7A_n77A	30	5+10	DFT-BPSK	M+H	Inner_Full	25.58	1.88	27.46	30	PASS
DC_7A_n77A	30	5+10	DFT-BPSK	M+H	Outer_Full	25.58	1.88	27.46	30	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+H	Inner_1RB_Left	25	1.88	26.88	30	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+H	Inner_1RB_Right	25.26	1.88	27.14	30	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+H	Inner_Full	25.51	1.88	27.39	30	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+H	Outer_Full	25.52	1.88	27.4	30	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+H	Inner_1RB_Left	23.79	1.88	25.67	30	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+H	Inner_1RB_Right	24.07	1.88	25.95	30	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+H	Inner_Full	23.97	1.88	25.85	30	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+H	Outer_Full	22.9	1.88	24.78	30	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+H	Inner_1RB_Left	22.29	1.88	24.17	30	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+H	Inner_1RB_Right	22.45	1.88	24.33	30	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+H	Inner_Full	22.43	1.88	24.31	30	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+H	Outer_Full	22.42	1.88	24.3	30	PASS
DC_7A_n77A	30	5+10	DFT-256QAM	M+H	Inner_1RB_Left	20.37	1.88	22.25	30	PASS
DC_7A_n77A	30	5+10	DFT-256QAM	M+H	Inner_1RB_Right	20.61	1.88	22.49	30	PASS
DC_7A_n77A	30	5+10	DFT-256QAM	M+H	Inner_Full	20.51	1.88	22.39	30	PASS
DC_7A_n77A	30	5+10	DFT-256QAM	M+H	Outer_Full	20.65	1.88	22.53	30	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+L	Inner_1RB_Left	26.21	1.88	28.09	30	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+L	Inner_1RB_Right	25.95	1.88	27.83	30	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+L	Inner_Full	26.2	1.88	28.08	30	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+L	Outer_Full	25.67	1.88	27.55	30	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+L	Inner_1RB_Left	26.44	1.88	28.32	30	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+L	Inner_1RB_Right	26.18	1.88	28.06	30	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+L	Inner_Full	26.12	1.88	28	30	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+L	Outer_Full	25.2	1.88	27.08	30	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+L	Inner_1RB_Left	25.09	1.88	26.97	30	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+L	Inner_1RB_Right	24.82	1.88	26.7	30	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+L	Inner_Full	25.07	1.88	26.95	30	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+L	Outer_Full	24.07	1.88	25.95	30	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+L	Inner_1RB_Left	23.7	1.88	25.58	30	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+L	Inner_1RB_Right	23.46	1.88	25.34	30	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+L	Inner_Full	23.47	1.88	25.35	30	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+L	Outer_Full	23.59	1.88	25.47	30	PASS
DC_7A_n77A	30	5+15	DFT-256QAM	M+L	Inner_1RB_Left	21.81	1.88	23.69	30	PASS

DC_7A_n77A	30	5+15	DFT-256QAM	M+L	Inner_1RB_Right	21.55	1.88	23.43	30	PASS
DC_7A_n77A	30	5+15	DFT-256QAM	M+L	Inner_Full	21.55	1.88	23.43	30	PASS
DC_7A_n77A	30	5+15	DFT-256QAM	M+L	Outer_Full	21.59	1.88	23.47	30	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+M	Inner_1RB_Left	25.56	1.88	27.44	30	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+M	Inner_1RB_Right	25.47	1.88	27.35	30	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+M	Inner_Full	25.48	1.88	27.36	30	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+M	Outer_Full	25.56	1.88	27.44	30	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+M	Inner_1RB_Left	25.69	1.88	27.57	30	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+M	Inner_1RB_Right	25.59	1.88	27.47	30	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+M	Inner_Full	25.33	1.88	27.21	30	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+M	Outer_Full	25.44	1.88	27.32	30	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+M	Inner_1RB_Left	24.4	1.88	26.28	30	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+M	Inner_1RB_Right	24.33	1.88	26.21	30	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+M	Inner_Full	24.29	1.88	26.17	30	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+M	Outer_Full	23.18	1.88	25.06	30	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+M	Inner_1RB_Left	23.22	1.88	25.1	30	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+M	Inner_1RB_Right	23.11	1.88	24.99	30	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+M	Inner_Full	22.86	1.88	24.74	30	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+M	Outer_Full	22.79	1.88	24.67	30	PASS
DC_7A_n77A	30	5+15	DFT-256QAM	M+M	Inner_1RB_Left	20.88	1.88	22.76	30	PASS
DC_7A_n77A	30	5+15	DFT-256QAM	M+M	Inner_1RB_Right	20.85	1.88	22.73	30	PASS
DC_7A_n77A	30	5+15	DFT-256QAM	M+M	Inner_Full	20.86	1.88	22.74	30	PASS
DC_7A_n77A	30	5+15	DFT-256QAM	M+M	Outer_Full	20.8	1.88	22.68	30	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+H	Inner_1RB_Left	24.67	1.88	26.55	30	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+H	Inner_1RB_Right	24.97	1.88	26.85	30	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+H	Inner_Full	25.92	1.88	27.8	30	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+H	Outer_Full	25.44	1.88	27.32	30	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+H	Inner_1RB_Left	25.2	1.88	27.08	30	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+H	Inner_1RB_Right	25.53	1.88	27.41	30	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+H	Inner_Full	25.84	1.88	27.72	30	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+H	Outer_Full	25.52	1.88	27.4	30	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+H	Inner_1RB_Left	23.8	1.88	25.68	30	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+H	Inner_1RB_Right	24.12	1.88	26	30	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+H	Inner_Full	23.79	1.88	25.67	30	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+H	Outer_Full	22.81	1.88	24.69	30	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+H	Inner_1RB_Left	22.22	1.88	24.1	30	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+H	Inner_1RB_Right	22.65	1.88	24.53	30	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+H	Inner_Full	22.25	1.88	24.13	30	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+H	Outer_Full	22.34	1.88	24.22	30	PASS
DC_7A_n77A	30	5+15	DFT-256QAM	M+H	Inner_1RB_Left	20.56	1.88	22.44	30	PASS
DC_7A_n77A	30	5+15	DFT-256QAM	M+H	Inner_1RB_Right	20.86	1.88	22.74	30	PASS

DC_7A_n77A	30	5+15	DFT-256QAM	M+H	Inner_Full	20.24	1.88	22.12	30	PASS
DC_7A_n77A	30	5+15	DFT-256QAM	M+H	Outer_Full	20.36	1.88	22.24	30	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+L	Inner_1RB_Left	26.21	1.88	28.09	30	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+L	Inner_1RB_Right	26.04	1.88	27.92	30	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+L	Inner_Full	26.28	1.88	28.16	30	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+L	Outer_Full	25.72	1.88	27.6	30	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+L	Inner_1RB_Left	26.08	1.88	27.96	30	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+L	Inner_1RB_Right	25.82	1.88	27.7	30	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+L	Inner_Full	26.16	1.88	28.04	30	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+L	Outer_Full	25.55	1.88	27.43	30	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+L	Inner_1RB_Left	25.14	1.88	27.02	30	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+L	Inner_1RB_Right	24.99	1.88	26.87	30	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+L	Inner_Full	24.82	1.88	26.7	30	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+L	Outer_Full	24.06	1.88	25.94	30	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+L	Inner_1RB_Left	23.99	1.88	25.87	30	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+L	Inner_1RB_Right	23.92	1.88	25.8	30	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+L	Inner_Full	23.55	1.88	25.43	30	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+L	Outer_Full	23.62	1.88	25.5	30	PASS
DC_7A_n77A	30	5+20	DFT-256QAM	M+L	Inner_1RB_Left	21.74	1.88	23.62	30	PASS
DC_7A_n77A	30	5+20	DFT-256QAM	M+L	Inner_1RB_Right	21.64	1.88	23.52	30	PASS
DC_7A_n77A	30	5+20	DFT-256QAM	M+L	Inner_Full	21.48	1.88	23.36	30	PASS
DC_7A_n77A	30	5+20	DFT-256QAM	M+L	Outer_Full	21.72	1.88	23.6	30	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+M	Inner_1RB_Left	25.59	1.88	27.47	30	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+M	Inner_1RB_Right	25.4	1.88	27.28	30	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+M	Inner_Full	25.55	1.88	27.43	30	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+M	Outer_Full	25.53	1.88	27.41	30	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+M	Inner_1RB_Left	25.82	1.88	27.7	30	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+M	Inner_1RB_Right	25.52	1.88	27.4	30	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+M	Inner_Full	25.32	1.88	27.2	30	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+M	Outer_Full	25.46	1.88	27.34	30	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+M	Inner_1RB_Left	24.67	1.88	26.55	30	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+M	Inner_1RB_Right	24.51	1.88	26.39	30	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+M	Inner_Full	24.3	1.88	26.18	30	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+M	Outer_Full	23.37	1.88	25.25	30	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+M	Inner_1RB_Left	22.79	1.88	24.67	30	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+M	Inner_1RB_Right	22.59	1.88	24.47	30	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+M	Inner_Full	22.79	1.88	24.67	30	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+M	Outer_Full	22.83	1.88	24.71	30	PASS
DC_7A_n77A	30	5+20	DFT-256QAM	M+M	Inner_1RB_Left	20.76	1.88	22.64	30	PASS
DC_7A_n77A	30	5+20	DFT-256QAM	M+M	Inner_1RB_Right	20.63	1.88	22.51	30	PASS
DC_7A_n77A	30	5+20	DFT-256QAM	M+M	Inner_Full	20.89	1.88	22.77	30	PASS

DC_7A_n77A	30	5+20	DFT-256QAM	M+M	Outer_Full	20.92	1.88	22.8	30	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+H	Inner_1RB_Left	25.03	1.88	26.91	30	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+H	Inner_1RB_Right	25.2	1.88	27.08	30	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+H	Inner_Full	25.89	1.88	27.77	30	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+H	Outer_Full	25.41	1.88	27.29	30	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+H	Inner_1RB_Left	24.95	1.88	26.83	30	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+H	Inner_1RB_Right	25.24	1.88	27.12	30	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+H	Inner_Full	25.84	1.88	27.72	30	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+H	Outer_Full	25.56	1.88	27.44	30	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+H	Inner_1RB_Left	24.03	1.88	25.91	30	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+H	Inner_1RB_Right	24.15	1.88	26.03	30	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+H	Inner_Full	23.67	1.88	25.55	30	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+H	Outer_Full	22.79	1.88	24.67	30	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+H	Inner_1RB_Left	22.56	1.88	24.44	30	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+H	Inner_1RB_Right	22.75	1.88	24.63	30	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+H	Inner_Full	22.25	1.88	24.13	30	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+H	Outer_Full	22.27	1.88	24.15	30	PASS
DC_7A_n77A	30	5+20	DFT-256QAM	M+H	Inner_1RB_Left	20.09	1.88	21.97	30	PASS
DC_7A_n77A	30	5+20	DFT-256QAM	M+H	Inner_1RB_Right	20.34	1.88	22.22	30	PASS
DC_7A_n77A	30	5+20	DFT-256QAM	M+H	Inner_Full	20.35	1.88	22.23	30	PASS
DC_7A_n77A	30	5+20	DFT-256QAM	M+H	Outer_Full	20.22	1.88	22.1	30	PASS
DC_7A_n77A	30	5+25	DFT-BPSK	M+L	Inner_1RB_Left	26.2	1.88	28.08	30	PASS
DC_7A_n77A	30	5+25	DFT-BPSK	M+L	Inner_1RB_Right	26.16	1.88	28.04	30	PASS
DC_7A_n77A	30	5+25	DFT-BPSK	M+L	Inner_Full	26.07	1.88	27.95	30	PASS
DC_7A_n77A	30	5+25	DFT-BPSK	M+L	Outer_Full	25.49	1.88	27.37	30	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+L	Inner_1RB_Left	26.22	1.88	28.1	30	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+L	Inner_1RB_Right	26.1	1.88	27.98	30	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+L	Inner_Full	25.95	1.88	27.83	30	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+L	Outer_Full	25.53	1.88	27.41	30	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+L	Inner_1RB_Left	25.14	1.88	27.02	30	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+L	Inner_1RB_Right	24.99	1.88	26.87	30	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+L	Inner_Full	24.68	1.88	26.56	30	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+L	Outer_Full	23.87	1.88	25.75	30	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+L	Inner_1RB_Left	23.81	1.88	25.69	30	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+L	Inner_1RB_Right	23.64	1.88	25.52	30	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+L	Inner_Full	23.2	1.88	25.08	30	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+L	Outer_Full	23.35	1.88	25.23	30	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+L	Inner_1RB_Left	21.41	1.88	23.29	30	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+L	Inner_1RB_Right	21.29	1.88	23.17	30	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+L	Inner_Full	21.28	1.88	23.16	30	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+L	Outer_Full	21.41	1.88	23.29	30	PASS

DC_7A_n77A	30	5+25	DFT-BPSK	M+M	Inner_1RB_Left	25.97	1.88	27.85	30	PASS
DC_7A_n77A	30	5+25	DFT-BPSK	M+M	Inner_1RB_Right	25.67	1.88	27.55	30	PASS
DC_7A_n77A	30	5+25	DFT-BPSK	M+M	Inner_Full	25.58	1.88	27.46	30	PASS
DC_7A_n77A	30	5+25	DFT-BPSK	M+M	Outer_Full	25.59	1.88	27.47	30	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+M	Inner_1RB_Left	26.02	1.88	27.9	30	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+M	Inner_1RB_Right	25.75	1.88	27.63	30	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+M	Inner_Full	25.35	1.88	27.23	30	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+M	Outer_Full	25.48	1.88	27.36	30	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+M	Inner_1RB_Left	24.58	1.88	26.46	30	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+M	Inner_1RB_Right	24.38	1.88	26.26	30	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+M	Inner_Full	24.28	1.88	26.16	30	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+M	Outer_Full	23.48	1.88	25.36	30	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+M	Inner_1RB_Left	23.7	1.88	25.58	30	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+M	Inner_1RB_Right	23.49	1.88	25.37	30	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+M	Inner_Full	22.74	1.88	24.62	30	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+M	Outer_Full	22.92	1.88	24.8	30	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+M	Inner_1RB_Left	21.47	1.88	23.35	30	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+M	Inner_1RB_Right	21.22	1.88	23.1	30	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+M	Inner_Full	20.92	1.88	22.8	30	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+M	Outer_Full	21.04	1.88	22.92	30	PASS
DC_7A_n77A	30	5+25	DFT-BPSK	M+H	Inner_1RB_Left	25.36	1.88	27.24	30	PASS
DC_7A_n77A	30	5+25	DFT-BPSK	M+H	Inner_1RB_Right	25.44	1.88	27.32	30	PASS
DC_7A_n77A	30	5+25	DFT-BPSK	M+H	Inner_Full	25.54	1.88	27.42	30	PASS
DC_7A_n77A	30	5+25	DFT-BPSK	M+H	Outer_Full	25.58	1.88	27.46	30	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+H	Inner_1RB_Left	25.11	1.88	26.99	30	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+H	Inner_1RB_Right	25.18	1.88	27.06	30	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+H	Inner_Full	25.89	1.88	27.77	30	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+H	Outer_Full	25.53	1.88	27.41	30	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+H	Inner_1RB_Left	24.19	1.88	26.07	30	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+H	Inner_1RB_Right	24.32	1.88	26.2	30	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+H	Inner_Full	23.88	1.88	25.76	30	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+H	Outer_Full	23.03	1.88	24.91	30	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+H	Inner_1RB_Left	22.72	1.88	24.6	30	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+H	Inner_1RB_Right	22.87	1.88	24.75	30	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+H	Inner_Full	22.38	1.88	24.26	30	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+H	Outer_Full	22.46	1.88	24.34	30	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+H	Inner_1RB_Left	20.59	1.88	22.47	30	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+H	Inner_1RB_Right	20.81	1.88	22.69	30	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+H	Inner_Full	20.54	1.88	22.42	30	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+H	Outer_Full	20.58	1.88	22.46	30	PASS
DC_7A_n77A	30	5+30	DFT-BPSK	M+L	Inner_1RB_Left	26.09	1.88	27.97	30	PASS

DC_7A_n77A	30	5+30	DFT-BPSK	M+L	Inner_1RB_Right	25.93	1.88	27.81	30	PASS
DC_7A_n77A	30	5+30	DFT-BPSK	M+L	Inner_Full	25.95	1.88	27.83	30	PASS
DC_7A_n77A	30	5+30	DFT-BPSK	M+L	Outer_Full	25.56	1.88	27.44	30	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+L	Inner_1RB_Left	26.15	1.88	28.03	30	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+L	Inner_1RB_Right	26.09	1.88	27.97	30	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+L	Inner_Full	25.91	1.88	27.79	30	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+L	Outer_Full	25.97	1.88	27.85	30	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+L	Inner_1RB_Left	24.96	1.88	26.84	30	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+L	Inner_1RB_Right	24.89	1.88	26.77	30	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+L	Inner_Full	24.77	1.88	26.65	30	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+L	Outer_Full	23.93	1.88	25.81	30	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+L	Inner_1RB_Left	23.59	1.88	25.47	30	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+L	Inner_1RB_Right	23.47	1.88	25.35	30	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+L	Inner_Full	23.27	1.88	25.15	30	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+L	Outer_Full	23.36	1.88	25.24	30	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+L	Inner_1RB_Left	21.77	1.88	23.65	30	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+L	Inner_1RB_Right	21.61	1.88	23.49	30	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+L	Inner_Full	21.24	1.88	23.12	30	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+L	Outer_Full	21.37	1.88	23.25	30	PASS
DC_7A_n77A	30	5+30	DFT-BPSK	M+M	Inner_1RB_Left	25.88	1.88	27.76	30	PASS
DC_7A_n77A	30	5+30	DFT-BPSK	M+M	Inner_1RB_Right	25.5	1.88	27.38	30	PASS
DC_7A_n77A	30	5+30	DFT-BPSK	M+M	Inner_Full	25.64	1.88	27.52	30	PASS
DC_7A_n77A	30	5+30	DFT-BPSK	M+M	Outer_Full	25.21	1.88	27.09	30	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+M	Inner_1RB_Left	25.89	1.88	27.77	30	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+M	Inner_1RB_Right	25.51	1.88	27.39	30	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+M	Inner_Full	25.44	1.88	27.32	30	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+M	Outer_Full	25.55	1.88	27.43	30	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+M	Inner_1RB_Left	24.92	1.88	26.8	30	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+M	Inner_1RB_Right	24.66	1.88	26.54	30	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+M	Inner_Full	24.27	1.88	26.15	30	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+M	Outer_Full	23.4	1.88	25.28	30	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+M	Inner_1RB_Left	23.01	1.88	24.89	30	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+M	Inner_1RB_Right	22.63	1.88	24.51	30	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+M	Inner_Full	22.89	1.88	24.77	30	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+M	Outer_Full	23.08	1.88	24.96	30	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+M	Inner_1RB_Left	21.13	1.88	23.01	30	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+M	Inner_1RB_Right	20.75	1.88	22.63	30	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+M	Inner_Full	20.89	1.88	22.77	30	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+M	Outer_Full	21.02	1.88	22.9	30	PASS
DC_7A_n77A	30	5+30	DFT-BPSK	M+H	Inner_1RB_Left	25.17	1.88	27.05	30	PASS
DC_7A_n77A	30	5+30	DFT-BPSK	M+H	Inner_1RB_Right	25.1	1.88	26.98	30	PASS

DC_7A_n77A	30	5+30	DFT-BPSK	M+H	Inner_Full	25.56	1.88	27.44	30	PASS
DC_7A_n77A	30	5+30	DFT-BPSK	M+H	Outer_Full	25.63	1.88	27.51	30	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+H	Inner_1RB_Left	25.62	1.88	27.5	30	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+H	Inner_1RB_Right	25.49	1.88	27.37	30	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+H	Inner_Full	25.52	1.88	27.4	30	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+H	Outer_Full	25.59	1.88	27.47	30	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+H	Inner_1RB_Left	24.81	1.88	26.69	30	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+H	Inner_1RB_Right	24.69	1.88	26.57	30	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+H	Inner_Full	23.84	1.88	25.72	30	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+H	Outer_Full	23.04	1.88	24.92	30	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+H	Inner_1RB_Left	22.89	1.88	24.77	30	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+H	Inner_1RB_Right	22.87	1.88	24.75	30	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+H	Inner_Full	22.4	1.88	24.28	30	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+H	Outer_Full	22.52	1.88	24.4	30	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+H	Inner_1RB_Left	20.42	1.88	22.3	30	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+H	Inner_1RB_Right	20.37	1.88	22.25	30	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+H	Inner_Full	20.44	1.88	22.32	30	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+H	Outer_Full	20.52	1.88	22.4	30	PASS
DC_7A_n77A	30	5+40	DFT-BPSK	M+L	Inner_1RB_Left	25.99	1.88	27.87	30	PASS
DC_7A_n77A	30	5+40	DFT-BPSK	M+L	Inner_1RB_Right	25.64	1.88	27.52	30	PASS
DC_7A_n77A	30	5+40	DFT-BPSK	M+L	Inner_Full	26	1.88	27.88	30	PASS
DC_7A_n77A	30	5+40	DFT-BPSK	M+L	Outer_Full	25.45	1.88	27.33	30	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+L	Inner_1RB_Left	26.07	1.88	27.95	30	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+L	Inner_1RB_Right	25.83	1.88	27.71	30	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+L	Inner_Full	25.95	1.88	27.83	30	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+L	Outer_Full	25.56	1.88	27.44	30	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+L	Inner_1RB_Left	24.86	1.88	26.74	30	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+L	Inner_1RB_Right	24.56	1.88	26.44	30	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+L	Inner_Full	24.84	1.88	26.72	30	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+L	Outer_Full	23.93	1.88	25.81	30	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+L	Inner_1RB_Left	23.51	1.88	25.39	30	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+L	Inner_1RB_Right	23.13	1.88	25.01	30	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+L	Inner_Full	23.39	1.88	25.27	30	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+L	Outer_Full	23.44	1.88	25.32	30	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+L	Inner_1RB_Left	21.59	1.88	23.47	30	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+L	Inner_1RB_Right	21.26	1.88	23.14	30	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+L	Inner_Full	21.41	1.88	23.29	30	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+L	Outer_Full	21.42	1.88	23.3	30	PASS
DC_7A_n77A	30	5+40	DFT-BPSK	M+M	Inner_1RB_Left	25.94	1.88	27.82	30	PASS
DC_7A_n77A	30	5+40	DFT-BPSK	M+M	Inner_1RB_Right	25.37	1.88	27.25	30	PASS
DC_7A_n77A	30	5+40	DFT-BPSK	M+M	Inner_Full	25.59	1.88	27.47	30	PASS

DC_7A_n77A	30	5+40	DFT-BPSK	M+M	Outer_Full	25.13	1.88	27.01	30	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+M	Inner_1RB_Left	26.06	1.88	27.94	30	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+M	Inner_1RB_Right	25.53	1.88	27.41	30	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+M	Inner_Full	25.46	1.88	27.34	30	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+M	Outer_Full	25.53	1.88	27.41	30	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+M	Inner_1RB_Left	24.74	1.88	26.62	30	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+M	Inner_1RB_Right	24.16	1.88	26.04	30	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+M	Inner_Full	24.29	1.88	26.17	30	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+M	Outer_Full	23.5	1.88	25.38	30	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+M	Inner_1RB_Left	23.54	1.88	25.42	30	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+M	Inner_1RB_Right	23.05	1.88	24.93	30	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+M	Inner_Full	22.98	1.88	24.86	30	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+M	Outer_Full	23.03	1.88	24.91	30	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+M	Inner_1RB_Left	21.54	1.88	23.42	30	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+M	Inner_1RB_Right	21.02	1.88	22.9	30	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+M	Inner_Full	20.83	1.88	22.71	30	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+M	Outer_Full	21.09	1.88	22.97	30	PASS
DC_7A_n77A	30	5+40	DFT-BPSK	M+H	Inner_1RB_Left	25.61	1.88	27.49	30	PASS
DC_7A_n77A	30	5+40	DFT-BPSK	M+H	Inner_1RB_Right	25.36	1.88	27.24	30	PASS
DC_7A_n77A	30	5+40	DFT-BPSK	M+H	Inner_Full	25.13	1.88	27.01	30	PASS
DC_7A_n77A	30	5+40	DFT-BPSK	M+H	Outer_Full	25.73	1.88	27.61	30	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+H	Inner_1RB_Left	25.63	1.88	27.51	30	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+H	Inner_1RB_Right	25.32	1.88	27.2	30	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+H	Inner_Full	25.51	1.88	27.39	30	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+H	Outer_Full	25.58	1.88	27.46	30	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+H	Inner_1RB_Left	24.46	1.88	26.34	30	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+H	Inner_1RB_Right	24.16	1.88	26.04	30	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+H	Inner_Full	23.89	1.88	25.77	30	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+H	Outer_Full	23.03	1.88	24.91	30	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+H	Inner_1RB_Left	23.08	1.88	24.96	30	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+H	Inner_1RB_Right	22.86	1.88	24.74	30	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+H	Inner_Full	22.54	1.88	24.42	30	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+H	Outer_Full	22.64	1.88	24.52	30	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+H	Inner_1RB_Left	21	1.88	22.88	30	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+H	Inner_1RB_Right	20.7	1.88	22.58	30	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+H	Inner_Full	20.49	1.88	22.37	30	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+H	Outer_Full	20.56	1.88	22.44	30	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+L	Inner_1RB_Left	26.19	1.88	28.07	30	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+L	Inner_1RB_Right	25.56	1.88	27.44	30	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+L	Inner_Full	26.22	1.88	28.1	30	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+L	Outer_Full	25.67	1.88	27.55	30	PASS

DC_7A_n77A	30	5+50	DFT-QPSK	M+L	Inner_1RB_Left	26.25	1.88	28.13	30	PASS
DC_7A_n77A	30	5+50	DFT-QPSK	M+L	Inner_1RB_Right	25.64	1.88	27.52	30	PASS
DC_7A_n77A	30	5+50	DFT-QPSK	M+L	Inner_Full	26.16	1.88	28.04	30	PASS
DC_7A_n77A	30	5+50	DFT-QPSK	M+L	Outer_Full	25.16	1.88	27.04	30	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+L	Inner_1RB_Left	25.01	1.88	26.89	30	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+L	Inner_1RB_Right	24.4	1.88	26.28	30	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+L	Inner_Full	25.05	1.88	26.93	30	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+L	Outer_Full	23.93	1.88	25.81	30	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+L	Inner_1RB_Left	23.75	1.88	25.63	30	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+L	Inner_1RB_Right	23.12	1.88	25	30	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+L	Inner_Full	23.47	1.88	25.35	30	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+L	Outer_Full	23.47	1.88	25.35	30	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+L	Inner_1RB_Left	21.74	1.88	23.62	30	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+L	Inner_1RB_Right	21.21	1.88	23.09	30	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+L	Inner_Full	21.53	1.88	23.41	30	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+L	Outer_Full	21.52	1.88	23.4	30	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+M	Inner_1RB_Left	26.1	1.88	27.98	30	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+M	Inner_1RB_Right	25.41	1.88	27.29	30	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+M	Inner_Full	25.79	1.88	27.67	30	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+M	Outer_Full	25.24	1.88	27.12	30	PASS
DC_7A_n77A	30	5+50	DFT-QPSK	M+M	Inner_1RB_Left	26.01	1.88	27.89	30	PASS
DC_7A_n77A	30	5+50	DFT-QPSK	M+M	Inner_1RB_Right	25.35	1.88	27.23	30	PASS
DC_7A_n77A	30	5+50	DFT-QPSK	M+M	Inner_Full	25.73	1.88	27.61	30	PASS
DC_7A_n77A	30	5+50	DFT-QPSK	M+M	Outer_Full	25.8	1.88	27.68	30	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+M	Inner_1RB_Left	25	1.88	26.88	30	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+M	Inner_1RB_Right	24.34	1.88	26.22	30	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+M	Inner_Full	24.57	1.88	26.45	30	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+M	Outer_Full	23.67	1.88	25.55	30	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+M	Inner_1RB_Left	23.51	1.88	25.39	30	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+M	Inner_1RB_Right	22.82	1.88	24.7	30	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+M	Inner_Full	23.14	1.88	25.02	30	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+M	Outer_Full	23.17	1.88	25.05	30	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+M	Inner_1RB_Left	21.4	1.88	23.28	30	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+M	Inner_1RB_Right	20.72	1.88	22.6	30	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+M	Inner_Full	21.1	1.88	22.98	30	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+M	Outer_Full	21.2	1.88	23.08	30	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+H	Inner_1RB_Left	25.64	1.88	27.52	30	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+H	Inner_1RB_Right	25.47	1.88	27.35	30	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+H	Inner_Full	25.45	1.88	27.33	30	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+H	Outer_Full	25.57	1.88	27.45	30	PASS
DC_7A_n77A	30	5+50	DFT-QPSK	M+H	Inner_1RB_Left	25.61	1.88	27.49	30	PASS

DC_7A_n77A	30	5+50	DFT-QPSK	M+H	Inner_1RB_Right	25.54	1.88	27.42	30	PASS
DC_7A_n77A	30	5+50	DFT-QPSK	M+H	Inner_Full	25.38	1.88	27.26	30	PASS
DC_7A_n77A	30	5+50	DFT-QPSK	M+H	Outer_Full	25.42	1.88	27.3	30	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+H	Inner_1RB_Left	24.59	1.88	26.47	30	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+H	Inner_1RB_Right	24.57	1.88	26.45	30	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+H	Inner_Full	24.29	1.88	26.17	30	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+H	Outer_Full	23.29	1.88	25.17	30	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+H	Inner_1RB_Left	23.04	1.88	24.92	30	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+H	Inner_1RB_Right	22.9	1.88	24.78	30	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+H	Inner_Full	22.79	1.88	24.67	30	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+H	Outer_Full	22.86	1.88	24.74	30	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+H	Inner_1RB_Left	20.89	1.88	22.77	30	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+H	Inner_1RB_Right	20.89	1.88	22.77	30	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+H	Inner_Full	20.75	1.88	22.63	30	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+H	Outer_Full	20.9	1.88	22.78	30	PASS
DC_7A_n77A	30	5+60	DFT-BPSK	M+L	Inner_1RB_Left	25.96	1.88	27.84	30	PASS
DC_7A_n77A	30	5+60	DFT-BPSK	M+L	Inner_1RB_Right	25.52	1.88	27.4	30	PASS
DC_7A_n77A	30	5+60	DFT-BPSK	M+L	Inner_Full	25.87	1.88	27.75	30	PASS
DC_7A_n77A	30	5+60	DFT-BPSK	M+L	Outer_Full	25.24	1.88	27.12	30	PASS
DC_7A_n77A	30	5+60	DFT-QPSK	M+L	Inner_1RB_Left	26.04	1.88	27.92	30	PASS
DC_7A_n77A	30	5+60	DFT-QPSK	M+L	Inner_1RB_Right	25.52	1.88	27.4	30	PASS
DC_7A_n77A	30	5+60	DFT-QPSK	M+L	Inner_Full	25.7	1.88	27.58	30	PASS
DC_7A_n77A	30	5+60	DFT-QPSK	M+L	Outer_Full	25.64	1.88	27.52	30	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+L	Inner_1RB_Left	24.81	1.88	26.69	30	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+L	Inner_1RB_Right	24.08	1.88	25.96	30	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+L	Inner_Full	24.64	1.88	26.52	30	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+L	Outer_Full	23.65	1.88	25.53	30	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+L	Inner_1RB_Left	23.35	1.88	25.23	30	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+L	Inner_1RB_Right	22.78	1.88	24.66	30	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+L	Inner_Full	23.19	1.88	25.07	30	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+L	Outer_Full	23.09	1.88	24.97	30	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+L	Inner_1RB_Left	21.46	1.88	23.34	30	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+L	Inner_1RB_Right	20.88	1.88	22.76	30	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+L	Inner_Full	21.25	1.88	23.13	30	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+L	Outer_Full	21.16	1.88	23.04	30	PASS
DC_7A_n77A	30	5+60	DFT-BPSK	M+M	Inner_1RB_Left	25.7	1.88	27.58	30	PASS
DC_7A_n77A	30	5+60	DFT-BPSK	M+M	Inner_1RB_Right	25	1.88	26.88	30	PASS
DC_7A_n77A	30	5+60	DFT-BPSK	M+M	Inner_Full	25.42	1.88	27.3	30	PASS
DC_7A_n77A	30	5+60	DFT-BPSK	M+M	Outer_Full	25.58	1.88	27.46	30	PASS
DC_7A_n77A	30	5+60	DFT-QPSK	M+M	Inner_1RB_Left	25.71	1.88	27.59	30	PASS
DC_7A_n77A	30	5+60	DFT-QPSK	M+M	Inner_1RB_Right	24.97	1.88	26.85	30	PASS

DC_7A_n77A	30	5+60	DFT-QPSK	M+M	Inner_Full	25.31	1.88	27.19	30	PASS
DC_7A_n77A	30	5+60	DFT-QPSK	M+M	Outer_Full	25.46	1.88	27.34	30	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+M	Inner_1RB_Left	24.43	1.88	26.31	30	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+M	Inner_1RB_Right	23.69	1.88	25.57	30	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+M	Inner_Full	24.3	1.88	26.18	30	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+M	Outer_Full	23.44	1.88	25.32	30	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+M	Inner_1RB_Left	23.38	1.88	25.26	30	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+M	Inner_1RB_Right	22.67	1.88	24.55	30	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+M	Inner_Full	22.84	1.88	24.72	30	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+M	Outer_Full	23.03	1.88	24.91	30	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+M	Inner_1RB_Left	21.25	1.88	23.13	30	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+M	Inner_1RB_Right	20.5	1.88	22.38	30	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+M	Inner_Full	20.88	1.88	22.76	30	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+M	Outer_Full	20.91	1.88	22.79	30	PASS
DC_7A_n77A	30	5+60	DFT-BPSK	M+H	Inner_1RB_Left	25.56	1.88	27.44	30	PASS
DC_7A_n77A	30	5+60	DFT-BPSK	M+H	Inner_1RB_Right	25.22	1.88	27.1	30	PASS
DC_7A_n77A	30	5+60	DFT-BPSK	M+H	Inner_Full	25.23	1.88	27.11	30	PASS
DC_7A_n77A	30	5+60	DFT-BPSK	M+H	Outer_Full	25.69	1.88	27.57	30	PASS
DC_7A_n77A	30	5+60	DFT-QPSK	M+H	Inner_1RB_Left	25.57	1.88	27.45	30	PASS
DC_7A_n77A	30	5+60	DFT-QPSK	M+H	Inner_1RB_Right	25.28	1.88	27.16	30	PASS
DC_7A_n77A	30	5+60	DFT-QPSK	M+H	Inner_Full	25.56	1.88	27.44	30	PASS
DC_7A_n77A	30	5+60	DFT-QPSK	M+H	Outer_Full	25.53	1.88	27.41	30	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+H	Inner_1RB_Left	24.46	1.88	26.34	30	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+H	Inner_1RB_Right	24.12	1.88	26	30	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+H	Inner_Full	24.56	1.88	26.44	30	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+H	Outer_Full	23.17	1.88	25.05	30	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+H	Inner_1RB_Left	23.14	1.88	25.02	30	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+H	Inner_1RB_Right	22.77	1.88	24.65	30	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+H	Inner_Full	22.63	1.88	24.51	30	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+H	Outer_Full	22.71	1.88	24.59	30	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+H	Inner_1RB_Left	21.08	1.88	22.96	30	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+H	Inner_1RB_Right	20.73	1.88	22.61	30	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+H	Inner_Full	20.61	1.88	22.49	30	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+H	Outer_Full	20.65	1.88	22.53	30	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+L	Inner_1RB_Left	26.15	1.88	28.03	30	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+L	Inner_1RB_Right	25.36	1.88	27.24	30	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+L	Inner_Full	26.02	1.88	27.9	30	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+L	Outer_Full	25.27	1.88	27.15	30	PASS
DC_7A_n77A	30	5+70	DFT-QPSK	M+L	Inner_1RB_Left	26.05	1.88	27.93	30	PASS
DC_7A_n77A	30	5+70	DFT-QPSK	M+L	Inner_1RB_Right	25.38	1.88	27.26	30	PASS
DC_7A_n77A	30	5+70	DFT-QPSK	M+L	Inner_Full	25.83	1.88	27.71	30	PASS

DC_7A_n77A	30	5+70	DFT-QPSK	M+L	Outer_Full	25.51	1.88	27.39	30	PASS
DC_7A_n77A	30	5+70	DFT-16QAM	M+L	Inner_1RB_Left	24.75	1.88	26.63	30	PASS
DC_7A_n77A	30	5+70	DFT-16QAM	M+L	Inner_1RB_Right	24.17	1.88	26.05	30	PASS
DC_7A_n77A	30	5+70	DFT-16QAM	M+L	Inner_Full	24.68	1.88	26.56	30	PASS
DC_7A_n77A	30	5+70	DFT-16QAM	M+L	Outer_Full	23.61	1.88	25.49	30	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+L	Inner_1RB_Left	23.54	1.88	25.42	30	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+L	Inner_1RB_Right	22.82	1.88	24.7	30	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+L	Inner_Full	23.26	1.88	25.14	30	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+L	Outer_Full	23.17	1.88	25.05	30	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+L	Inner_1RB_Left	21.48	1.88	23.36	30	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+L	Inner_1RB_Right	20.94	1.88	22.82	30	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+L	Inner_Full	21.2	1.88	23.08	30	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+L	Outer_Full	21.16	1.88	23.04	30	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+M	Inner_1RB_Left	25.8	1.88	27.68	30	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+M	Inner_1RB_Right	24.99	1.88	26.87	30	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+M	Inner_Full	25.74	1.88	27.62	30	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+M	Outer_Full	25.17	1.88	27.05	30	PASS
DC_7A_n77A	30	5+70	DFT-QPSK	M+M	Inner_1RB_Left	25.67	1.88	27.55	30	PASS
DC_7A_n77A	30	5+70	DFT-QPSK	M+M	Inner_1RB_Right	24.89	1.88	26.77	30	PASS
DC_7A_n77A	30	5+70	DFT-QPSK	M+M	Inner_Full	25.67	1.88	27.55	30	PASS
DC_7A_n77A	30	5+70	DFT-QPSK	M+M	Outer_Full	25.67	1.88	27.55	30	PASS
DC_7A_n77A	30	5+70	DFT-16QAM	M+M	Inner_1RB_Left	24.75	1.88	26.63	30	PASS
DC_7A_n77A	30	5+70	DFT-16QAM	M+M	Inner_1RB_Right	23.85	1.88	25.73	30	PASS
DC_7A_n77A	30	5+70	DFT-16QAM	M+M	Inner_Full	24.51	1.88	26.39	30	PASS
DC_7A_n77A	30	5+70	DFT-16QAM	M+M	Outer_Full	23.57	1.88	25.45	30	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+M	Inner_1RB_Left	23.19	1.88	25.07	30	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+M	Inner_1RB_Right	22.31	1.88	24.19	30	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+M	Inner_Full	23.14	1.88	25.02	30	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+M	Outer_Full	23.09	1.88	24.97	30	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+M	Inner_1RB_Left	21.03	1.88	22.91	30	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+M	Inner_1RB_Right	20.2	1.88	22.08	30	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+M	Inner_Full	21.18	1.88	23.06	30	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+M	Outer_Full	21.14	1.88	23.02	30	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+H	Inner_1RB_Left	25.91	1.88	27.79	30	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+H	Inner_1RB_Right	25.25	1.88	27.13	30	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+H	Inner_Full	25.51	1.88	27.39	30	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+H	Outer_Full	25.88	1.88	27.76	30	PASS
DC_7A_n77A	30	5+70	DFT-QPSK	M+H	Inner_1RB_Left	25.94	1.88	27.82	30	PASS
DC_7A_n77A	30	5+70	DFT-QPSK	M+H	Inner_1RB_Right	25.22	1.88	27.1	30	PASS
DC_7A_n77A	30	5+70	DFT-QPSK	M+H	Inner_Full	25.37	1.88	27.25	30	PASS
DC_7A_n77A	30	5+70	DFT-QPSK	M+H	Outer_Full	25.34	1.88	27.22	30	PASS

DC_7A_n77A	30	5+70	DFT-16QAM	M+H	Inner_1RB_Left	24.86	1.88	26.74	30	PASS
DC_7A_n77A	30	5+70	DFT-16QAM	M+H	Inner_1RB_Right	24.46	1.88	26.34	30	PASS
DC_7A_n77A	30	5+70	DFT-16QAM	M+H	Inner_Full	24.26	1.88	26.14	30	PASS
DC_7A_n77A	30	5+70	DFT-16QAM	M+H	Outer_Full	23.26	1.88	25.14	30	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+H	Inner_1RB_Left	23.38	1.88	25.26	30	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+H	Inner_1RB_Right	22.74	1.88	24.62	30	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+H	Inner_Full	22.77	1.88	24.65	30	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+H	Outer_Full	22.76	1.88	24.64	30	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+H	Inner_1RB_Left	21.19	1.88	23.07	30	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+H	Inner_1RB_Right	20.58	1.88	22.46	30	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+H	Inner_Full	20.78	1.88	22.66	30	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+H	Outer_Full	20.87	1.88	22.75	30	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+L	Inner_1RB_Left	26.24	1.88	28.12	30	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+L	Inner_1RB_Right	25.27	1.88	27.15	30	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+L	Inner_Full	25.92	1.88	27.8	30	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+L	Outer_Full	25.34	1.88	27.22	30	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+L	Inner_1RB_Left	26.14	1.88	28.02	30	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+L	Inner_1RB_Right	25.27	1.88	27.15	30	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+L	Inner_Full	25.78	1.88	27.66	30	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+L	Outer_Full	25.65	1.88	27.53	30	PASS
DC_7A_n77A	30	5+80	DFT-16QAM	M+L	Inner_1RB_Left	24.91	1.88	26.79	30	PASS
DC_7A_n77A	30	5+80	DFT-16QAM	M+L	Inner_1RB_Right	24.01	1.88	25.89	30	PASS
DC_7A_n77A	30	5+80	DFT-16QAM	M+L	Inner_Full	24.64	1.88	26.52	30	PASS
DC_7A_n77A	30	5+80	DFT-16QAM	M+L	Outer_Full	23.59	1.88	25.47	30	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+L	Inner_1RB_Left	23.48	1.88	25.36	30	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+L	Inner_1RB_Right	22.56	1.88	24.44	30	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+L	Inner_Full	23.26	1.88	25.14	30	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+L	Outer_Full	23.21	1.88	25.09	30	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+L	Inner_1RB_Left	21.58	1.88	23.46	30	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+L	Inner_1RB_Right	20.71	1.88	22.59	30	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+L	Inner_Full	21.26	1.88	23.14	30	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+L	Outer_Full	21.17	1.88	23.05	30	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+M	Inner_1RB_Left	25.94	1.88	27.82	30	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+M	Inner_1RB_Right	24.99	1.88	26.87	30	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+M	Inner_Full	25.76	1.88	27.64	30	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+M	Outer_Full	25.51	1.88	27.39	30	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+M	Inner_1RB_Left	25.93	1.88	27.81	30	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+M	Inner_1RB_Right	25.08	1.88	26.96	30	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+M	Inner_Full	25.65	1.88	27.53	30	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+M	Outer_Full	25.67	1.88	27.55	30	PASS
DC_7A_n77A	30	5+80	DFT-16QAM	M+M	Inner_1RB_Left	24.7	1.88	26.58	30	PASS

DC_7A_n77A	30	5+80	DFT-16QAM	M+M	Inner_1RB_Right	23.73	1.88	25.61	30	PASS
DC_7A_n77A	30	5+80	DFT-16QAM	M+M	Inner_Full	24.53	1.88	26.41	30	PASS
DC_7A_n77A	30	5+80	DFT-16QAM	M+M	Outer_Full	23.55	1.88	25.43	30	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+M	Inner_1RB_Left	23.59	1.88	25.47	30	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+M	Inner_1RB_Right	22.73	1.88	24.61	30	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+M	Inner_Full	23.12	1.88	25	30	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+M	Outer_Full	23.07	1.88	24.95	30	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+M	Inner_1RB_Left	21.49	1.88	23.37	30	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+M	Inner_1RB_Right	20.69	1.88	22.57	30	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+M	Inner_Full	21.08	1.88	22.96	30	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+M	Outer_Full	21.04	1.88	22.92	30	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+H	Inner_1RB_Left	25.96	1.88	27.84	30	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+H	Inner_1RB_Right	25.4	1.88	27.28	30	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+H	Inner_Full	25.53	1.88	27.41	30	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+H	Outer_Full	25.52	1.88	27.4	30	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+H	Inner_1RB_Left	25.92	1.88	27.8	30	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+H	Inner_1RB_Right	25.26	1.88	27.14	30	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+H	Inner_Full	25.43	1.88	27.31	30	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+H	Outer_Full	25.45	1.88	27.33	30	PASS
DC_7A_n77A	30	5+80	DFT-16QAM	M+H	Inner_1RB_Left	24.85	1.88	26.73	30	PASS
DC_7A_n77A	30	5+80	DFT-16QAM	M+H	Inner_1RB_Right	24.29	1.88	26.17	30	PASS
DC_7A_n77A	30	5+80	DFT-16QAM	M+H	Inner_Full	24.34	1.88	26.22	30	PASS
DC_7A_n77A	30	5+80	DFT-16QAM	M+H	Outer_Full	23.42	1.88	25.3	30	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+H	Inner_1RB_Left	23.42	1.88	25.3	30	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+H	Inner_1RB_Right	22.87	1.88	24.75	30	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+H	Inner_Full	22.89	1.88	24.77	30	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+H	Outer_Full	22.95	1.88	24.83	30	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+H	Inner_1RB_Left	21.4	1.88	23.28	30	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+H	Inner_1RB_Right	20.88	1.88	22.76	30	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+H	Inner_Full	20.84	1.88	22.72	30	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+H	Outer_Full	20.89	1.88	22.77	30	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+L	Inner_1RB_Left	26.19	1.88	28.07	30	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+L	Inner_1RB_Right	25.07	1.88	26.95	30	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+L	Inner_Full	25.81	1.88	27.69	30	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+L	Outer_Full	25.31	1.88	27.19	30	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+L	Inner_1RB_Left	26.06	1.88	27.94	30	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+L	Inner_1RB_Right	25.2	1.88	27.08	30	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+L	Inner_Full	25.7	1.88	27.58	30	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+L	Outer_Full	25.71	1.88	27.59	30	PASS
DC_7A_n77A	30	5+90	DFT-16QAM	M+L	Inner_1RB_Left	24.89	1.88	26.77	30	PASS
DC_7A_n77A	30	5+90	DFT-16QAM	M+L	Inner_1RB_Right	23.88	1.88	25.76	30	PASS

DC_7A_n77A	30	5+90	DFT-16QAM	M+L	Inner_Full	24.61	1.88	26.49	30	PASS
DC_7A_n77A	30	5+90	DFT-16QAM	M+L	Outer_Full	23.67	1.88	25.55	30	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+L	Inner_1RB_Left	23.71	1.88	25.59	30	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+L	Inner_1RB_Right	22.51	1.88	24.39	30	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+L	Inner_Full	23.17	1.88	25.05	30	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+L	Outer_Full	23.2	1.88	25.08	30	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+L	Inner_1RB_Left	21.7	1.88	23.58	30	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+L	Inner_1RB_Right	20.53	1.88	22.41	30	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+L	Inner_Full	21.15	1.88	23.03	30	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+L	Outer_Full	21.14	1.88	23.02	30	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+M	Inner_1RB_Left	26.11	1.88	27.99	30	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+M	Inner_1RB_Right	25.14	1.88	27.02	30	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+M	Inner_Full	25.77	1.88	27.65	30	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+M	Outer_Full	25.17	1.88	27.05	30	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+M	Inner_1RB_Left	25.96	1.88	27.84	30	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+M	Inner_1RB_Right	25.05	1.88	26.93	30	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+M	Inner_Full	25.63	1.88	27.51	30	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+M	Outer_Full	25.56	1.88	27.44	30	PASS
DC_7A_n77A	30	5+90	DFT-16QAM	M+M	Inner_1RB_Left	25.03	1.88	26.91	30	PASS
DC_7A_n77A	30	5+90	DFT-16QAM	M+M	Inner_1RB_Right	24.07	1.88	25.95	30	PASS
DC_7A_n77A	30	5+90	DFT-16QAM	M+M	Inner_Full	24.56	1.88	26.44	30	PASS
DC_7A_n77A	30	5+90	DFT-16QAM	M+M	Outer_Full	23.54	1.88	25.42	30	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+M	Inner_1RB_Left	23.44	1.88	25.32	30	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+M	Inner_1RB_Right	22.5	1.88	24.38	30	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+M	Inner_Full	23.09	1.88	24.97	30	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+M	Outer_Full	23.05	1.88	24.93	30	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+M	Inner_1RB_Left	21.45	1.88	23.33	30	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+M	Inner_1RB_Right	20.52	1.88	22.4	30	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+M	Inner_Full	21.14	1.88	23.02	30	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+M	Outer_Full	21.1	1.88	22.98	30	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+H	Inner_1RB_Left	25.94	1.88	27.82	30	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+H	Inner_1RB_Right	25.51	1.88	27.39	30	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+H	Inner_Full	25.63	1.88	27.51	30	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+H	Outer_Full	25.57	1.88	27.45	30	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+H	Inner_1RB_Left	25.98	1.88	27.86	30	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+H	Inner_1RB_Right	25.44	1.88	27.32	30	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+H	Inner_Full	25.6	1.88	27.48	30	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+H	Outer_Full	25.51	1.88	27.39	30	PASS
DC_7A_n77A	30	5+90	DFT-16QAM	M+H	Inner_1RB_Left	24.98	1.88	26.86	30	PASS
DC_7A_n77A	30	5+90	DFT-16QAM	M+H	Inner_1RB_Right	24.46	1.88	26.34	30	PASS
DC_7A_n77A	30	5+90	DFT-16QAM	M+H	Inner_Full	24.44	1.88	26.32	30	PASS

DC_7A_n77A	30	5+90	DFT-16QAM	M+H	Outer_Full	23.45	1.88	25.33	30	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+H	Inner_1RB_Left	23.41	1.88	25.29	30	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+H	Inner_1RB_Right	22.8	1.88	24.68	30	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+H	Inner_Full	22.99	1.88	24.87	30	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+H	Outer_Full	23.02	1.88	24.9	30	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+H	Inner_1RB_Left	21.36	1.88	23.24	30	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+H	Inner_1RB_Right	20.71	1.88	22.59	30	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+H	Inner_Full	21.07	1.88	22.95	30	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+H	Outer_Full	21.01	1.88	22.89	30	PASS
DC_7A_n77A	30	5+100	DFT-BPSK	M+L	Inner_1RB_Left	26.17	1.88	28.05	30	PASS
DC_7A_n77A	30	5+100	DFT-BPSK	M+L	Inner_1RB_Right	25.48	1.88	27.36	30	PASS
DC_7A_n77A	30	5+100	DFT-BPSK	M+L	Inner_Full	25.77	1.88	27.65	30	PASS
DC_7A_n77A	30	5+100	DFT-BPSK	M+L	Outer_Full	25.19	1.88	27.07	30	PASS
DC_7A_n77A	30	5+100	DFT-QPSK	M+L	Inner_1RB_Left	26.12	1.88	28	30	PASS
DC_7A_n77A	30	5+100	DFT-QPSK	M+L	Inner_1RB_Right	25.42	1.88	27.3	30	PASS
DC_7A_n77A	30	5+100	DFT-QPSK	M+L	Inner_Full	25.63	1.88	27.51	30	PASS
DC_7A_n77A	30	5+100	DFT-QPSK	M+L	Outer_Full	25.67	1.88	27.55	30	PASS
DC_7A_n77A	30	5+100	DFT-16QAM	M+L	Inner_1RB_Left	25.01	1.88	26.89	30	PASS
DC_7A_n77A	30	5+100	DFT-16QAM	M+L	Inner_1RB_Right	24.12	1.88	26	30	PASS
DC_7A_n77A	30	5+100	DFT-16QAM	M+L	Inner_Full	24.58	1.88	26.46	30	PASS
DC_7A_n77A	30	5+100	DFT-16QAM	M+L	Outer_Full	23.61	1.88	25.49	30	PASS
DC_7A_n77A	30	5+100	DFT-64QAM	M+L	Inner_1RB_Left	23.5	1.88	25.38	30	PASS
DC_7A_n77A	30	5+100	DFT-64QAM	M+L	Inner_1RB_Right	22.75	1.88	24.63	30	PASS
DC_7A_n77A	30	5+100	DFT-64QAM	M+L	Inner_Full	23.13	1.88	25.01	30	PASS
DC_7A_n77A	30	5+100	DFT-64QAM	M+L	Outer_Full	23.11	1.88	24.99	30	PASS
DC_7A_n77A	30	5+100	DFT-256QAM	M+L	Inner_1RB_Left	21.61	1.88	23.49	30	PASS
DC_7A_n77A	30	5+100	DFT-256QAM	M+L	Inner_1RB_Right	20.93	1.88	22.81	30	PASS
DC_7A_n77A	30	5+100	DFT-256QAM	M+L	Inner_Full	21.13	1.88	23.01	30	PASS
DC_7A_n77A	30	5+100	DFT-256QAM	M+L	Outer_Full	21.08	1.88	22.96	30	PASS

Peak-to-Average Ratio

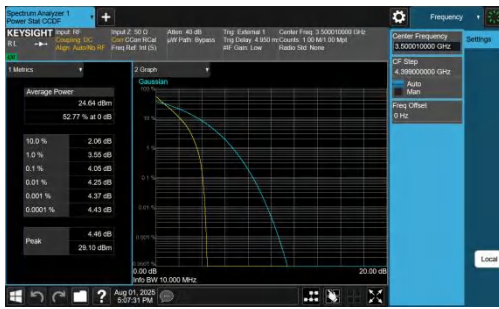
Peak-to-Average Ratio(CCDF)

Test Result

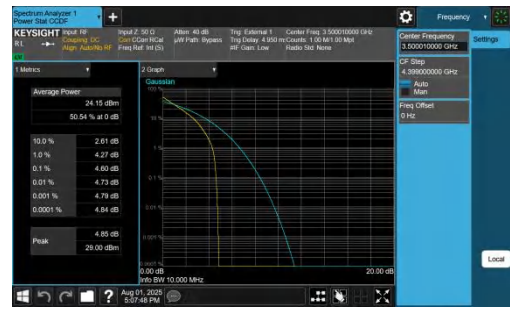
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (dB)	Limit (dB)	Verdict
DC_7A_n77A	30	5+10	DFT-BPSK	M+M	Outer_Full	4.05	≤13	PASS
DC_7A_n77A	30	5+10	DFT-QPSK	M+M	Outer_Full	4.6	≤13	PASS
DC_7A_n77A	30	5+10	DFT-16QAM	M+M	Outer_Full	5.58	≤13	PASS
DC_7A_n77A	30	5+10	DFT-64QAM	M+M	Outer_Full	5.99	≤13	PASS
DC_7A_n77A	30	5+10	DFT-256QAM	M+M	Outer_Full	6.48	≤13	PASS
DC_7A_n77A	30	5+15	DFT-BPSK	M+M	Outer_Full	4.02	≤13	PASS
DC_7A_n77A	30	5+15	DFT-QPSK	M+M	Outer_Full	4.58	≤13	PASS
DC_7A_n77A	30	5+15	DFT-16QAM	M+M	Outer_Full	5.47	≤13	PASS
DC_7A_n77A	30	5+15	DFT-64QAM	M+M	Outer_Full	6.04	≤13	PASS
DC_7A_n77A	30	5+15	DFT-256QAM	M+M	Outer_Full	6.72	≤13	PASS
DC_7A_n77A	30	5+20	DFT-BPSK	M+M	Outer_Full	4.01	≤13	PASS
DC_7A_n77A	30	5+20	DFT-QPSK	M+M	Outer_Full	4.57	≤13	PASS
DC_7A_n77A	30	5+20	DFT-16QAM	M+M	Outer_Full	5.51	≤13	PASS
DC_7A_n77A	30	5+20	DFT-64QAM	M+M	Outer_Full	5.97	≤13	PASS
DC_7A_n77A	30	5+20	DFT-256QAM	M+M	Outer_Full	6.67	≤13	PASS
DC_7A_n77A	30	5+25	DFT-BPSK	M+M	Outer_Full	4.11	≤13	PASS
DC_7A_n77A	30	5+25	DFT-QPSK	M+M	Outer_Full	4.69	≤13	PASS
DC_7A_n77A	30	5+25	DFT-16QAM	M+M	Outer_Full	5.86	≤13	PASS
DC_7A_n77A	30	5+25	DFT-64QAM	M+M	Outer_Full	6.23	≤13	PASS
DC_7A_n77A	30	5+25	DFT-256QAM	M+M	Outer_Full	6.82	≤13	PASS
DC_7A_n77A	30	5+30	DFT-BPSK	M+M	Outer_Full	3.97	≤13	PASS
DC_7A_n77A	30	5+30	DFT-QPSK	M+M	Outer_Full	4.55	≤13	PASS
DC_7A_n77A	30	5+30	DFT-16QAM	M+M	Outer_Full	5.5	≤13	PASS
DC_7A_n77A	30	5+30	DFT-64QAM	M+M	Outer_Full	6.03	≤13	PASS
DC_7A_n77A	30	5+30	DFT-256QAM	M+M	Outer_Full	6.78	≤13	PASS
DC_7A_n77A	30	5+40	DFT-BPSK	M+M	Outer_Full	4.04	≤13	PASS
DC_7A_n77A	30	5+40	DFT-QPSK	M+M	Outer_Full	4.57	≤13	PASS
DC_7A_n77A	30	5+40	DFT-16QAM	M+M	Outer_Full	5.57	≤13	PASS
DC_7A_n77A	30	5+40	DFT-64QAM	M+M	Outer_Full	6	≤13	PASS
DC_7A_n77A	30	5+40	DFT-256QAM	M+M	Outer_Full	6.78	≤13	PASS
DC_7A_n77A	30	5+50	DFT-BPSK	M+M	Outer_Full	4.16	≤13	PASS
DC_7A_n77A	30	5+50	DFT-QPSK	M+M	Outer_Full	4.71	≤13	PASS
DC_7A_n77A	30	5+50	DFT-16QAM	M+M	Outer_Full	5.68	≤13	PASS
DC_7A_n77A	30	5+50	DFT-64QAM	M+M	Outer_Full	6.12	≤13	PASS
DC_7A_n77A	30	5+50	DFT-256QAM	M+M	Outer_Full	6.82	≤13	PASS

DC_7A_n77A	30	5+60	DFT-BPSK	M+M	Outer_Full	4.32	≤13	PASS
DC_7A_n77A	30	5+60	DFT-QPSK	M+M	Outer_Full	4.83	≤13	PASS
DC_7A_n77A	30	5+60	DFT-16QAM	M+M	Outer_Full	5.78	≤13	PASS
DC_7A_n77A	30	5+60	DFT-64QAM	M+M	Outer_Full	6.23	≤13	PASS
DC_7A_n77A	30	5+60	DFT-256QAM	M+M	Outer_Full	6.77	≤13	PASS
DC_7A_n77A	30	5+70	DFT-BPSK	M+M	Outer_Full	4.27	≤13	PASS
DC_7A_n77A	30	5+70	DFT-QPSK	M+M	Outer_Full	4.76	≤13	PASS
DC_7A_n77A	30	5+70	DFT-16QAM	M+M	Outer_Full	5.82	≤13	PASS
DC_7A_n77A	30	5+70	DFT-64QAM	M+M	Outer_Full	6.16	≤13	PASS
DC_7A_n77A	30	5+70	DFT-256QAM	M+M	Outer_Full	6.79	≤13	PASS
DC_7A_n77A	30	5+80	DFT-BPSK	M+M	Outer_Full	4.31	≤13	PASS
DC_7A_n77A	30	5+80	DFT-QPSK	M+M	Outer_Full	4.89	≤13	PASS
DC_7A_n77A	30	5+80	DFT-16QAM	M+M	Outer_Full	5.84	≤13	PASS
DC_7A_n77A	30	5+80	DFT-64QAM	M+M	Outer_Full	6.32	≤13	PASS
DC_7A_n77A	30	5+80	DFT-256QAM	M+M	Outer_Full	6.82	≤13	PASS
DC_7A_n77A	30	5+90	DFT-BPSK	M+M	Outer_Full	4.42	≤13	PASS
DC_7A_n77A	30	5+90	DFT-QPSK	M+M	Outer_Full	4.79	≤13	PASS
DC_7A_n77A	30	5+90	DFT-16QAM	M+M	Outer_Full	5.74	≤13	PASS
DC_7A_n77A	30	5+90	DFT-64QAM	M+M	Outer_Full	6.16	≤13	PASS
DC_7A_n77A	30	5+90	DFT-256QAM	M+M	Outer_Full	6.85	≤13	PASS
DC_7A_n77A	30	5+100	DFT-BPSK	M+M	Outer_Full	4.42	≤13	PASS
DC_7A_n77A	30	5+100	DFT-QPSK	M+M	Outer_Full	4.98	≤13	PASS
DC_7A_n77A	30	5+100	DFT-16QAM	M+M	Outer_Full	5.96	≤13	PASS
DC_7A_n77A	30	5+100	DFT-64QAM	M+M	Outer_Full	6.41	≤13	PASS
DC_7A_n77A	30	5+100	DFT-256QAM	M+M	Outer_Full	6.79	≤13	PASS

Test Graphs



NTNV-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-QPSK-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-16QAM-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-64QAM-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-256QAM-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+15-M+M-QPSK-IDLE-DFT-BPSK-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+15-M+M-QPSK-IDLE-DFT-QPSK-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+15-M+M-QPSK-IDLE-DFT-16QAM-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+15-M+M-QPSK-IDLE-DFT-64QAM
-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+15-M+M-QPSK-IDLE-DFT-256QAM
-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+20-M+M-QPSK-IDLE-DFT-BPSK-
Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+20-M+M-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS



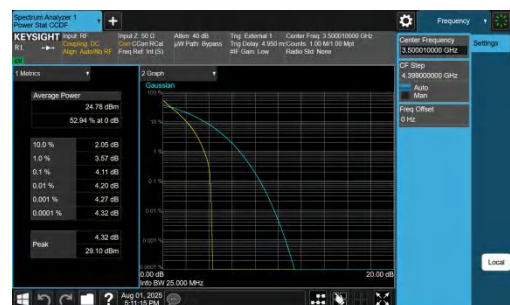
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-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+20-M+M-QPSK-IDLE-DFT-64QAM-
Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+20-M+M-QPSK-IDLE-DFT-256QA
M-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+25-M+M-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+25-M+M-QPSK-IDLE-DFT-QPSK-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+25-M+M-QPSK-IDLE-DFT-16QAM-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+25-M+M-QPSK-IDLE-DFT-64QAM-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+25-M+M-QPSK-IDLE-DFT-256QAM-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+30-M+M-QPSK-IDLE-DFT-BPSK-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+30-M+M-QPSK-IDLE-DFT-QPSK-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+30-M+M-QPSK-IDLE-DFT-16QAM-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+30-M+M-QPSK-IDLE-DFT-64QAM-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+30-M+M-QPSK-IDLE-DFT-256QA
M-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+40-M+M-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS



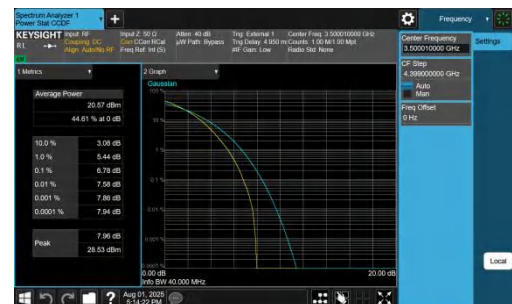
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Outer_Full-PASS



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Outer_Full-PASS



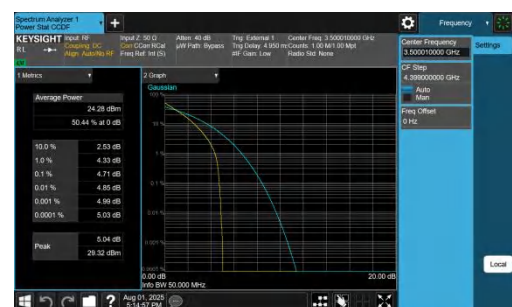
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-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+40-M+M-QPSK-IDLE-DFT-256QAM
-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+50-M+M-QPSK-IDLE-DFT-BPSK-
Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+50-M+M-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+50-M+M-QPSK-IDLE-DFT-16QAM
-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+50-M+M-QPSK-IDLE-DFT-64QAM-
Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+50-M+M-QPSK-IDLE-DFT-256QA
M-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+60-M+M-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+60-M+M-QPSK-IDLE-DFT-QPSK-
Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+60-M+M-QPSK-IDLE-DFT-16QAM-
Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+60-M+M-QPSK-IDLE-DFT-64QAM
-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+60-M+M-QPSK-IDLE-DFT-256QAM
-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+70-M+M-QPSK-IDLE-DFT-BPSK-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+70-M+M-QPSK-IDLE-DFT-QPSK-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+70-M+M-QPSK-IDLE-DFT-16QAM-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+70-M+M-QPSK-IDLE-DFT-64QAM-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+70-M+M-QPSK-IDLE-DFT-256QA M-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+80-M+M-QPSK-IDLE-DFT-BPSK-Outer_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+80-M+M-QPSK-IDLE-DFT-QPSK-Outter_Full-PASS



NTNV-DC_7A_n77A-PC2-30-5+80-M+M-QPSK-IDLE-DFT-16QAM-Outter_Full-PASS


NTNV-DC_7A_n77A-PC2-30-5+80-M+M-QPSK-IDLE-DFT-64QAM
-Outer_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+80-M+M-QPSK-IDLE-DFT-256QAM
-Outer_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+90-M+M-QPSK-IDLE-DFT-BPSK-
Outer_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+90-M+M-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+90-M+M-QPSK-IDLE-DFT-16QAM
-Outer_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+90-M+M-QPSK-IDLE-DFT-64QAM-
Outer_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+90-M+M-QPSK-IDLE-DFT-256QA
M-Outer_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+100-M+M-QPSK-IDLE-DFT-BPSK-
Outer_Full-PASS



NTVN-DC_7A_n77A-PC2-30-5+100-M+M-QPSK-IDLE-DFT-QPSK
-Outer_Full-PASS



NTVN-DC_7A_n77A-PC2-30-5+100-M+M-QPSK-IDLE-DFT-16QAM
-Outer_Full-PASS



NTVN-DC_7A_n77A-PC2-30-5+100-M+M-QPSK-IDLE-DFT-64QA
M-Outer_Full-PASS



NTVN-DC_7A_n77A-PC2-30-5+100-M+M-QPSK-IDLE-DFT-256QA
M-Outer_Full-PASS

26dB Bandwidth and Occupied Bandwidth

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_7A_n77A	30	5+10	CP-QPSK	M+M	Outer_Full	8.6251	10.08	PASS
DC_7A_n77A	30	5+10	CP-16QAM	M+M	Outer_Full	8.6893	10.07	PASS
DC_7A_n77A	30	5+10	CP-64QAM	M+M	Outer_Full	8.6223	10.09	PASS
DC_7A_n77A	30	5+10	CP-256QAM	M+M	Outer_Full	8.6519	9.839	PASS
DC_7A_n77A	30	5+15	CP-QPSK	M+M	Outer_Full	13.663	15.50	PASS
DC_7A_n77A	30	5+15	CP-16QAM	M+M	Outer_Full	13.688	15.50	PASS
DC_7A_n77A	30	5+15	CP-64QAM	M+M	Outer_Full	13.650	15.28	PASS
DC_7A_n77A	30	5+15	CP-256QAM	M+M	Outer_Full	13.696	15.24	PASS
DC_7A_n77A	30	5+20	CP-QPSK	M+M	Outer_Full	18.347	20.18	PASS
DC_7A_n77A	30	5+20	CP-16QAM	M+M	Outer_Full	18.323	20.26	PASS
DC_7A_n77A	30	5+20	CP-64QAM	M+M	Outer_Full	18.307	20.20	PASS
DC_7A_n77A	30	5+20	CP-256QAM	M+M	Outer_Full	18.294	20.34	PASS
DC_7A_n77A	30	5+25	CP-QPSK	M+M	Outer_Full	23.336	25.49	PASS
DC_7A_n77A	30	5+25	CP-16QAM	M+M	Outer_Full	23.318	25.36	PASS
DC_7A_n77A	30	5+25	CP-64QAM	M+M	Outer_Full	23.329	25.42	PASS
DC_7A_n77A	30	5+25	CP-256QAM	M+M	Outer_Full	23.308	25.43	PASS
DC_7A_n77A	30	5+30	CP-QPSK	M+M	Outer_Full	27.915	30.21	PASS
DC_7A_n77A	30	5+30	CP-16QAM	M+M	Outer_Full	27.972	30.29	PASS
DC_7A_n77A	30	5+30	CP-64QAM	M+M	Outer_Full	28.027	30.19	PASS
DC_7A_n77A	30	5+30	CP-256QAM	M+M	Outer_Full	27.904	30.14	PASS
DC_7A_n77A	30	5+40	CP-QPSK	M+M	Outer_Full	37.981	40.83	PASS
DC_7A_n77A	30	5+40	CP-16QAM	M+M	Outer_Full	37.966	40.63	PASS
DC_7A_n77A	30	5+40	CP-64QAM	M+M	Outer_Full	38.010	40.61	PASS
DC_7A_n77A	30	5+40	CP-256QAM	M+M	Outer_Full	37.953	40.62	PASS
DC_7A_n77A	30	5+50	CP-QPSK	M+M	Outer_Full	47.679	50.66	PASS
DC_7A_n77A	30	5+50	CP-16QAM	M+M	Outer_Full	47.563	50.71	PASS
DC_7A_n77A	30	5+50	CP-64QAM	M+M	Outer_Full	47.728	50.80	PASS
DC_7A_n77A	30	5+50	CP-256QAM	M+M	Outer_Full	47.547	50.53	PASS
DC_7A_n77A	30	5+60	CP-QPSK	M+M	Outer_Full	57.916	61.25	PASS
DC_7A_n77A	30	5+60	CP-16QAM	M+M	Outer_Full	58.080	61.40	PASS
DC_7A_n77A	30	5+60	CP-64QAM	M+M	Outer_Full	57.948	61.25	PASS
DC_7A_n77A	30	5+60	CP-256QAM	M+M	Outer_Full	57.960	61.40	PASS
DC_7A_n77A	30	5+70	CP-QPSK	M+M	Outer_Full	67.737	71.72	PASS
DC_7A_n77A	30	5+70	CP-16QAM	M+M	Outer_Full	67.670	71.55	PASS
DC_7A_n77A	30	5+70	CP-64QAM	M+M	Outer_Full	67.586	71.61	PASS
DC_7A_n77A	30	5+70	CP-256QAM	M+M	Outer_Full	67.627	71.51	PASS

DC_7A_n77A	30	5+80	CP-QPSK	M+M	Outer_Full	77.501	81.71	PASS
DC_7A_n77A	30	5+80	CP-16QAM	M+M	Outer_Full	77.707	81.81	PASS
DC_7A_n77A	30	5+80	CP-64QAM	M+M	Outer_Full	77.560	81.83	PASS
DC_7A_n77A	30	5+80	CP-256QAM	M+M	Outer_Full	77.608	81.67	PASS
DC_7A_n77A	30	5+90	CP-QPSK	M+M	Outer_Full	87.796	92.47	PASS
DC_7A_n77A	30	5+90	CP-16QAM	M+M	Outer_Full	87.815	92.31	PASS
DC_7A_n77A	30	5+90	CP-64QAM	M+M	Outer_Full	87.691	92.28	PASS
DC_7A_n77A	30	5+90	CP-256QAM	M+M	Outer_Full	87.685	92.38	PASS
DC_7A_n77A	30	5+100	CP-QPSK	M+M	Outer_Full	97.962	102.6	PASS
DC_7A_n77A	30	5+100	CP-16QAM	M+M	Outer_Full	97.734	102.6	PASS
DC_7A_n77A	30	5+100	CP-64QAM	M+M	Outer_Full	97.615	102.8	PASS
DC_7A_n77A	30	5+100	CP-256QAM	M+M	Outer_Full	97.516	102.6	PASS

Test Graphs



NTNV-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-Ant1-PASS



NTNV-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-Ant1-PASS



NTNV-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-CP-64QAM-O
Outer_Full-Ant1-PASS



NTNV-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-CP-256QAM-O
Outer_Full-Ant1-PASS



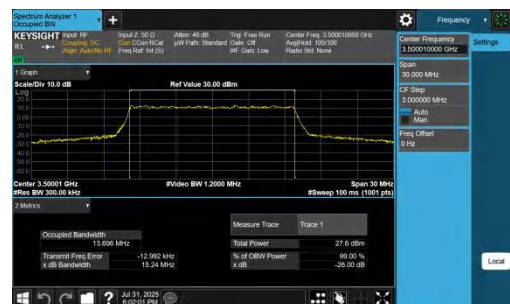
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uter_Full-Ant1-PASS



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uter_Full-Ant1-PASS



NTNV-DC_7A_n77A-PC2-30-5+15-M+M-QPSK-IDLE-CP-64QAM-O
Outer_Full-Ant1-PASS



NTNV-DC_7A_n77A-PC2-30-5+15-M+M-QPSK-IDLE-CP-256QAM-O
Outer_Full-Ant1-PASS



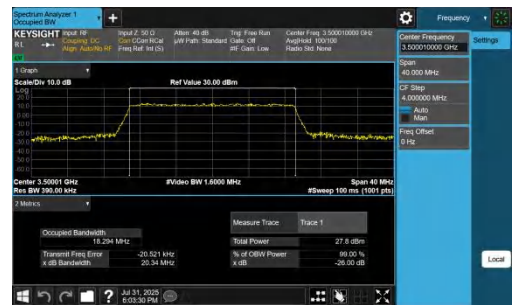
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uter_Full-Ant1-PASS



NTNV-DC_7A_n77A-PC2-30-5+20-M+M-QPSK-IDLE-CP-64QAM-
Outer_Full-Ant1-PASS



NTNV-DC_7A_n77A-PC2-30-5+20-M+M-QPSK-IDLE-CP-256QAM-
Outer_Full-Ant1-PASS



NTNV-DC_7A_n77A-PC2-30-5+25-M+M-QPSK-IDLE-CP-QPSK-O
uter_Full-Ant1-PASS



NTNV-DC_7A_n77A-PC2-30-5+25-M+M-QPSK-IDLE-CP-16QAM-O
uter_Full-Ant1-PASS



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Outer_Full-Ant1-PASS



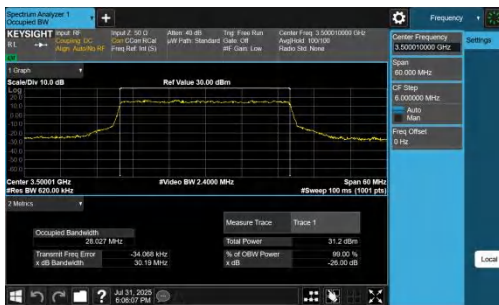
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Outer_Full-Ant1-PASS



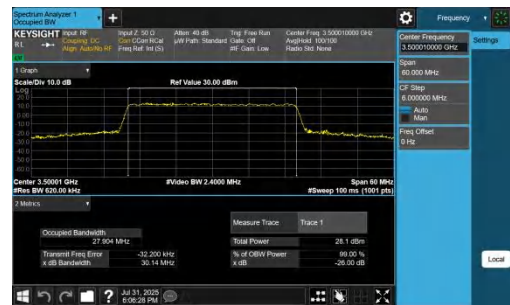
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uter_Full-Ant1-PASS



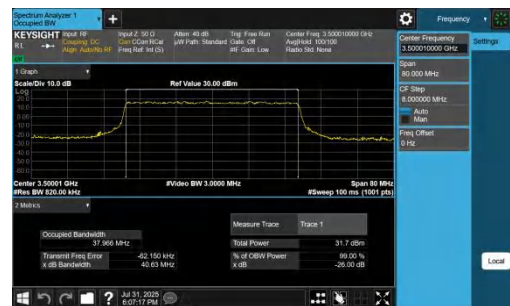
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Outer_Full-Ant1-PASS



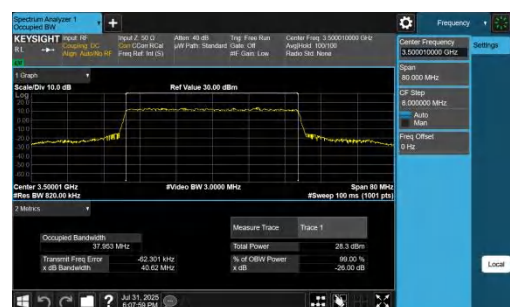
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uter_Full-Ant1-PASS



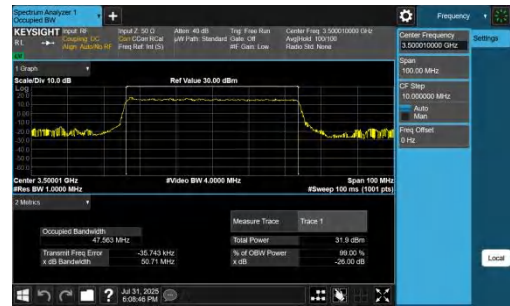
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Outer_Full-Ant1-PASS



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Outer_Full-Ant1-PASS



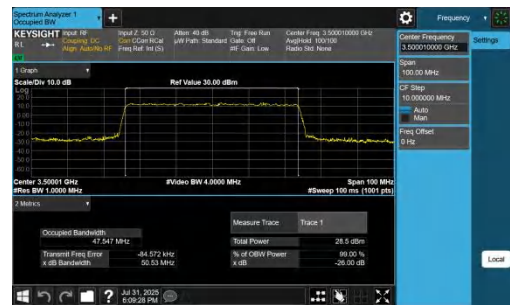
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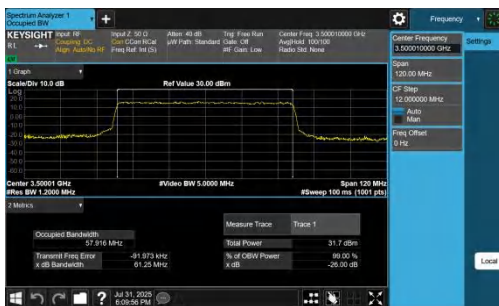
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uter_Full-Ant1-PASS



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Outer_Full-Ant1-PASS



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Outer_Full-Ant1-PASS



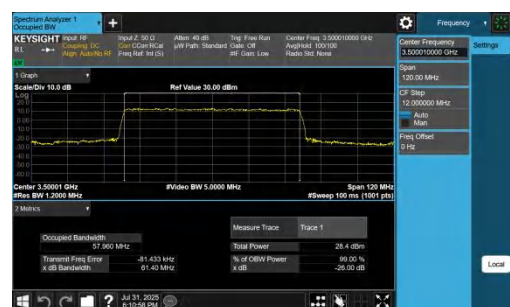
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uter_Full-Ant1-PASS



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uter_Full-Ant1-PASS



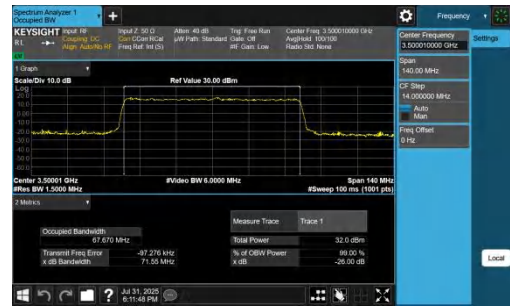
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Outer_Full-Ant1-PASS



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Outer_Full-Ant1-PASS



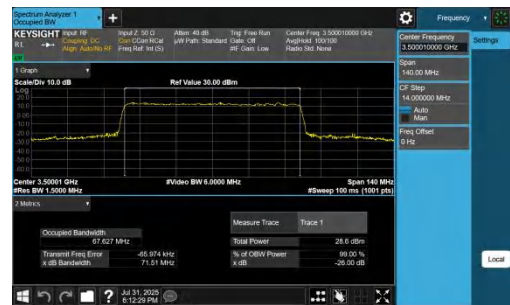
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uter_Full-Ant1-PASS



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uter_Full-Ant1-PASS



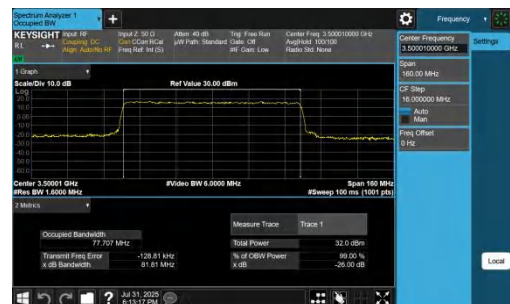
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Outer_Full-Ant1-PASS



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Outer_Full-Ant1-PASS



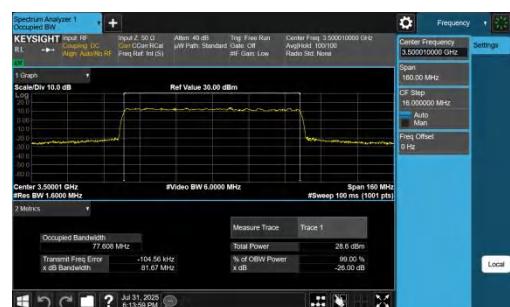
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uter_Full-Ant1-PASS



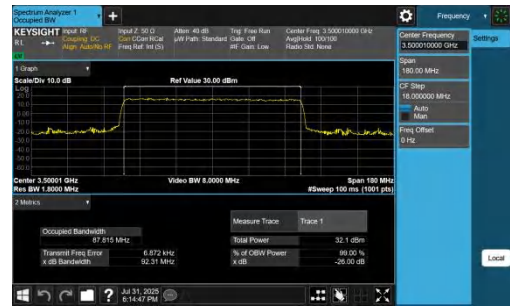
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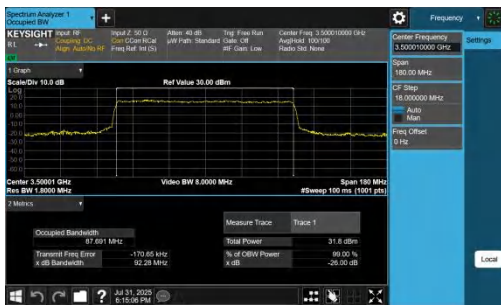
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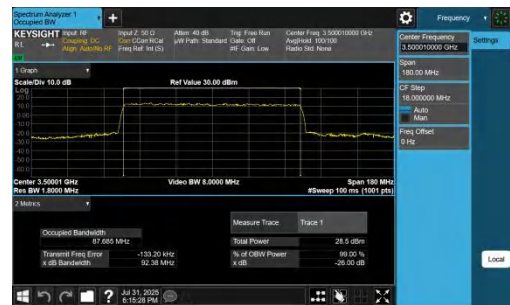
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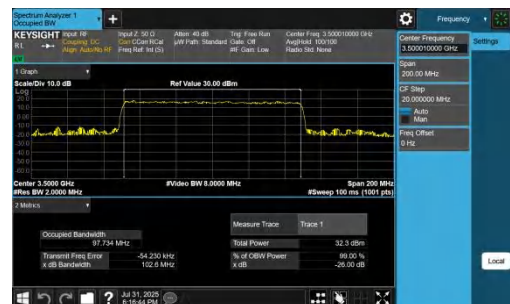
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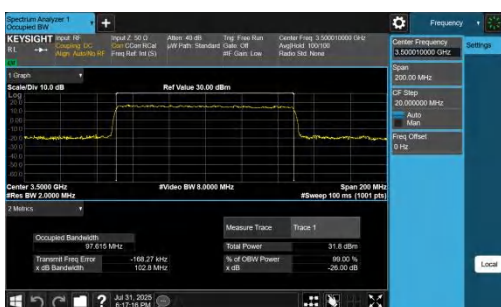
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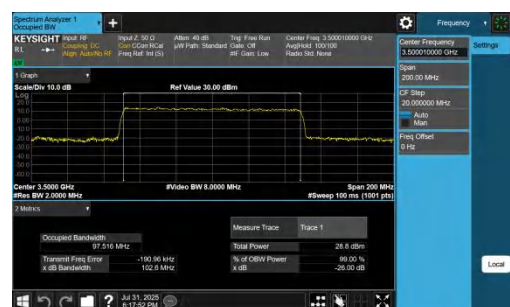
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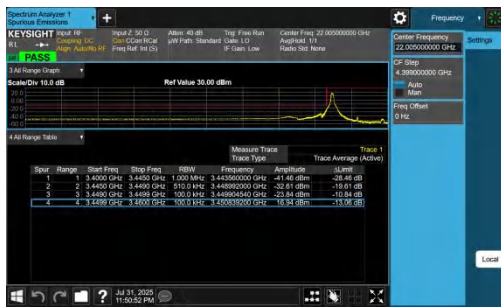
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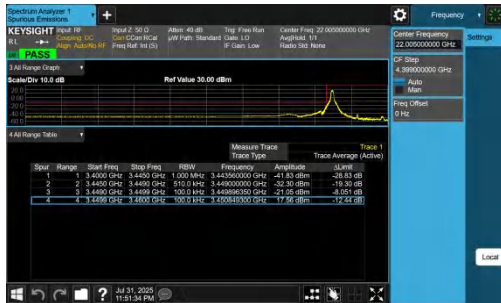


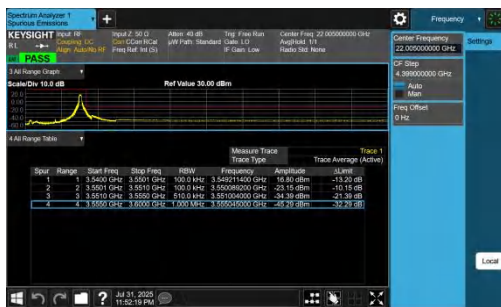
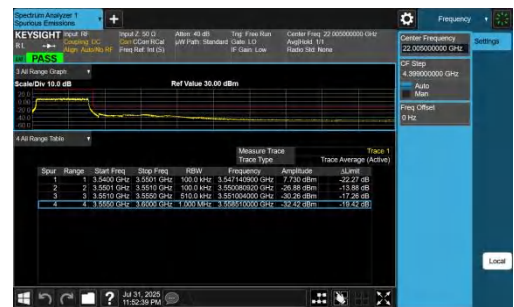
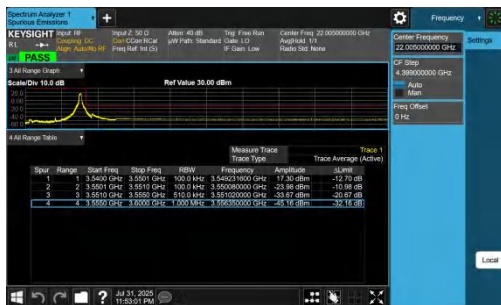
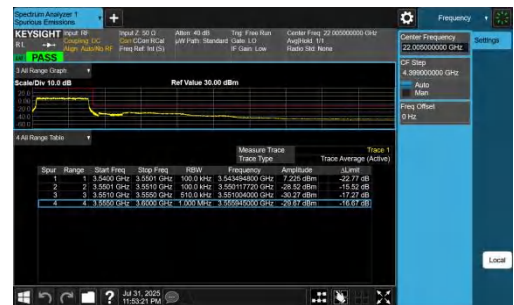
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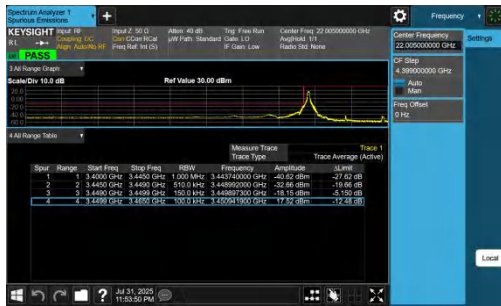
Band Edge

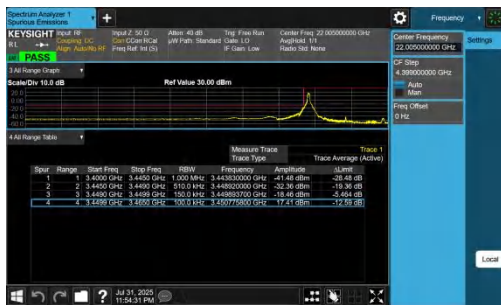
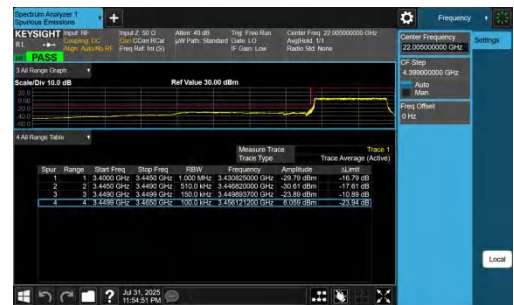
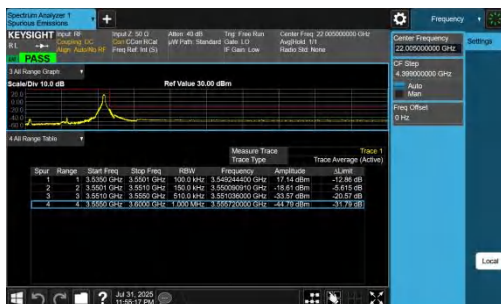
Test Graphs


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dge_1RB_Left-PASS

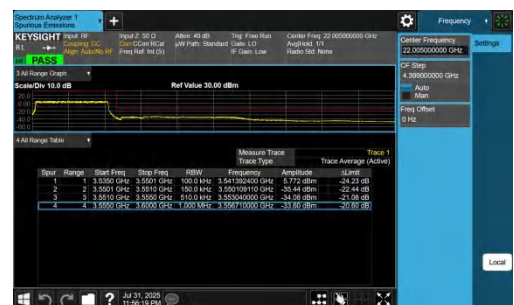
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uter_Full-PASS

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dge_1RB_Left-PASS

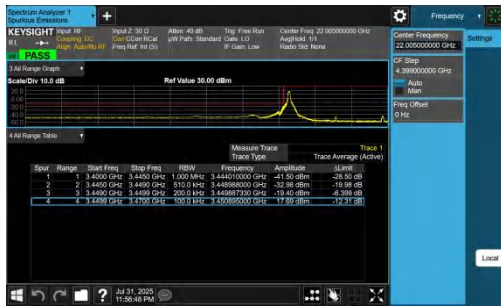
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dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+H-QPSK-IDLE-DFT-QPSK-E
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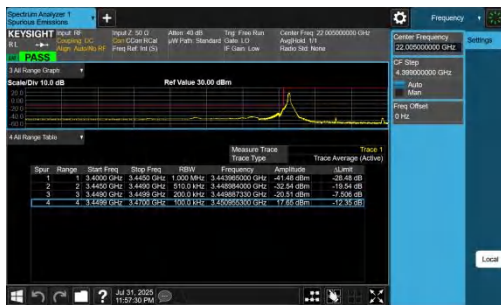
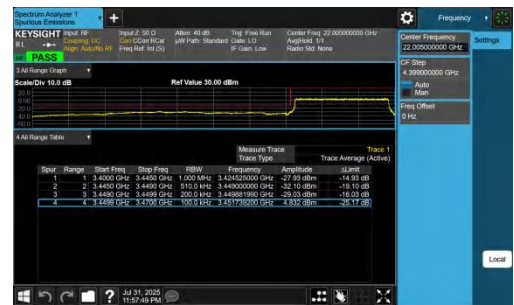
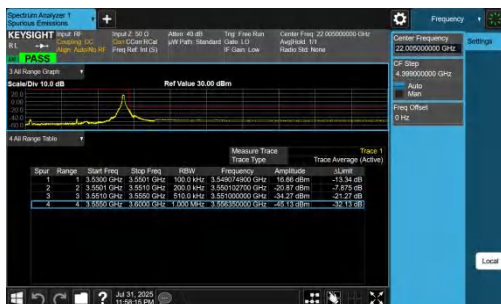
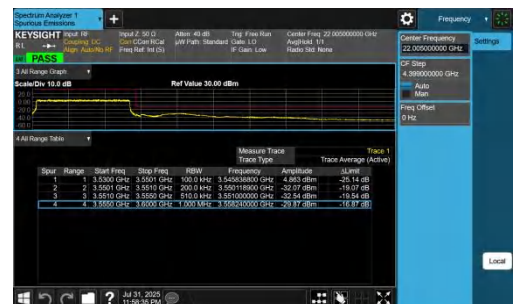

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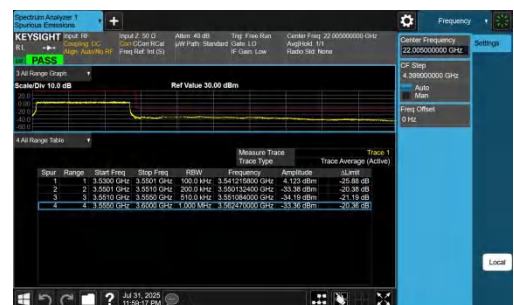
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dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+15-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+15-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

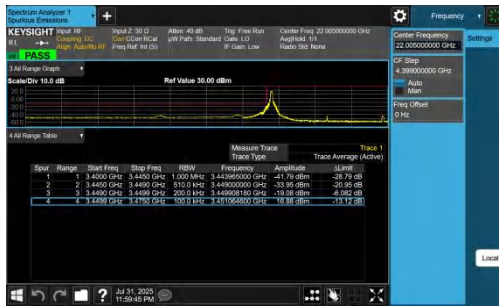
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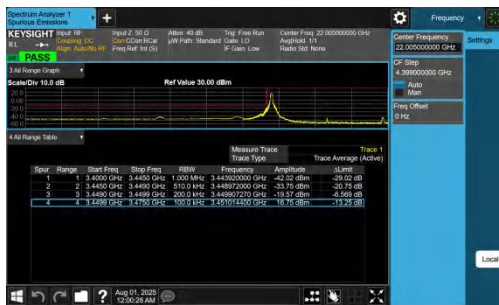
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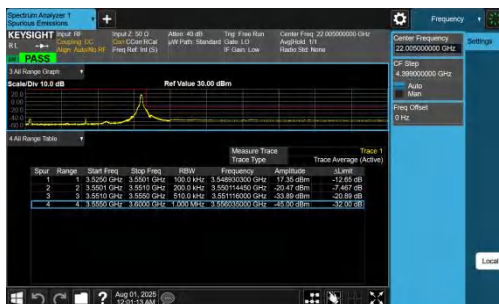
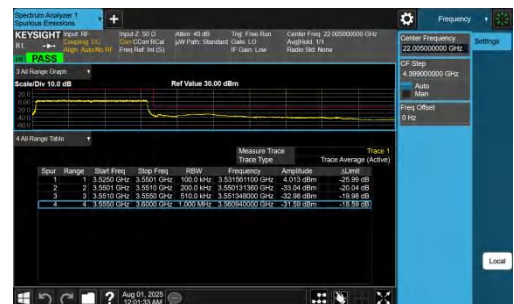
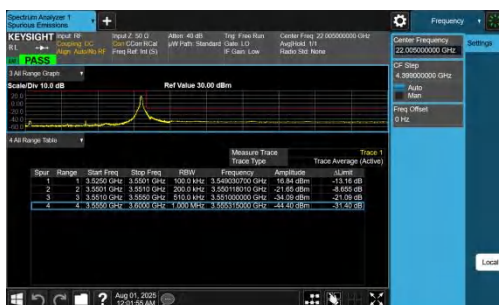

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dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+20-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+20-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+20-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+20-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+20-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

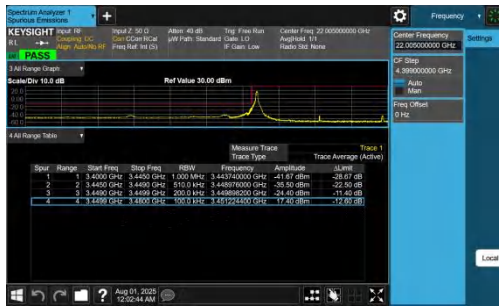
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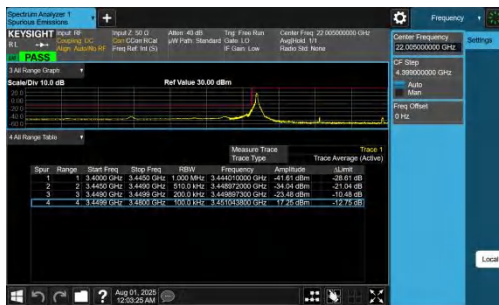
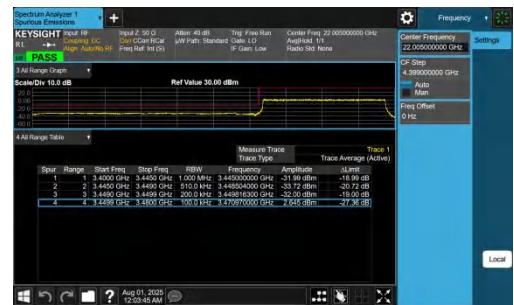
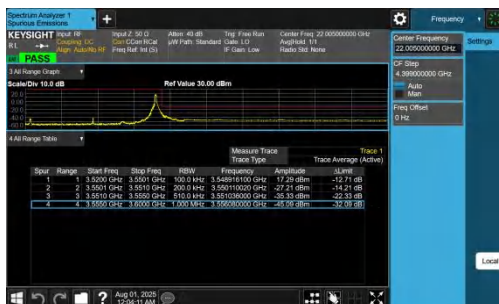
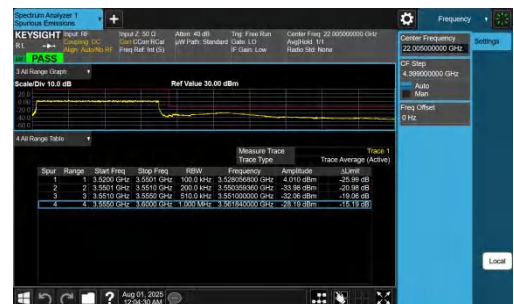
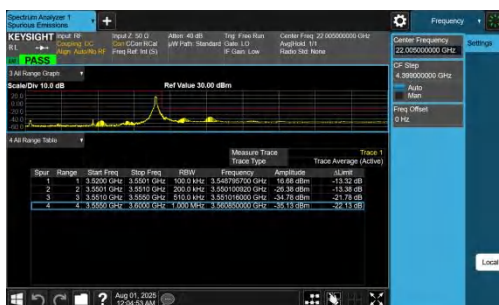
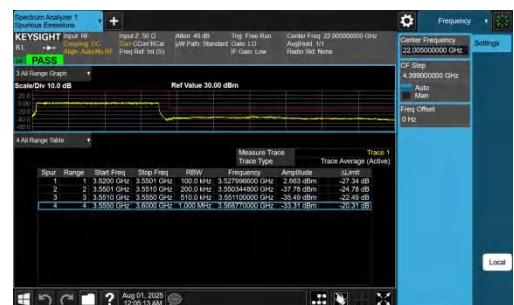

NTNV-DC_7A_n77A-PC2-30-5+25-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

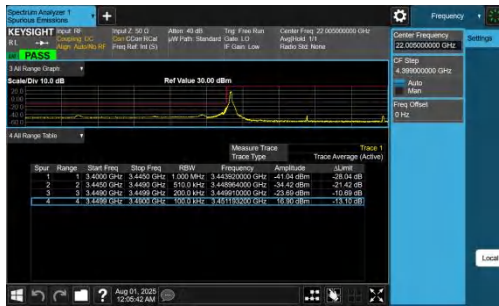
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dge_1RB_Left-PASS

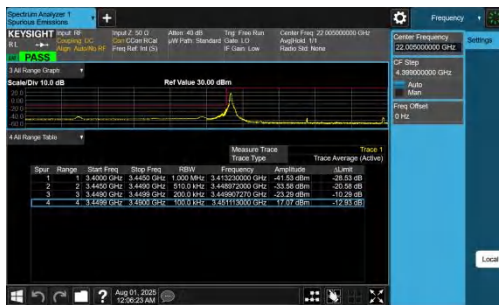
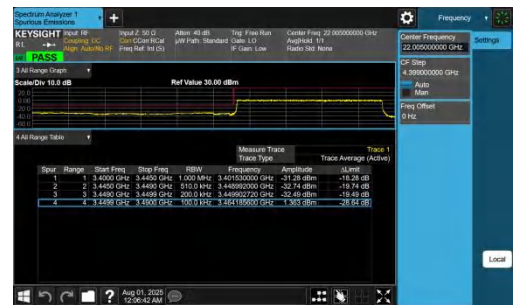
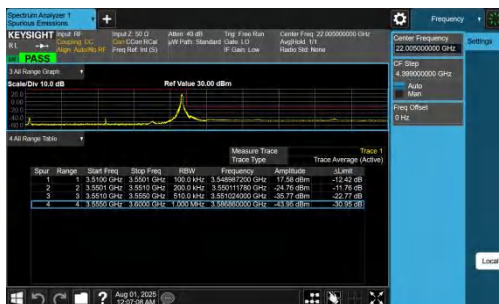
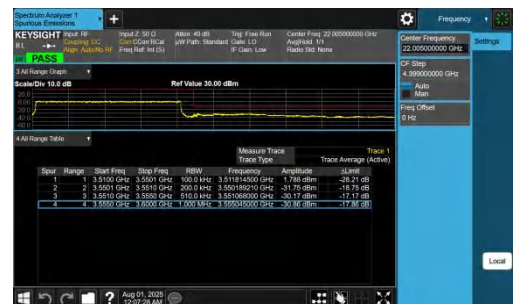
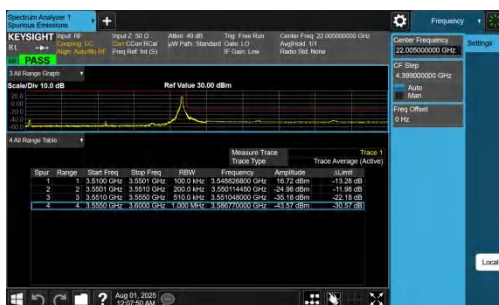
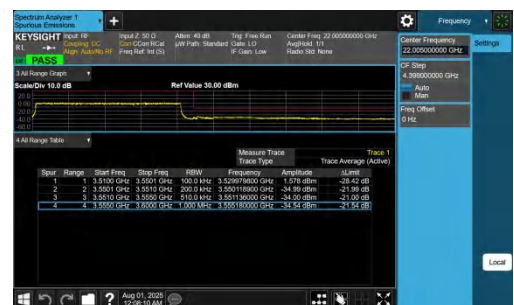
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uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+25-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+25-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+25-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

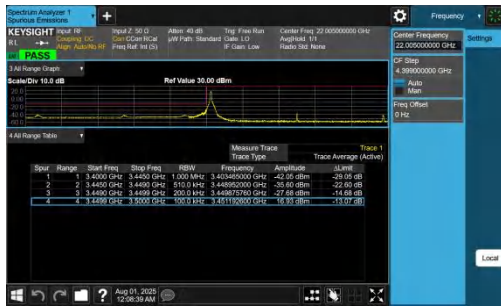
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uter_Full-PASS

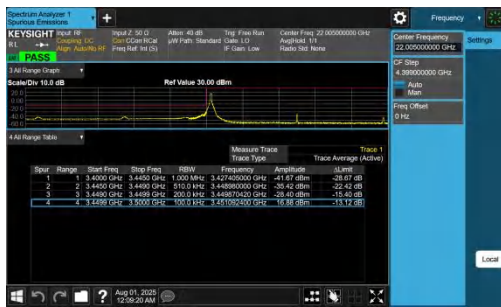
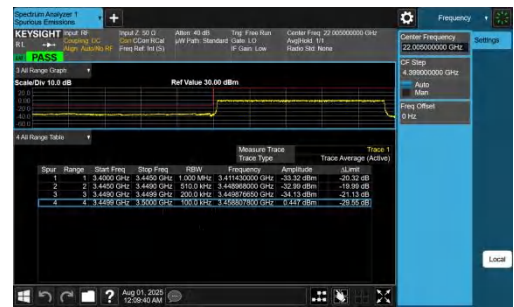
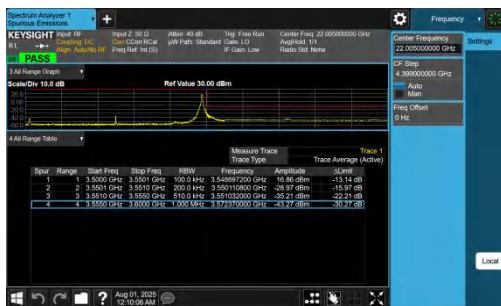
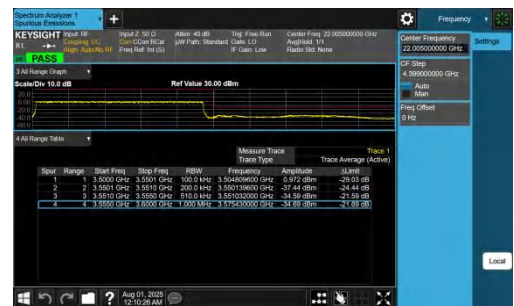
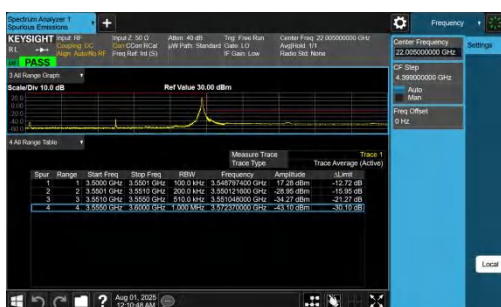
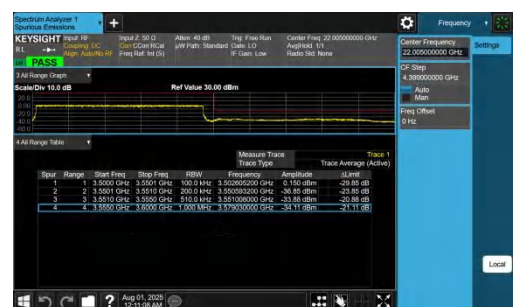

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dge_1RB_Left-PASS

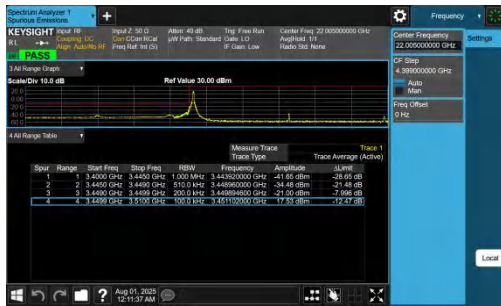
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uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+30-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+30-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+30-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+30-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+30-M+H-QPSK-IDLE-DFT-QPSK-E
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NTNV-DC_7A_n77A-PC2-30-5+30-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

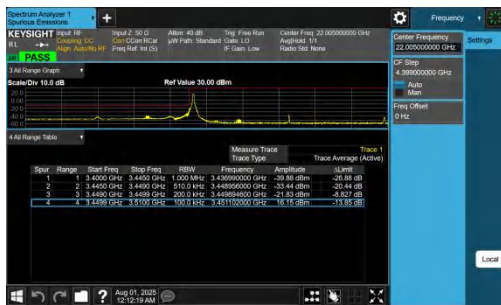
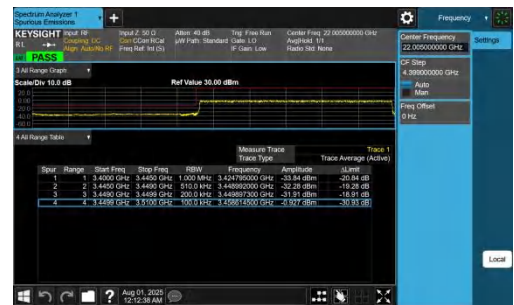
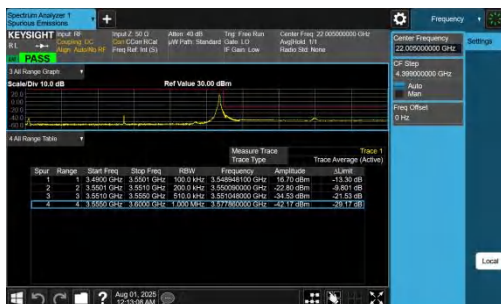
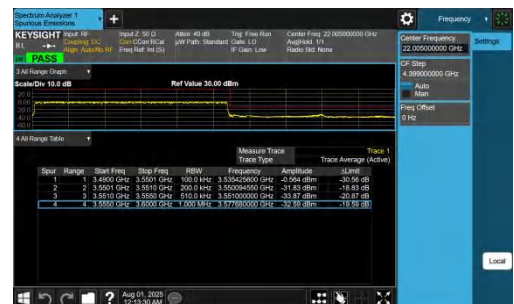

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dge_1RB_Left-PASS

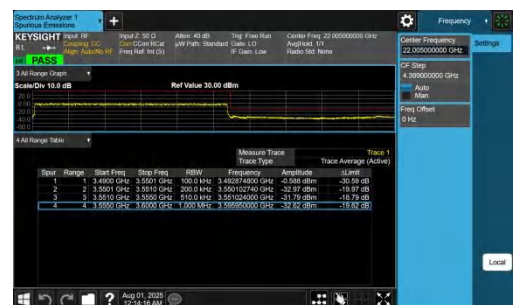
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uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+40-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+40-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+40-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+40-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+40-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+40-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

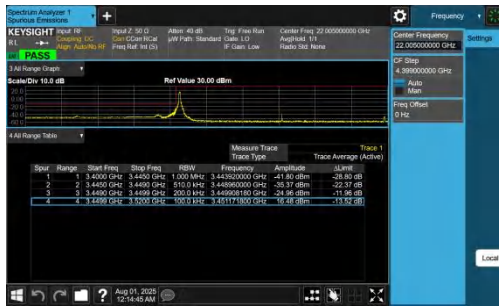
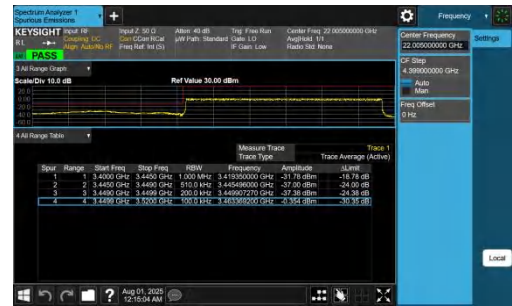
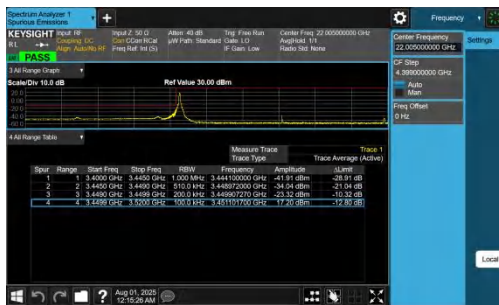
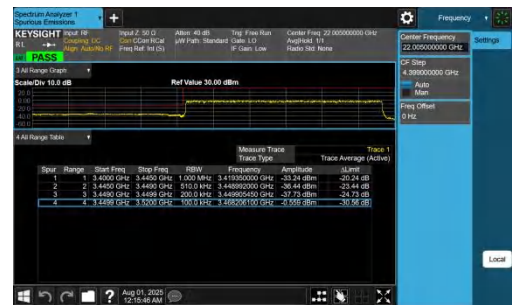
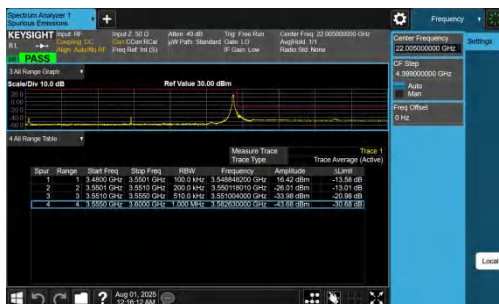
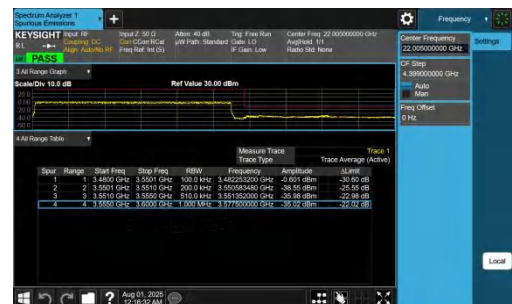
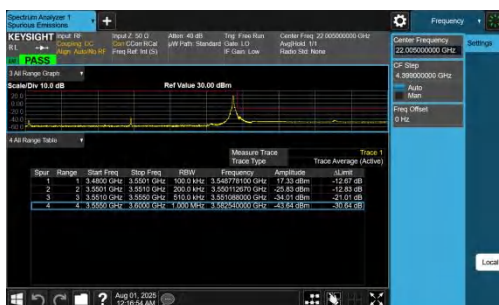
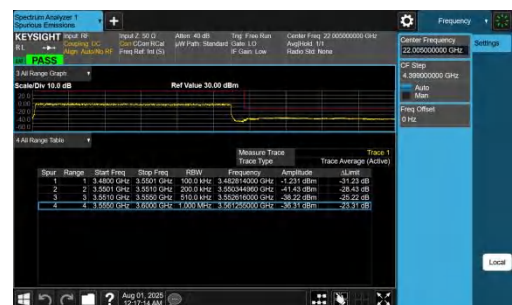

NTNV-DC_7A_n77A-PC2-30-5+50-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

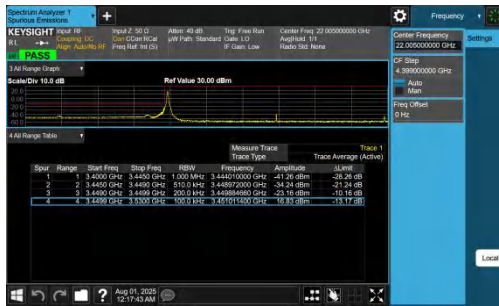
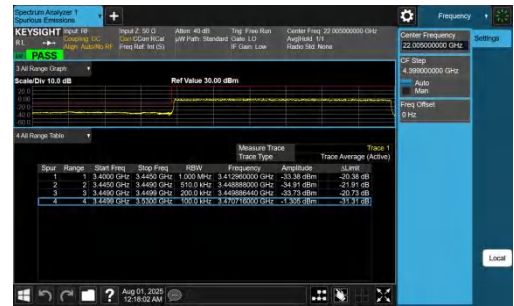
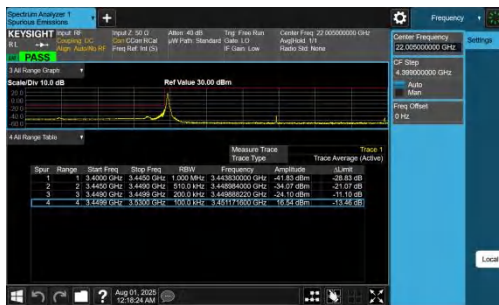
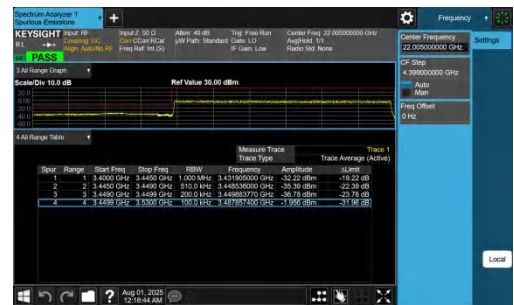
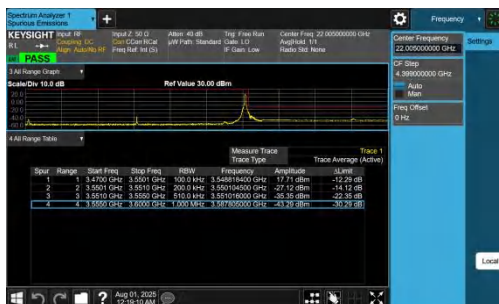
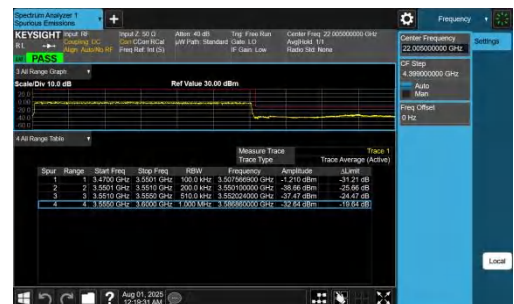
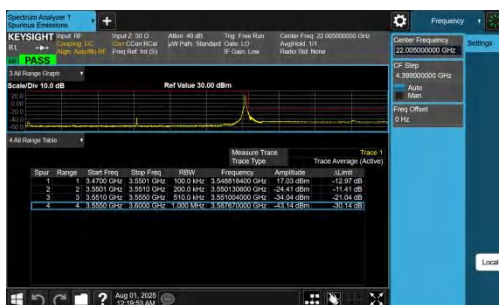
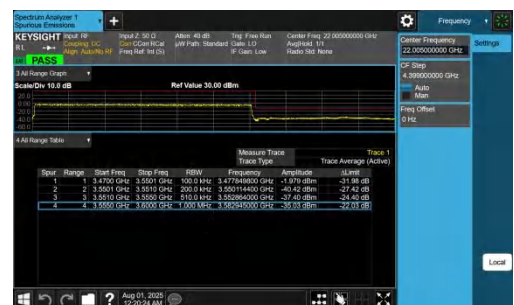
NTNV-DC_7A_n77A-PC2-30-5+50-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+50-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+50-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+50-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+50-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+50-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+50-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

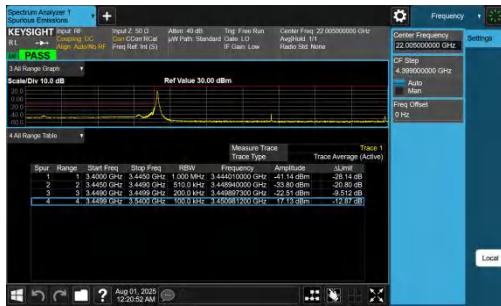
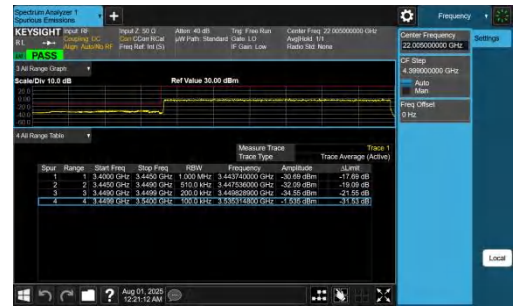
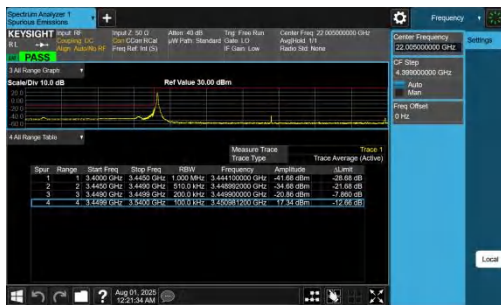
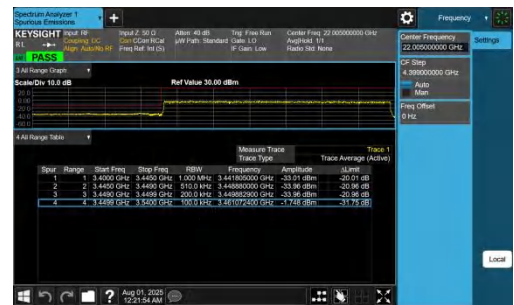
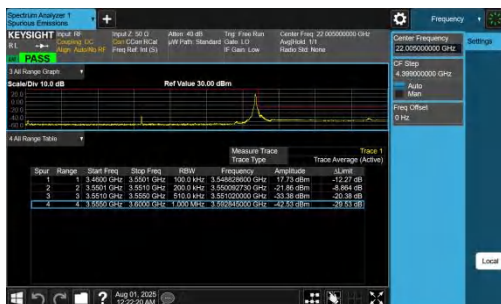
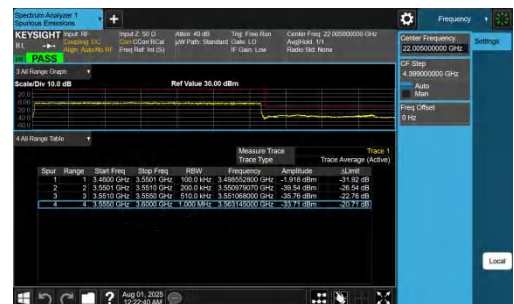
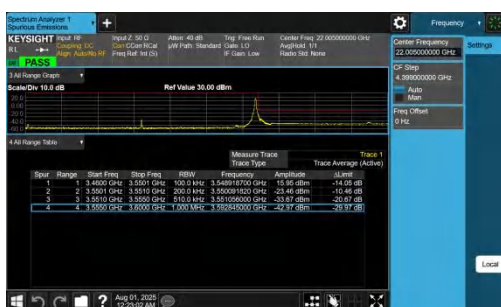
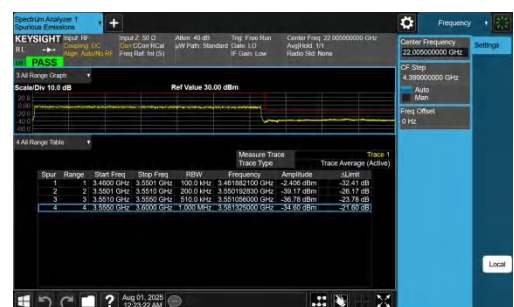

NTNV-DC_7A_n77A-PC2-30-5+60-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

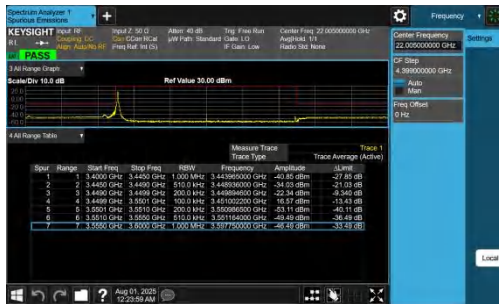
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uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+60-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+60-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+60-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+60-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+60-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

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uter_Full-PASS

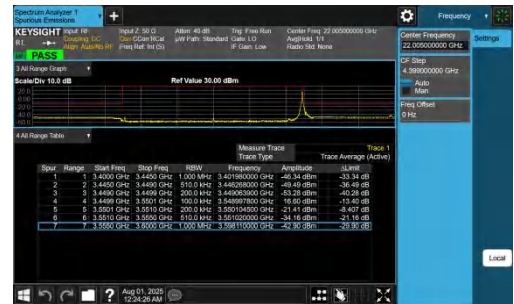

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dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+70-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+70-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+70-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+70-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+70-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+70-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+70-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS


NTNV-DC_7A_n77A-PC2-30-5+80-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+80-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+80-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+80-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+80-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+80-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+80-M+H-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+80-M+H-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

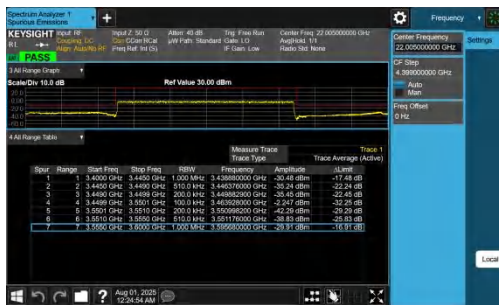

NTNV-DC_7A_n77A-PC2-30-5+90-M+L-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+90-M+L-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+90-M+L-QPSK-IDLE-DFT-QPSK-E
dge_1RB_Left-PASS

NTNV-DC_7A_n77A-PC2-30-5+90-M+L-QPSK-IDLE-DFT-QPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+90-M+H-QPSK-IDLE-DFT-BPSK-E
dge_1RB_Right-PASS

NTNV-DC_7A_n77A-PC2-30-5+90-M+H-QPSK-IDLE-DFT-BPSK-O
uter_Full-PASS

NTNV-DC_7A_n77A-PC2-30-5+90-M+H-QPSK-IDLE-DFT-QPSK-E
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uter_Full-PASS



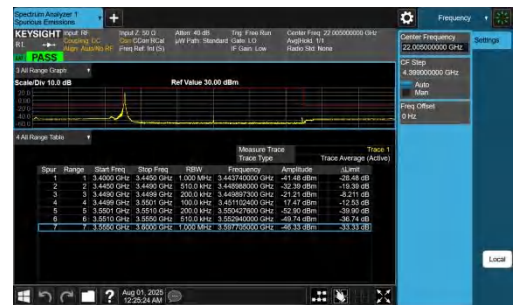
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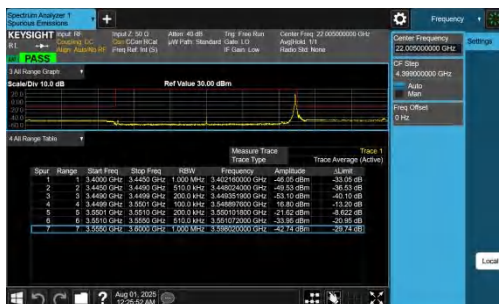
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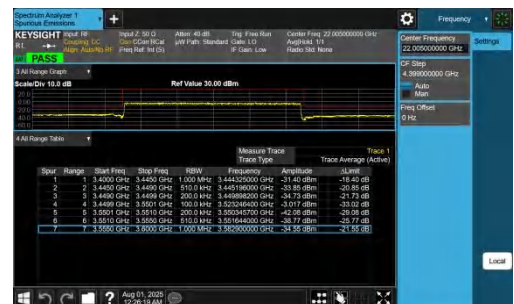
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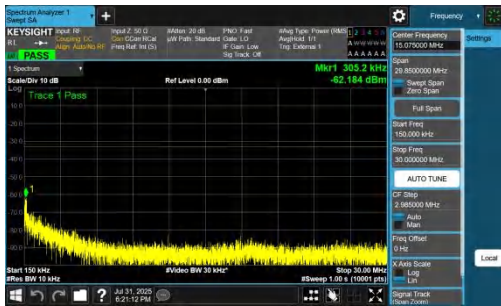
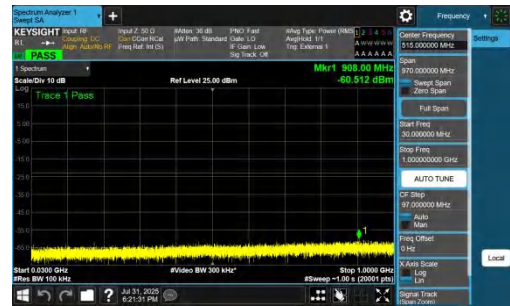
NTNV-DC_7A_n77A-PC2-30-5+100-M+L-QPSK-IDLE-DFT-QPSK-Edge_1RB_Left-PASS



NTNV-DC_7A_n77A-PC2-30-5+100-M+L-QPSK-IDLE-DFT-QPSK-Edge_1RB_Right-PASS



NTNV-DC_7A_n77A-PC2-30-5+100-M+L-QPSK-IDLE-DFT-QPSK-Outer_Full-PASS

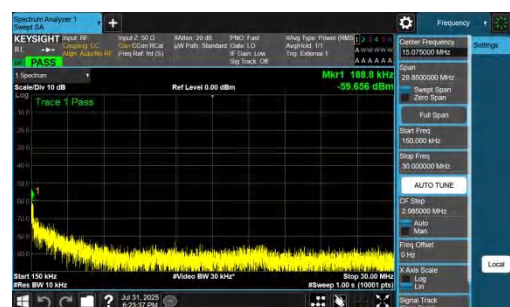

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Right-0.15-30-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Right-30-1000-PASS

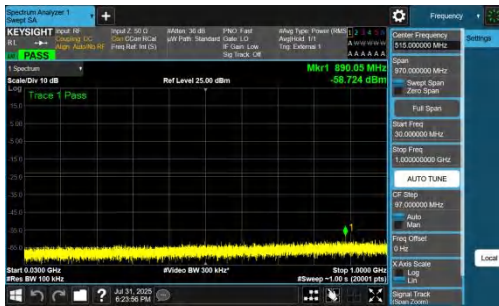
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ner_1RB_Right-1000-3000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Right-3000-6000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Right-6000-26000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Right-26000-40000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-0.009-0.15-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-0.15-30-PASS


NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-30-1000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-1000-3000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-3000-6000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-6000-26000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Left-26000-40000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-0.009-0.15-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-0.15-30-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-30-1000-PASS


NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-1000-3000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-3000-6000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-6000-26000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+L-QPSK-IDLE-DFT-QPSK-In
ner_1RB_Right-26000-40000-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-I
nner_1RB_Left-0.009-0.15-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-In
ner_1RB_Left-0.15-30-PASS

NTNV-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-I
nner_1RB_Left-30-1000-PASS

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ner_1RB_Left-1000-3000-PASS



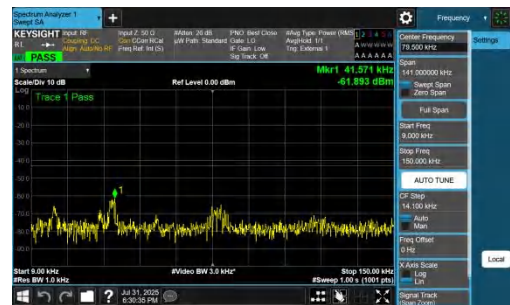
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NTVN-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Left-6000-26000-PASS



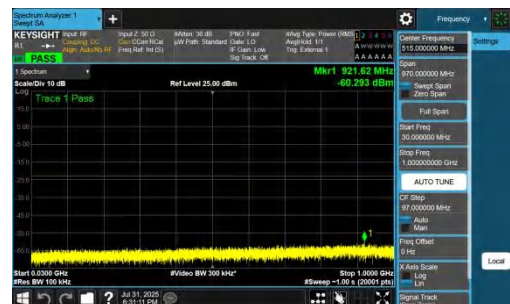
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NTVN-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Right-0.009-0.15-PASS



NTVN-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Right-0.15-30-PASS



NTVN-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Right-30-1000-PASS



NTVN-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Right-1000-3000-PASS



NTVN-DC_7A_n77A-PC2-30-5+10-M+M-QPSK-IDLE-DFT-BPSK-Inner_1RB_Right-3000-6000-PASS