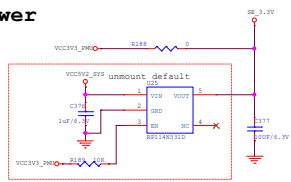
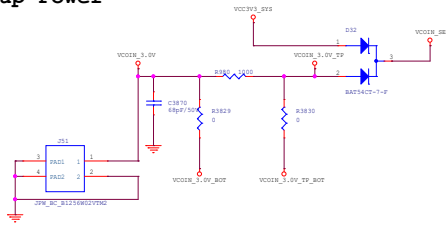


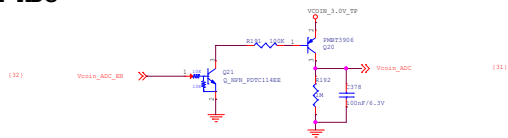
SE Power



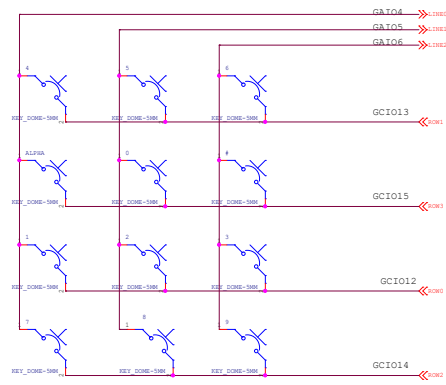
Backup Power



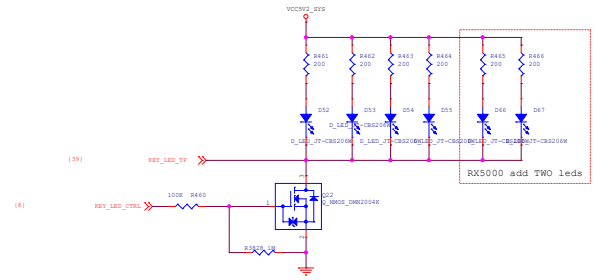
Vcoin ADC



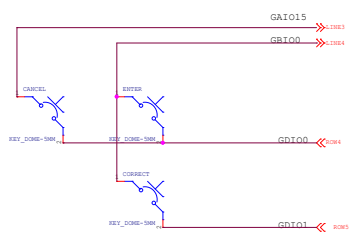
DIGITAL KEY



(32)
(32)
(32)
(32)
(32)
(32)
(32)
(32)

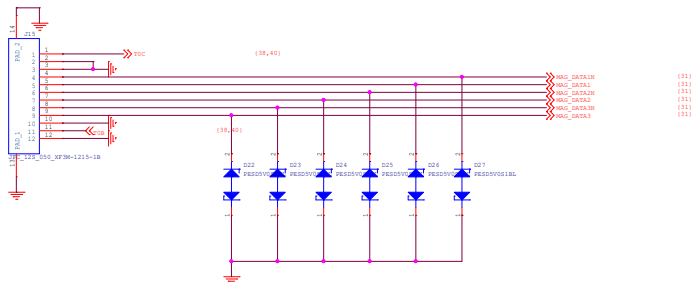


FUNC KEY

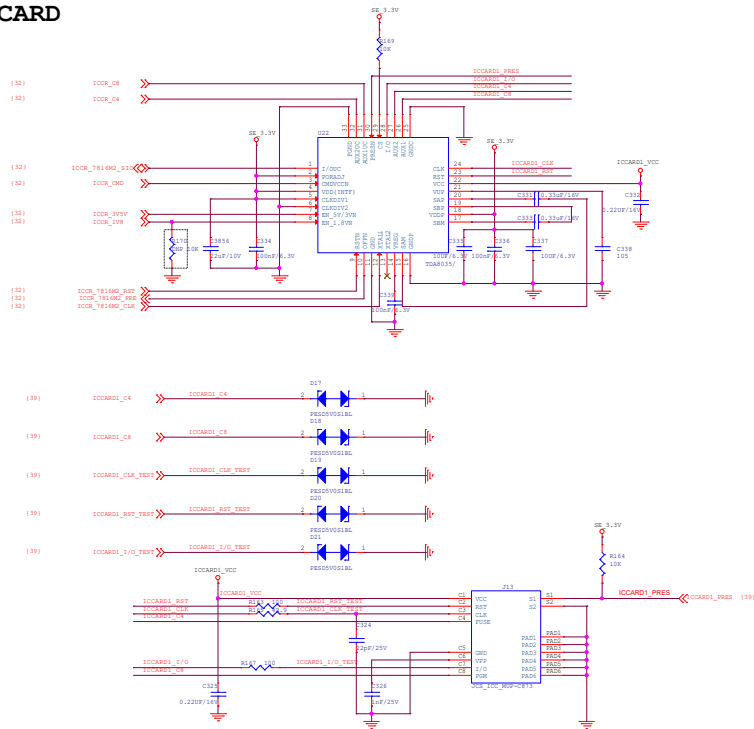


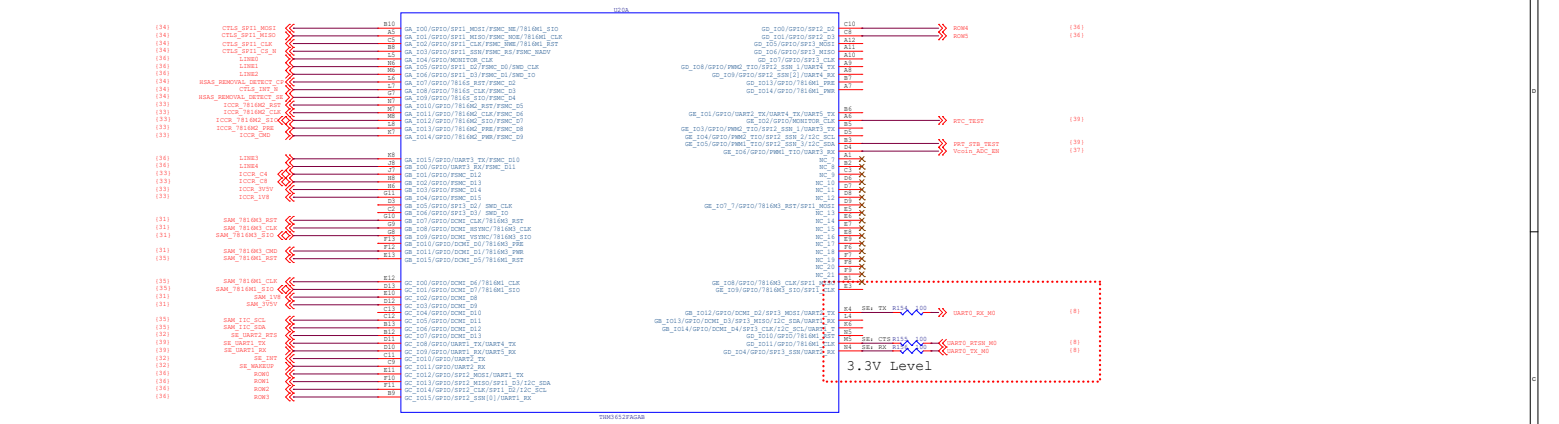
(32)
(32)
(32)
(32)

MAG

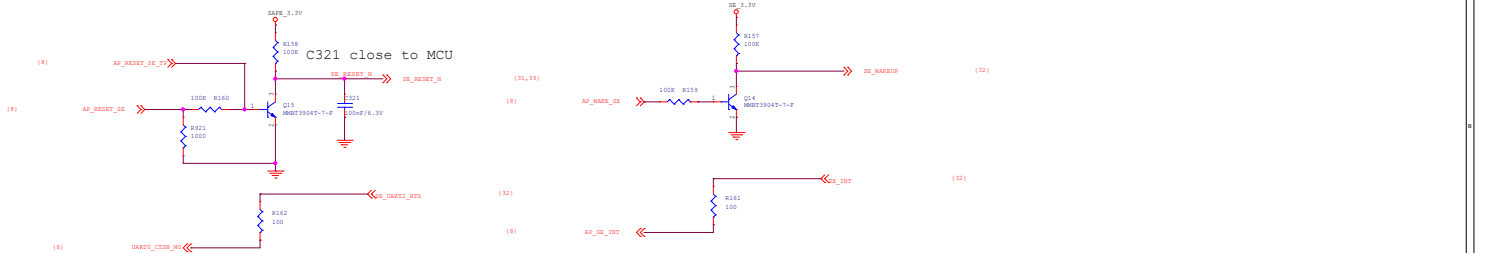


IC CARD

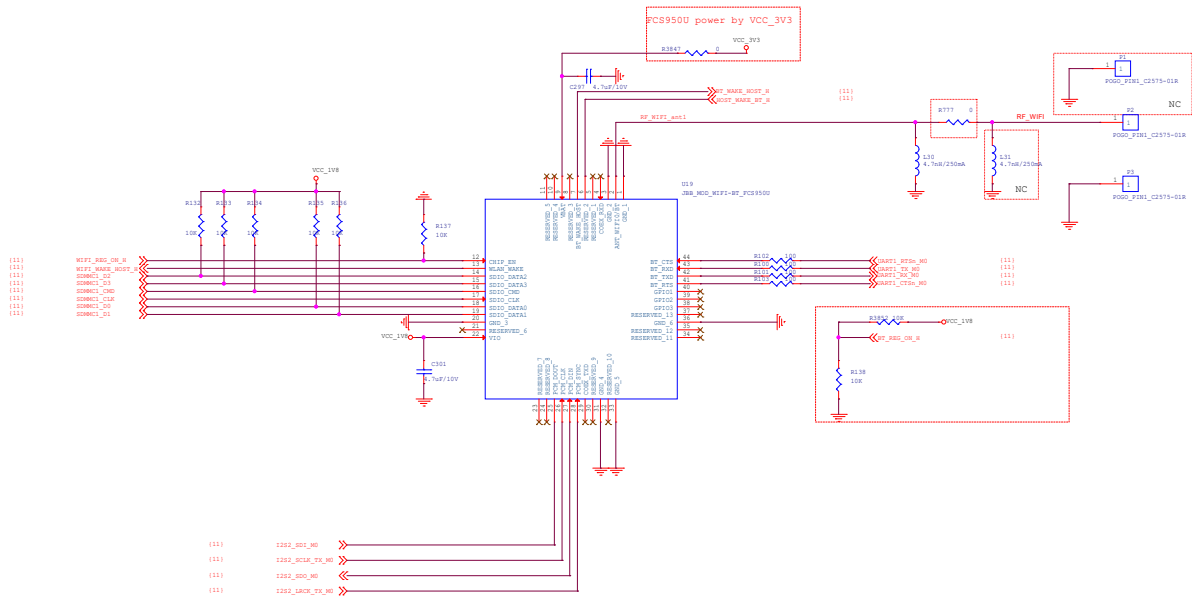




SE & AP Communication

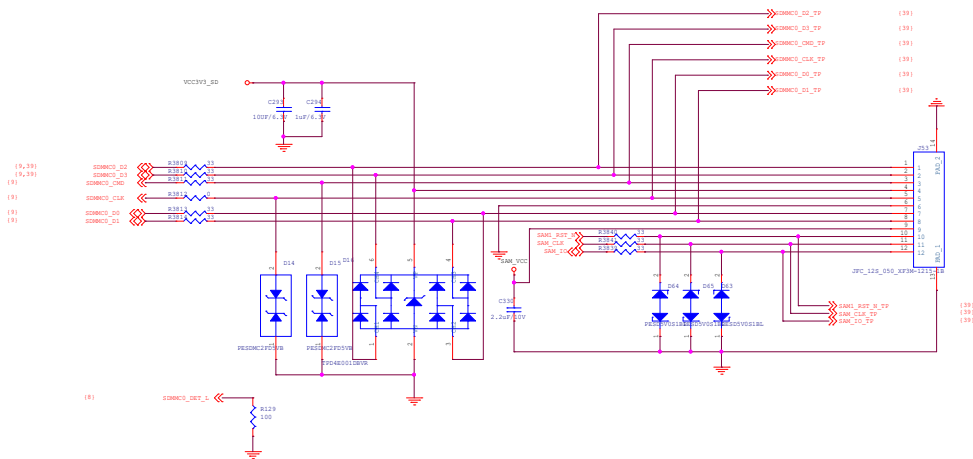


SDIO WIFI/BT Module

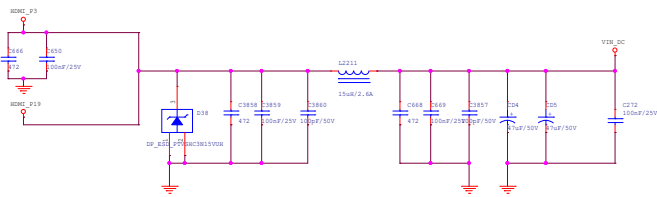


MicroSD Card

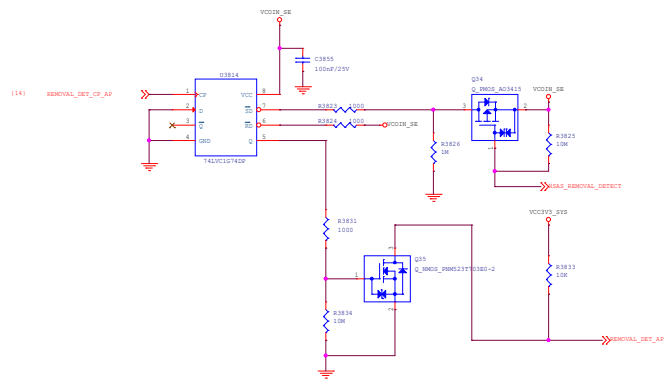
```
Note:
Take care of the type of MicroSD Card Jack,
The type here follow the logic below:
    DET is float @ card ejected; the internal pull up decided the IO pin status
    DET is connected to GND @ card inserted
If other type of Jack is used, need to shift the status of DET.
```



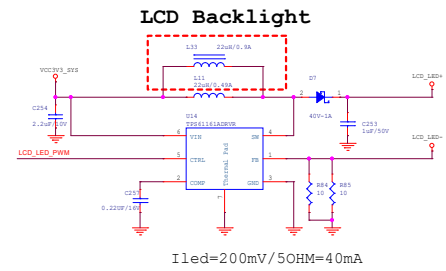
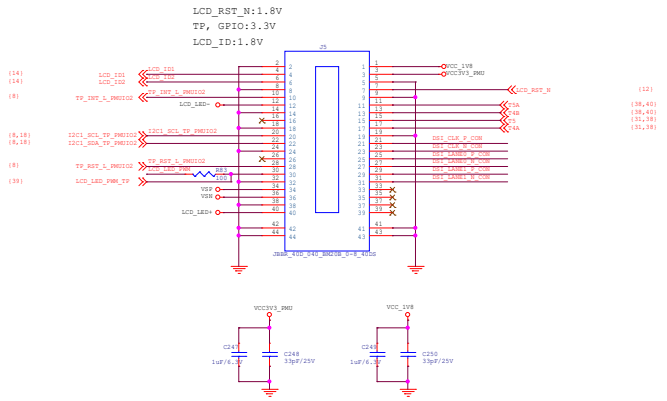
T0 sam-tf pcb



VOITH

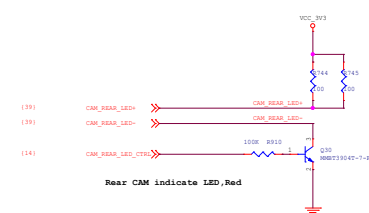
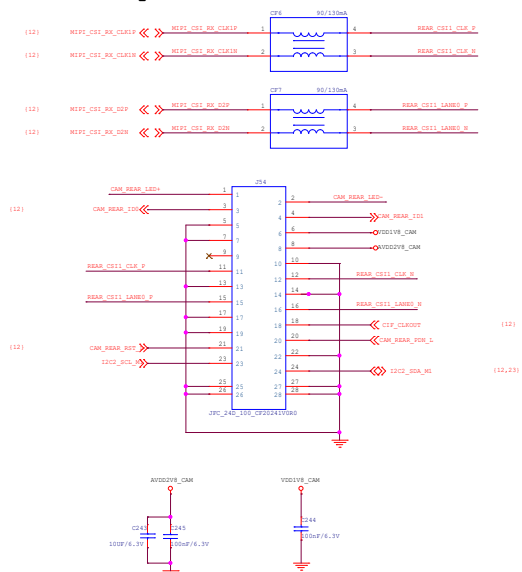


LCD & TP
MIPI Panel

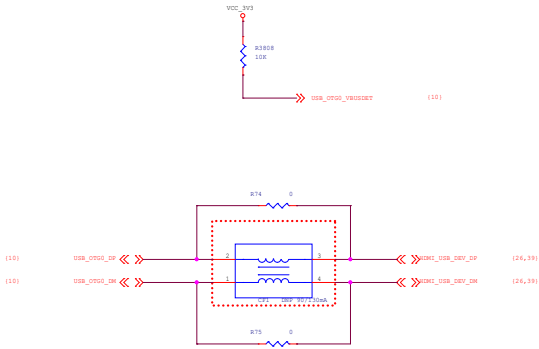


Iled=200mV/50HM=40mA

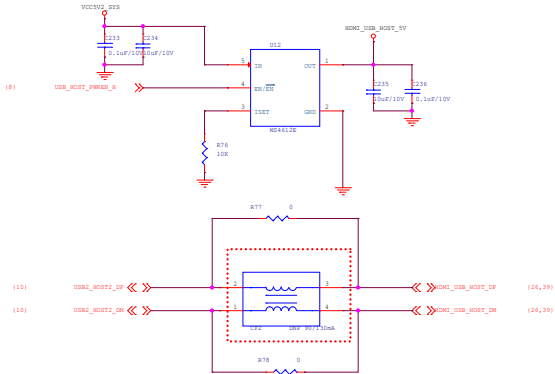
Rear Camera (option)



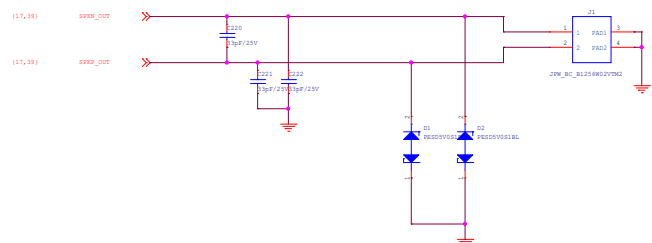
USB OTG0



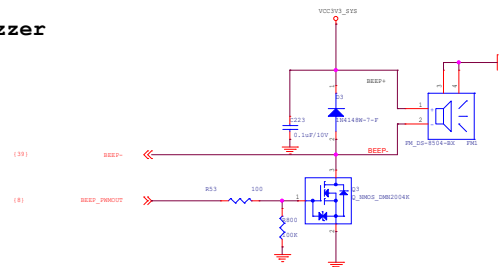
USB HOST2



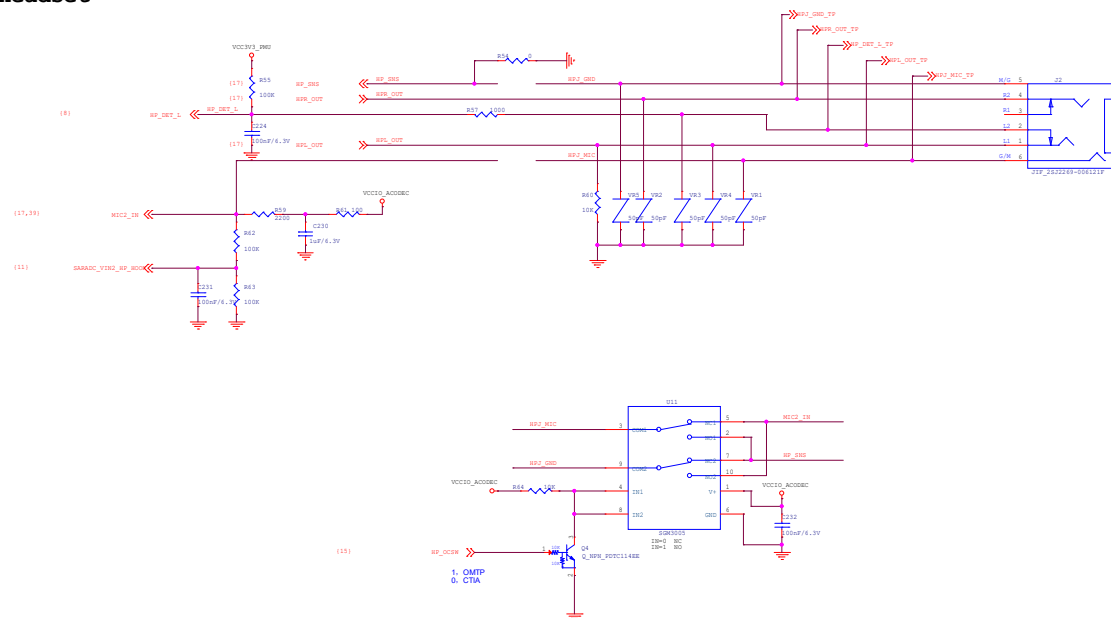
SPK



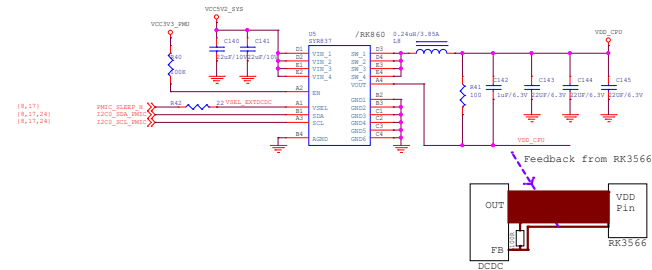
Buzzer



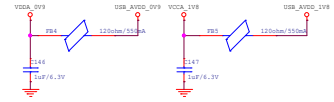
Headset



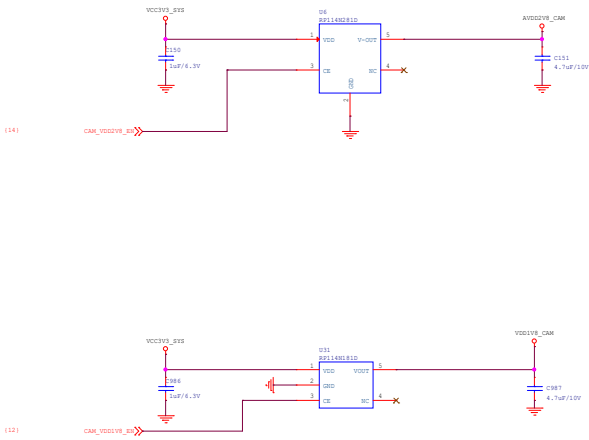
VDD_CPU_EXT



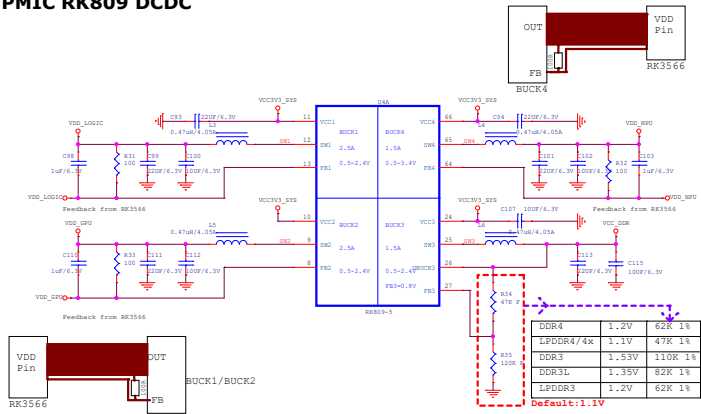
Power for USB



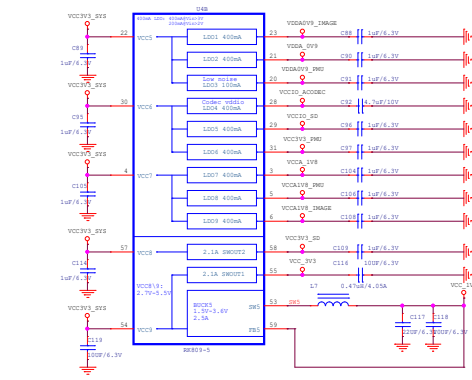
Power for Camera



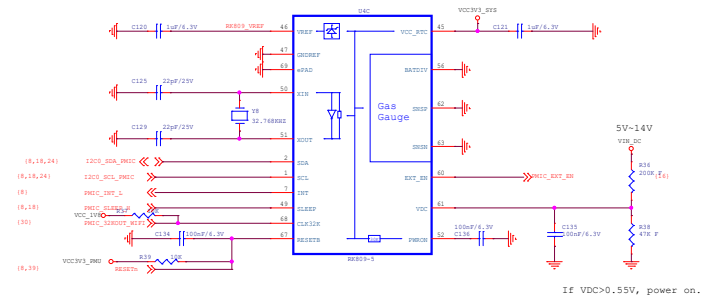
PMIC RK809 DCDC



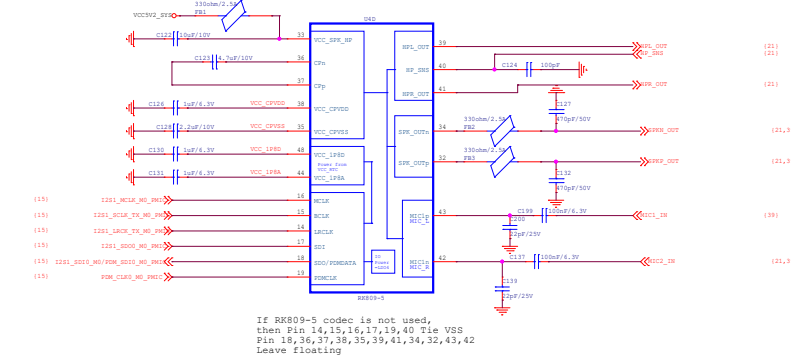
PMIC RK809 LDO



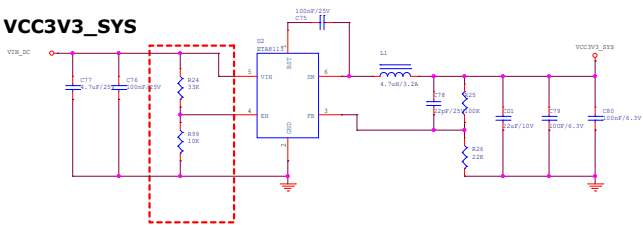
PMIC RK809 Management



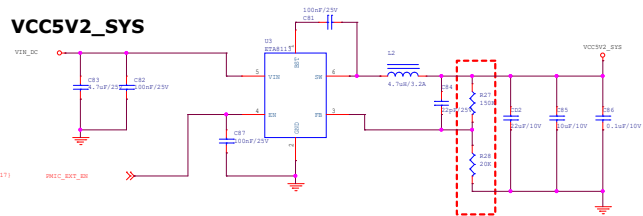
PMIC RK809 CODEC



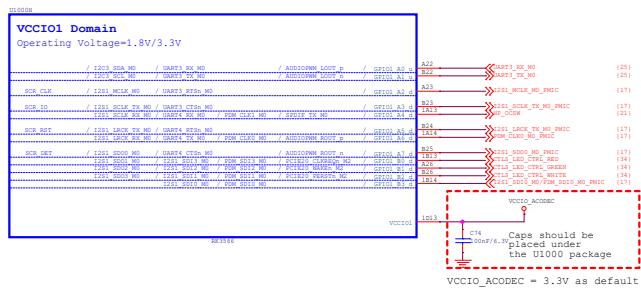
VCC3V3_SYS

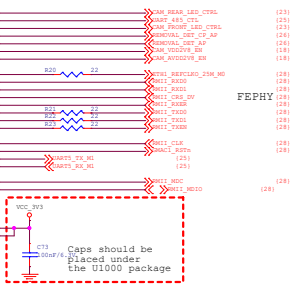


VCC5V2_SYS



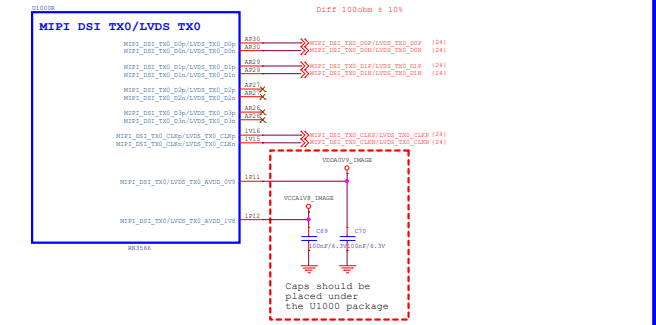
RK3566_H (VCCIO1 Domain)



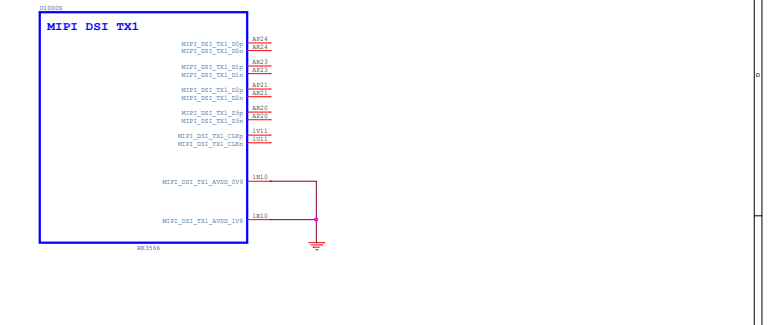
[illegible]

GMAC	Direction	GEPHY	GMAC	Direction	GEPHY
GMACx_TXD0	----->	PHYx_TXD0	GMACx_TXD0	----->	PHYx_TXD0
GMACx_TXD1	----->	PHYx_TXD1	GMACx_TXD1	----->	PHYx_TXD1
GMACx_TXD2	----->	PHYx_TXD2			
GMACx_TXD3	----->	PHYx_TXD3			
GMACx_TXEN	----->	PHYx_TXEN	GMACx_TXEN	----->	PHYx_TXEN
GMACx_TXCLK	----->	PHYx_TXCLK			
GMACx_RXD0	<-----	PHYx_RXD0	GMACx_RXD0	<-----	PHYx_RXD0
GMACx_RXD1	<-----	PHYx_RXD1	GMACx_RXD1	<-----	PHYx_RXD1
GMACx_RXD2	<-----	PHYx_RXD2			
GMACx_RXD3	<-----	PHYx_RXD3			
GMACx_RXDV	<-----	PHYx_RXDV	GMACx_RXDV	<-----	PHYx_CRS_DV
GMACx_RXCLK	<-----	PHYx_RXCLK			
GMACx_RXER	<-----		GMACx_RXER	<-----	PHYx_RXER
GMACx_MD0	<-----	PHYx_MD0	GMACx_MD0	<-----	PHYx_MD0
GMACx_MDIO	<-----	PHYx_MDIO	GMACx_MDIO	<-----	PHYx_MDIO
ETHX_TBEFLCLO_TSN	<-----	PHYx_XTALIN			
GMACx_MCLKINOUT	<-----	PHYx_CLKOUT125 (Option)	GMACx_MCLKINOUT	----->	PHYx_XTALIN/REFCLK
GPIO	<-----	PHYx_RSTn	GPIO	----->	PHYx_RSTn
GPIO	<-----	PHYx_INT/PMBE	GPIO	<-----	PHYx_INT/PMBE

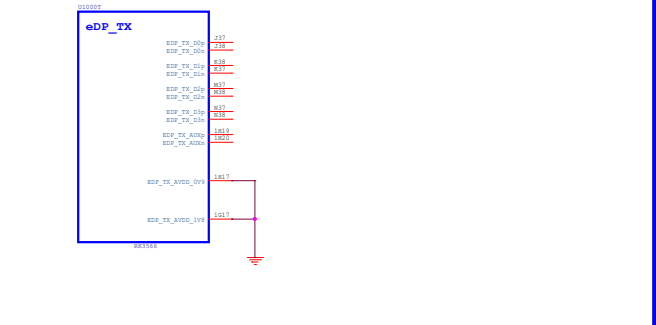
RK3566_R(MIPI_DSI_TX0/LVDS_TX0)



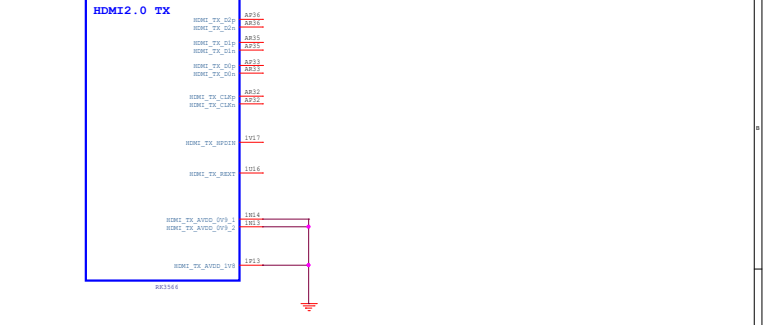
RK3566_S(MIPI_DSI_TX1)



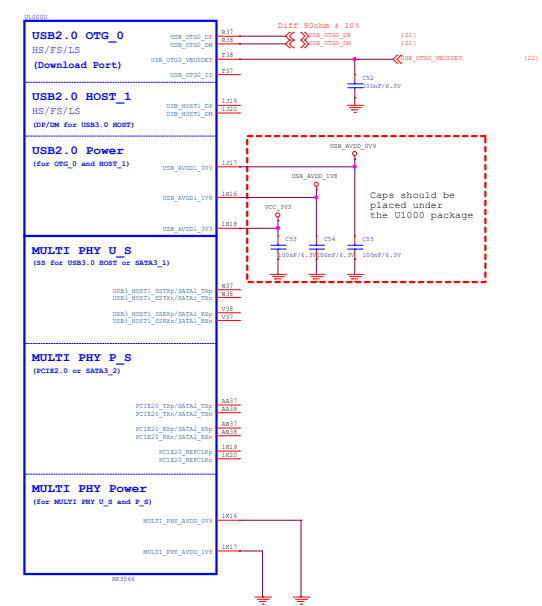
RK3566_T(eDP TX)



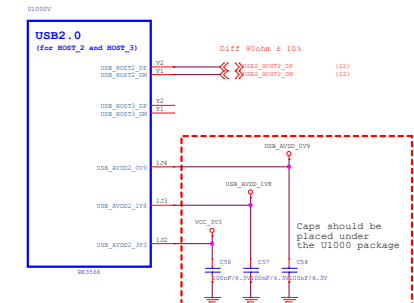
RK3566_Q(HDMI2.0 TX)



RK3566_U (USB3.0/PCIe2.0x1/SATA)



RK3566_V (USB2.0 HOST)

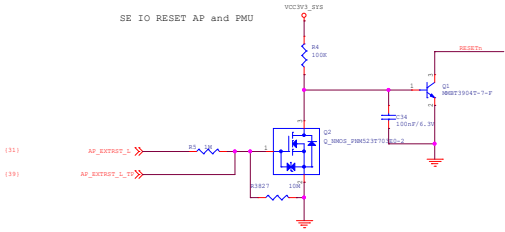


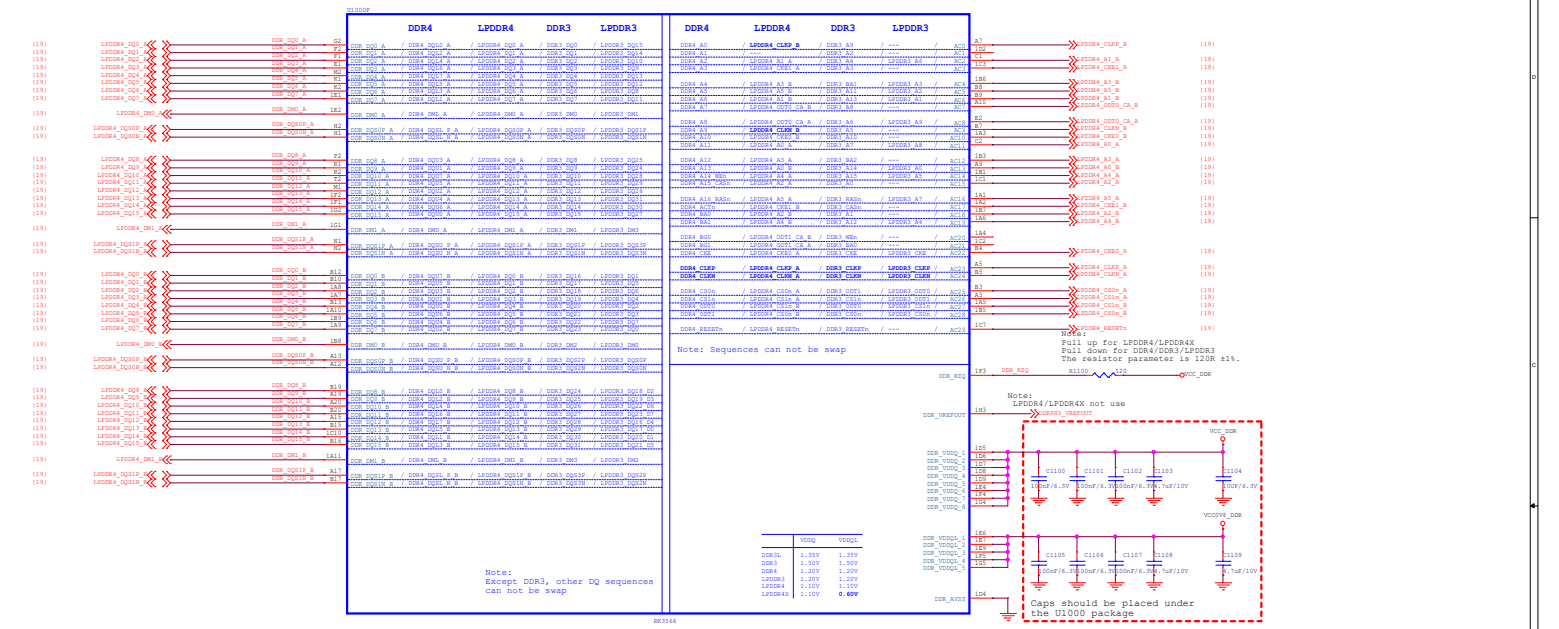
Flash Power Manage



VCCIO3 Domain
Operating Voltage=1.8V/3.3V

Pin	Signal	Connection	Notes
PIN00 NC	/ VCCIO3_C0	/ VCCIO3_C0 NC	(29)
PIN01 NC	/ VCCIO3_C1	/ VCCIO3_C1 NC	(29)
PIN02 NC	/ VCCIO3_C2	/ VCCIO3_C2 NC	(29,30)
PIN03 NC	/ VCCIO3_C3	/ VCCIO3_C3 NC	(29,30)
PIN04 NC	/ VCCIO3_C4	/ VCCIO3_C4 NC	(29)
PIN05 NC	/ VCCIO3_C5	/ VCCIO3_C5 NC	(29)
PIN06 NC	/ VCCIO3_C6	/ VCCIO3_C6 NC	(29)
PIN07 NC	/ VCCIO3_C7	/ VCCIO3_C7 NC	(29)
PIN08 NC	/ VCCIO3_C8	/ VCCIO3_C8 NC	(29)
PIN09 NC	/ VCCIO3_C9	/ VCCIO3_C9 NC	(29)
PIN10 NC	/ VCCIO3_C10	/ VCCIO3_C10 NC	(29)
PIN11 NC	/ VCCIO3_C11	/ VCCIO3_C11 NC	(29)
PIN12 NC	/ VCCIO3_C12	/ VCCIO3_C12 NC	(29)
PIN13 NC	/ VCCIO3_C13	/ VCCIO3_C13 NC	(29)
PIN14 NC	/ VCCIO3_C14	/ VCCIO3_C14 NC	(29)
PIN15 NC	/ VCCIO3_C15	/ VCCIO3_C15 NC	(29)
PIN16 NC	/ VCCIO3_C16	/ VCCIO3_C16 NC	(29)
PIN17 NC	/ VCCIO3_C17	/ VCCIO3_C17 NC	(29)
PIN18 NC	/ VCCIO3_C18	/ VCCIO3_C18 NC	(29)
PIN19 NC	/ VCCIO3_C19	/ VCCIO3_C19 NC	(29)
PIN20 NC	/ VCCIO3_C20	/ VCCIO3_C20 NC	(29)
PIN21 NC	/ VCCIO3_C21	/ VCCIO3_C21 NC	(29)
PIN22 NC	/ VCCIO3_C22	/ VCCIO3_C22 NC	(29)
PIN23 NC	/ VCCIO3_C23	/ VCCIO3_C23 NC	(29)
PIN24 NC	/ VCCIO3_C24	/ VCCIO3_C24 NC	(29)
PIN25 NC	/ VCCIO3_C25	/ VCCIO3_C25 NC	(29)
PIN26 NC	/ VCCIO3_C26	/ VCCIO3_C26 NC	(29)
PIN27 NC	/ VCCIO3_C27	/ VCCIO3_C27 NC	(29)
PIN28 NC	/ VCCIO3_C28	/ VCCIO3_C28 NC	(29)
PIN29 NC	/ VCCIO3_C29	/ VCCIO3_C29 NC	(29)
PIN30 NC	/ VCCIO3_C30	/ VCCIO3_C30 NC	(29)
PIN31 NC	/ VCCIO3_C31	/ VCCIO3_C31 NC	(29)
PIN32 NC	/ VCCIO3_C32	/ VCCIO3_C32 NC	(29)
PIN33 NC	/ VCCIO3_C33	/ VCCIO3_C33 NC	(29)
PIN34 NC	/ VCCIO3_C34	/ VCCIO3_C34 NC	(29)
PIN35 NC	/ VCCIO3_C35	/ VCCIO3_C35 NC	(29)
PIN36 NC	/ VCCIO3_C36	/ VCCIO3_C36 NC	(29)
PIN37 NC	/ VCCIO3_C37	/ VCCIO3_C37 NC	(29)
PIN38 NC	/ VCCIO3_C38	/ VCCIO3_C38 NC	(29)
PIN39 NC	/ VCCIO3_C39	/ VCCIO3_C39 NC	(29)
PIN40 NC	/ VCCIO3_C40	/ VCCIO3_C40 NC	(29)
PIN41 NC	/ VCCIO3_C41	/ VCCIO3_C41 NC	(29)
PIN42 NC	/ VCCIO3_C42	/ VCCIO3_C42 NC	(29)
PIN43 NC	/ VCCIO3_C43	/ VCCIO3_C43 NC	(29)
PIN44 NC	/ VCCIO3_C44	/ VCCIO3_C44 NC	(29)
PIN45 NC	/ VCCIO3_C45	/ VCCIO3_C45 NC	(29)
PIN46 NC	/ VCCIO3_C46	/ VCCIO3_C46 NC	(29)
PIN47 NC	/ VCCIO3_C47	/ VCCIO3_C47 NC	(29)
PIN48 NC	/ VCCIO3_C48	/ VCCIO3_C48 NC	(29)
PIN49 NC	/ VCCIO3_C49	/ VCCIO3_C49 NC	(29)
PIN50 NC	/ VCCIO3_C50	/ VCCIO3_C50 NC	(29)
PIN51 NC	/ VCCIO3_C51	/ VCCIO3_C51 NC	(29)
PIN52 NC	/ VCCIO3_C52	/ VCCIO3_C52 NC	(29)
PIN53 NC	/ VCCIO3_C53	/ VCCIO3_C53 NC	(29)
PIN54 NC	/ VCCIO3_C54	/ VCCIO3_C54 NC	(29)
PIN55 NC	/ VCCIO3_C55	/ VCCIO3_C55 NC	(29)
PIN56 NC	/ VCCIO3_C56	/ VCCIO3_C56 NC	(29)
PIN57 NC	/ VCCIO3_C57	/ VCCIO3_C57 NC	(29)
PIN58 NC	/ VCCIO3_C58	/ VCCIO3_C58 NC	(29)
PIN59 NC	/ VCCIO3_C59	/ VCCIO3_C59 NC	(29)
PIN60 NC	/ VCCIO3_C60	/ VCCIO3_C60 NC	(29)
PIN61 NC	/ VCCIO3_C61	/ VCCIO3_C61 NC	(29)
PIN62 NC	/ VCCIO3_C62	/ VCCIO3_C62 NC	(29)
PIN63 NC	/ VCCIO3_C63	/ VCCIO3_C63 NC	(29)
PIN64 NC	/ VCCIO3_C64	/ VCCIO3_C64 NC	(29)
PIN65 NC	/ VCCIO3_C65	/ VCCIO3_C65 NC	(29)
PIN66 NC	/ VCCIO3_C66	/ VCCIO3_C66 NC	(29)
PIN67 NC	/ VCCIO3_C67	/ VCCIO3_C67 NC	(29)
PIN68 NC	/ VCCIO3_C68	/ VCCIO3_C68 NC	(29)
PIN69 NC	/ VCCIO3_C69	/ VCCIO3_C69 NC	(29)
PIN70 NC	/ VCCIO3_C70	/ VCCIO3_C70 NC	(29)
PIN71 NC	/ VCCIO3_C71	/ VCCIO3_C71 NC	(29)
PIN72 NC	/ VCCIO3_C72	/ VCCIO3_C72 NC	(29)
PIN73 NC	/ VCCIO3_C73	/ VCCIO3_C73 NC	(29)
PIN74 NC	/ VCCIO3_C74	/ VCCIO3_C74 NC	(29)
PIN75 NC	/ VCCIO3_C75	/ VCCIO3_C7	





RK3566_ABCDE
(Power&GND)

