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Appendix B.42

NR Band n78(3700-3800)



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Effective (Isotropic) Radiated Power Output Data for SA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power	EIRP	LIMIT	verdict
N78-3700-3800	30	10	DFT-PI2BPSK	L	Inner_1RB_Left	25.98	28.92	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	L	Inner_1RB_Right	26.11	29.05	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	L	Outer_Full	25.57	28.51	30	pass
N78-3700-3800	30	10	DFT-QPSK	L	Inner_1RB_Left	26.03	28.97	30	pass
N78-3700-3800	30	10	DFT-QPSK	L	Inner_1RB_Right	26.03	28.97	30	pass
N78-3700-3800	30	10	DFT-QPSK	L	Outer_Full	25.05	27.99	30	pass
N78-3700-3800	30	10	DFT-16QAM	L	Inner_1RB_Left	25.03	27.97	30	pass
N78-3700-3800	30	10	DFT-16QAM	L	Inner_1RB_Right	24.98	27.92	30	pass
N78-3700-3800	30	10	DFT-16QAM	L	Outer_Full	24.04	26.98	30	pass
N78-3700-3800	30	10	DFT-64QAM	L	Inner_1RB_Left	23.37	26.31	30	pass
N78-3700-3800	30	10	DFT-64QAM	L	Inner_1RB_Right	23.45	26.39	30	pass
N78-3700-3800	30	10	DFT-64QAM	L	Outer_Full	23.53	26.47	30	pass
N78-3700-3800	30	10	DFT-256QAM	L	Inner_1RB_Left	21.57	24.51	30	pass
N78-3700-3800	30	10	DFT-256QAM	L	Inner_1RB_Right	21.53	24.47	30	pass
N78-3700-3800	30	10	DFT-256QAM	L	Outer_Full	21.56	24.5	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	M	Inner_1RB_Left	25.94	28.88	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	M	Inner_1RB_Right	26.01	28.95	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	M	Outer_Full	25.57	28.51	30	pass
N78-3700-3800	30	10	DFT-QPSK	M	Inner_1RB_Left	26.07	29.01	30	pass
N78-3700-3800	30	10	DFT-QPSK	M	Inner_1RB_Right	26.01	28.95	30	pass
N78-3700-3800	30	10	DFT-QPSK	M	Outer_Full	25.01	27.95	30	pass
N78-3700-3800	30	10	DFT-16QAM	M	Inner_1RB_Left	24.9	27.84	30	pass
N78-3700-3800	30	10	DFT-16QAM	M	Inner_1RB_Right	24.98	27.92	30	pass
N78-3700-3800	30	10	DFT-16QAM	M	Outer_Full	24.07	27.01	30	pass
N78-3700-3800	30	10	DFT-64QAM	M	Inner_1RB_Left	23.38	26.32	30	pass
N78-3700-3800	30	10	DFT-64QAM	M	Inner_1RB_Right	23.46	26.4	30	pass
N78-3700-3800	30	10	DFT-64QAM	M	Outer_Full	23.48	26.42	30	pass
N78-3700-3800	30	10	DFT-256QAM	M	Inner_1RB_Left	21.29	24.23	30	pass
N78-3700-3800	30	10	DFT-256QAM	M	Inner_1RB_Right	21.61	24.55	30	pass



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N78-3700-3800	30	10	DFT-256QAM	M	Outer_Full	21.46	24.4	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	H	Inner_1RB_Left	26.27	29.21	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	H	Inner_1RB_Right	26.37	29.31	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	H	Outer_Full	25.89	28.83	30	pass
N78-3700-3800	30	10	DFT-QPSK	H	Inner_1RB_Left	26.35	29.29	30	pass
N78-3700-3800	30	10	DFT-QPSK	H	Inner_1RB_Right	26.42	29.36	30	pass
N78-3700-3800	30	10	DFT-QPSK	H	Outer_Full	25.38	28.32	30	pass
N78-3700-3800	30	10	DFT-16QAM	H	Inner_1RB_Left	25.18	28.12	30	pass
N78-3700-3800	30	10	DFT-16QAM	H	Inner_1RB_Right	25.37	28.31	30	pass
N78-3700-3800	30	10	DFT-16QAM	H	Outer_Full	24.32	27.26	30	pass
N78-3700-3800	30	10	DFT-64QAM	H	Inner_1RB_Left	23.68	26.62	30	pass
N78-3700-3800	30	10	DFT-64QAM	H	Inner_1RB_Right	23.92	26.86	30	pass
N78-3700-3800	30	10	DFT-64QAM	H	Outer_Full	23.86	26.8	30	pass
N78-3700-3800	30	10	DFT-256QAM	H	Inner_1RB_Left	21.68	24.62	30	pass
N78-3700-3800	30	10	DFT-256QAM	H	Inner_1RB_Right	21.92	24.86	30	pass
N78-3700-3800	30	10	DFT-256QAM	H	Outer_Full	21.85	24.79	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	L	Inner_1RB_Left	26.21	29.15	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	L	Inner_1RB_Right	26.28	29.22	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	L	Outer_Full	25.73	28.67	30	pass
N78-3700-3800	30	15	DFT-QPSK	L	Inner_1RB_Left	26.18	29.12	30	pass
N78-3700-3800	30	15	DFT-QPSK	L	Inner_1RB_Right	26.23	29.17	30	pass
N78-3700-3800	30	15	DFT-QPSK	L	Outer_Full	25.2	28.14	30	pass
N78-3700-3800	30	15	DFT-16QAM	L	Inner_1RB_Left	25.23	28.17	30	pass
N78-3700-3800	30	15	DFT-16QAM	L	Inner_1RB_Right	25.23	28.17	30	pass
N78-3700-3800	30	15	DFT-16QAM	L	Outer_Full	24.22	27.16	30	pass
N78-3700-3800	30	15	DFT-64QAM	L	Inner_1RB_Left	23.62	26.56	30	pass
N78-3700-3800	30	15	DFT-64QAM	L	Inner_1RB_Right	23.67	26.61	30	pass
N78-3700-3800	30	15	DFT-64QAM	L	Outer_Full	23.75	26.69	30	pass
N78-3700-3800	30	15	DFT-256QAM	L	Inner_1RB_Left	21.64	24.58	30	pass
N78-3700-3800	30	15	DFT-256QAM	L	Inner_1RB_Right	21.67	24.61	30	pass
N78-3700-3800	30	15	DFT-256QAM	L	Outer_Full	21.73	24.67	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	M	Inner_1RB_Left	26.12	29.06	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	M	Inner_1RB_Right	26.17	29.11	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	M	Outer_Full	25.58	28.52	30	pass



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N78-3700-3800	30	15	DFT-QPSK	M	Inner_1RB_Left	26.15	29.09	30	pass
N78-3700-3800	30	15	DFT-QPSK	M	Inner_1RB_Right	26.17	29.11	30	pass
N78-3700-3800	30	15	DFT-QPSK	M	Outer_Full	25.07	28.01	30	pass
N78-3700-3800	30	15	DFT-16QAM	M	Inner_1RB_Left	25.11	28.05	30	pass
N78-3700-3800	30	15	DFT-16QAM	M	Inner_1RB_Right	25.11	28.05	30	pass
N78-3700-3800	30	15	DFT-16QAM	M	Outer_Full	24.02	26.96	30	pass
N78-3700-3800	30	15	DFT-64QAM	M	Inner_1RB_Left	23.59	26.53	30	pass
N78-3700-3800	30	15	DFT-64QAM	M	Inner_1RB_Right	23.41	26.35	30	pass
N78-3700-3800	30	15	DFT-64QAM	M	Outer_Full	23.55	26.49	30	pass
N78-3700-3800	30	15	DFT-256QAM	M	Inner_1RB_Left	21.63	24.57	30	pass
N78-3700-3800	30	15	DFT-256QAM	M	Inner_1RB_Right	21.72	24.66	30	pass
N78-3700-3800	30	15	DFT-256QAM	M	Outer_Full	21.59	24.53	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	H	Inner_1RB_Left	26.22	29.16	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	H	Inner_1RB_Right	26.47	29.41	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	H	Outer_Full	25.84	28.78	30	pass
N78-3700-3800	30	15	DFT-QPSK	H	Inner_1RB_Left	26.19	29.13	30	pass
N78-3700-3800	30	15	DFT-QPSK	H	Inner_1RB_Right	26.45	29.39	30	pass
N78-3700-3800	30	15	DFT-QPSK	H	Outer_Full	25.29	28.23	30	pass
N78-3700-3800	30	15	DFT-16QAM	H	Inner_1RB_Left	25.22	28.16	30	pass
N78-3700-3800	30	15	DFT-16QAM	H	Inner_1RB_Right	25.4	28.34	30	pass
N78-3700-3800	30	15	DFT-16QAM	H	Outer_Full	24.35	27.29	30	pass
N78-3700-3800	30	15	DFT-64QAM	H	Inner_1RB_Left	23.75	26.69	30	pass
N78-3700-3800	30	15	DFT-64QAM	H	Inner_1RB_Right	23.86	26.8	30	pass
N78-3700-3800	30	15	DFT-64QAM	H	Outer_Full	23.83	26.77	30	pass
N78-3700-3800	30	15	DFT-256QAM	H	Inner_1RB_Left	21.78	24.72	30	pass
N78-3700-3800	30	15	DFT-256QAM	H	Inner_1RB_Right	22.05	24.99	30	pass
N78-3700-3800	30	15	DFT-256QAM	H	Outer_Full	21.84	24.78	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	L	Inner_1RB_Left	26.14	29.08	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	L	Inner_1RB_Right	26.21	29.15	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	L	Outer_Full	25.66	28.6	30	pass
N78-3700-3800	30	20	DFT-QPSK	L	Inner_1RB_Left	26.14	29.08	30	pass
N78-3700-3800	30	20	DFT-QPSK	L	Inner_1RB_Right	26.13	29.07	30	pass
N78-3700-3800	30	20	DFT-QPSK	L	Outer_Full	25.19	28.13	30	pass
N78-3700-3800	30	20	DFT-16QAM	L	Inner_1RB_Left	25.05	27.99	30	pass



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N78-3700-3800	30	20	DFT-16QAM	L	Inner_1RB_Right	25.19	28.13	30	pass
N78-3700-3800	30	20	DFT-16QAM	L	Outer_Full	24.17	27.11	30	pass
N78-3700-3800	30	20	DFT-64QAM	L	Inner_1RB_Left	23.57	26.51	30	pass
N78-3700-3800	30	20	DFT-64QAM	L	Inner_1RB_Right	23.64	26.58	30	pass
N78-3700-3800	30	20	DFT-64QAM	L	Outer_Full	23.68	26.62	30	pass
N78-3700-3800	30	20	DFT-256QAM	L	Inner_1RB_Left	21.6	24.54	30	pass
N78-3700-3800	30	20	DFT-256QAM	L	Inner_1RB_Right	21.75	24.69	30	pass
N78-3700-3800	30	20	DFT-256QAM	L	Outer_Full	21.69	24.63	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	M	Inner_1RB_Left	26.2	29.14	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	M	Inner_1RB_Right	26.14	29.08	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	M	Outer_Full	25.58	28.52	30	pass
N78-3700-3800	30	20	DFT-QPSK	M	Inner_1RB_Left	26.14	29.08	30	pass
N78-3700-3800	30	20	DFT-QPSK	M	Inner_1RB_Right	26.18	29.12	30	pass
N78-3700-3800	30	20	DFT-QPSK	M	Outer_Full	25.07	28.01	30	pass
N78-3700-3800	30	20	DFT-16QAM	M	Inner_1RB_Left	25.12	28.06	30	pass
N78-3700-3800	30	20	DFT-16QAM	M	Inner_1RB_Right	25.11	28.05	30	pass
N78-3700-3800	30	20	DFT-16QAM	M	Outer_Full	24.07	27.01	30	pass
N78-3700-3800	30	20	DFT-64QAM	M	Inner_1RB_Left	23.61	26.55	30	pass
N78-3700-3800	30	20	DFT-64QAM	M	Inner_1RB_Right	23.49	26.43	30	pass
N78-3700-3800	30	20	DFT-64QAM	M	Outer_Full	23.55	26.49	30	pass
N78-3700-3800	30	20	DFT-256QAM	M	Inner_1RB_Left	21.88	24.82	30	pass
N78-3700-3800	30	20	DFT-256QAM	M	Inner_1RB_Right	21.64	24.58	30	pass
N78-3700-3800	30	20	DFT-256QAM	M	Outer_Full	21.59	24.53	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	H	Inner_1RB_Left	26.23	29.17	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	H	Inner_1RB_Right	26.48	29.42	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	H	Outer_Full	25.84	28.78	30	pass
N78-3700-3800	30	20	DFT-QPSK	H	Inner_1RB_Left	26.2	29.14	30	pass
N78-3700-3800	30	20	DFT-QPSK	H	Inner_1RB_Right	26.48	29.42	30	pass
N78-3700-3800	30	20	DFT-QPSK	H	Outer_Full	25.32	28.26	30	pass
N78-3700-3800	30	20	DFT-16QAM	H	Inner_1RB_Left	25.25	28.19	30	pass
N78-3700-3800	30	20	DFT-16QAM	H	Inner_1RB_Right	25.41	28.35	30	pass
N78-3700-3800	30	20	DFT-16QAM	H	Outer_Full	24.28	27.22	30	pass
N78-3700-3800	30	20	DFT-64QAM	H	Inner_1RB_Left	23.66	26.6	30	pass
N78-3700-3800	30	20	DFT-64QAM	H	Inner_1RB_Right	23.94	26.88	30	pass



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N78-3700-3800	30	20	DFT-64QAM	H	Outer_Full	23.81	26.75	30	pass
N78-3700-3800	30	20	DFT-256QAM	H	Inner_1RB_Left	21.78	24.72	30	pass
N78-3700-3800	30	20	DFT-256QAM	H	Inner_1RB_Right	21.84	24.78	30	pass
N78-3700-3800	30	20	DFT-256QAM	H	Outer_Full	21.83	24.77	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	L	Inner_1RB_Left	26.22	29.16	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	L	Inner_1RB_Right	26.26	29.2	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	L	Outer_Full	25.7	28.64	30	pass
N78-3700-3800	30	30	DFT-QPSK	L	Inner_1RB_Left	26.23	29.17	30	pass
N78-3700-3800	30	30	DFT-QPSK	L	Inner_1RB_Right	26.28	29.22	30	pass
N78-3700-3800	30	30	DFT-QPSK	L	Outer_Full	25.68	28.62	30	pass
N78-3700-3800	30	30	DFT-16QAM	L	Inner_1RB_Left	25.14	28.08	30	pass
N78-3700-3800	30	30	DFT-16QAM	L	Inner_1RB_Right	25.28	28.22	30	pass
N78-3700-3800	30	30	DFT-16QAM	L	Outer_Full	24.16	27.1	30	pass
N78-3700-3800	30	30	DFT-64QAM	L	Inner_1RB_Left	23.67	26.61	30	pass
N78-3700-3800	30	30	DFT-64QAM	L	Inner_1RB_Right	23.66	26.6	30	pass
N78-3700-3800	30	30	DFT-64QAM	L	Outer_Full	23.65	26.59	30	pass
N78-3700-3800	30	30	DFT-256QAM	L	Inner_1RB_Left	21.64	24.58	30	pass
N78-3700-3800	30	30	DFT-256QAM	L	Inner_1RB_Right	21.72	24.66	30	pass
N78-3700-3800	30	30	DFT-256QAM	L	Outer_Full	21.68	24.62	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	M	Inner_1RB_Left	26.35	29.29	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	M	Inner_1RB_Right	26.26	29.2	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	M	Outer_Full	25.72	28.66	30	pass
N78-3700-3800	30	30	DFT-QPSK	M	Inner_1RB_Left	26.37	29.31	30	pass
N78-3700-3800	30	30	DFT-QPSK	M	Inner_1RB_Right	26.27	29.21	30	pass
N78-3700-3800	30	30	DFT-QPSK	M	Outer_Full	25.19	28.13	30	pass
N78-3700-3800	30	30	DFT-16QAM	M	Inner_1RB_Left	25.25	28.19	30	pass
N78-3700-3800	30	30	DFT-16QAM	M	Inner_1RB_Right	25.23	28.17	30	pass
N78-3700-3800	30	30	DFT-16QAM	M	Outer_Full	24.16	27.1	30	pass
N78-3700-3800	30	30	DFT-64QAM	M	Inner_1RB_Left	23.84	26.78	30	pass
N78-3700-3800	30	30	DFT-64QAM	M	Inner_1RB_Right	23.72	26.66	30	pass
N78-3700-3800	30	30	DFT-64QAM	M	Outer_Full	23.7	26.64	30	pass
N78-3700-3800	30	30	DFT-256QAM	M	Inner_1RB_Left	21.81	24.75	30	pass
N78-3700-3800	30	30	DFT-256QAM	M	Inner_1RB_Right	21.8	24.74	30	pass
N78-3700-3800	30	30	DFT-256QAM	M	Outer_Full	21.74	24.68	30	pass



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N78-3700-3800	30	30	DFT-PI2BPSK	H	Inner_1RB_Left	26.37	29.31	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	H	Inner_1RB_Right	26.64	29.58	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	H	Outer_Full	25.96	28.9	30	pass
N78-3700-3800	30	30	DFT-QPSK	H	Inner_1RB_Left	26.42	29.36	30	pass
N78-3700-3800	30	30	DFT-QPSK	H	Inner_1RB_Right	26.62	29.56	30	pass
N78-3700-3800	30	30	DFT-QPSK	H	Outer_Full	25.42	28.36	30	pass
N78-3700-3800	30	30	DFT-16QAM	H	Inner_1RB_Left	25.32	28.26	30	pass
N78-3700-3800	30	30	DFT-16QAM	H	Inner_1RB_Right	25.66	28.6	30	pass
N78-3700-3800	30	30	DFT-16QAM	H	Outer_Full	24.42	27.36	30	pass
N78-3700-3800	30	30	DFT-64QAM	H	Inner_1RB_Left	23.87	26.81	30	pass
N78-3700-3800	30	30	DFT-64QAM	H	Inner_1RB_Right	23.98	26.92	30	pass
N78-3700-3800	30	30	DFT-64QAM	H	Outer_Full	23.84	26.78	30	pass
N78-3700-3800	30	30	DFT-256QAM	H	Inner_1RB_Left	21.87	24.81	30	pass
N78-3700-3800	30	30	DFT-256QAM	H	Inner_1RB_Right	22.21	25.15	30	pass
N78-3700-3800	30	30	DFT-256QAM	H	Outer_Full	21.93	24.87	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	L	Inner_1RB_Left	26.3	29.24	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	L	Inner_1RB_Right	26.38	29.32	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	L	Outer_Full	25.69	28.63	30	pass
N78-3700-3800	30	40	DFT-QPSK	L	Inner_1RB_Left	26.29	29.23	30	pass
N78-3700-3800	30	40	DFT-QPSK	L	Inner_1RB_Right	26.39	29.33	30	pass
N78-3700-3800	30	40	DFT-QPSK	L	Outer_Full	25.23	28.17	30	pass
N78-3700-3800	30	40	DFT-16QAM	L	Inner_1RB_Left	25.23	28.17	30	pass
N78-3700-3800	30	40	DFT-16QAM	L	Inner_1RB_Right	25.27	28.21	30	pass
N78-3700-3800	30	40	DFT-16QAM	L	Outer_Full	24.2	27.14	30	pass
N78-3700-3800	30	40	DFT-64QAM	L	Inner_1RB_Left	23.84	26.78	30	pass
N78-3700-3800	30	40	DFT-64QAM	L	Inner_1RB_Right	23.74	26.68	30	pass
N78-3700-3800	30	40	DFT-64QAM	L	Outer_Full	23.77	26.71	30	pass
N78-3700-3800	30	40	DFT-256QAM	L	Inner_1RB_Left	21.78	24.72	30	pass
N78-3700-3800	30	40	DFT-256QAM	L	Inner_1RB_Right	21.72	24.66	30	pass
N78-3700-3800	30	40	DFT-256QAM	L	Outer_Full	21.72	24.66	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	M	Inner_1RB_Left	26.52	29.46	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	M	Inner_1RB_Right	26.34	29.28	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	M	Outer_Full	25.73	28.67	30	pass
N78-3700-3800	30	40	DFT-QPSK	M	Inner_1RB_Left	26.43	29.37	30	pass



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N78-3700-3800	30	40	DFT-QPSK	M	Inner_1RB_Right	26.34	29.28	30	pass
N78-3700-3800	30	40	DFT-QPSK	M	Outer_Full	25.23	28.17	30	pass
N78-3700-3800	30	40	DFT-16QAM	M	Inner_1RB_Left	25.39	28.33	30	pass
N78-3700-3800	30	40	DFT-16QAM	M	Inner_1RB_Right	25.25	28.19	30	pass
N78-3700-3800	30	40	DFT-16QAM	M	Outer_Full	24.23	27.17	30	pass
N78-3700-3800	30	40	DFT-64QAM	M	Inner_1RB_Left	23.84	26.78	30	pass
N78-3700-3800	30	40	DFT-64QAM	M	Inner_1RB_Right	23.69	26.63	30	pass
N78-3700-3800	30	40	DFT-64QAM	M	Outer_Full	23.74	26.68	30	pass
N78-3700-3800	30	40	DFT-256QAM	M	Inner_1RB_Left	21.95	24.89	30	pass
N78-3700-3800	30	40	DFT-256QAM	M	Inner_1RB_Right	21.74	24.68	30	pass
N78-3700-3800	30	40	DFT-256QAM	M	Outer_Full	21.75	24.69	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	H	Inner_1RB_Left	26.27	29.21	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	H	Inner_1RB_Right	26.43	29.37	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	H	Outer_Full	26.63	29.57	30	pass
N78-3700-3800	30	40	DFT-QPSK	H	Inner_1RB_Left	26.26	29.2	30	pass
N78-3700-3800	30	40	DFT-QPSK	H	Inner_1RB_Right	26.46	29.4	30	pass
N78-3700-3800	30	40	DFT-QPSK	H	Outer_Full	25.15	28.09	30	pass
N78-3700-3800	30	40	DFT-16QAM	H	Inner_1RB_Left	25.29	28.23	30	pass
N78-3700-3800	30	40	DFT-16QAM	H	Inner_1RB_Right	25.4	28.34	30	pass
N78-3700-3800	30	40	DFT-16QAM	H	Outer_Full	24.15	27.09	30	pass
N78-3700-3800	30	40	DFT-64QAM	H	Inner_1RB_Left	23.64	26.58	30	pass
N78-3700-3800	30	40	DFT-64QAM	H	Inner_1RB_Right	23.98	26.92	30	pass
N78-3700-3800	30	40	DFT-64QAM	H	Outer_Full	23.61	26.55	30	pass
N78-3700-3800	30	40	DFT-256QAM	H	Inner_1RB_Left	21.8	24.74	30	pass
N78-3700-3800	30	40	DFT-256QAM	H	Inner_1RB_Right	21.94	24.88	30	pass
N78-3700-3800	30	40	DFT-256QAM	H	Outer_Full	21.64	24.58	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	L	Inner_1RB_Left	26.15	29.09	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	L	Inner_1RB_Right	26.01	28.95	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	L	Outer_Full	25.69	28.63	30	pass
N78-3700-3800	30	50	DFT-QPSK	L	Inner_1RB_Left	26.11	29.05	30	pass
N78-3700-3800	30	50	DFT-QPSK	L	Inner_1RB_Right	25.92	28.86	30	pass
N78-3700-3800	30	50	DFT-QPSK	L	Outer_Full	25.19	28.13	30	pass
N78-3700-3800	30	50	DFT-16QAM	L	Inner_1RB_Left	25.04	27.98	30	pass
N78-3700-3800	30	50	DFT-16QAM	L	Inner_1RB_Right	24.89	27.83	30	pass



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N78-3700-3800	30	50	DFT-16QAM	L	Outer_Full	24.19	27.13	30	pass
N78-3700-3800	30	50	DFT-64QAM	L	Inner_1RB_Left	23.61	26.55	30	pass
N78-3700-3800	30	50	DFT-64QAM	L	Inner_1RB_Right	23.29	26.23	30	pass
N78-3700-3800	30	50	DFT-64QAM	L	Outer_Full	23.69	26.63	30	pass
N78-3700-3800	30	50	DFT-256QAM	L	Inner_1RB_Left	21.7	24.64	30	pass
N78-3700-3800	30	50	DFT-256QAM	L	Inner_1RB_Right	21.45	24.39	30	pass
N78-3700-3800	30	50	DFT-256QAM	L	Outer_Full	21.73	24.67	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	M	Inner_1RB_Left	26.16	29.1	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	M	Inner_1RB_Right	25.97	28.91	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	M	Outer_Full	25.52	28.46	30	pass
N78-3700-3800	30	50	DFT-QPSK	M	Inner_1RB_Left	26.14	29.08	30	pass
N78-3700-3800	30	50	DFT-QPSK	M	Inner_1RB_Right	26.01	28.95	30	pass
N78-3700-3800	30	50	DFT-QPSK	M	Outer_Full	24.99	27.93	30	pass
N78-3700-3800	30	50	DFT-16QAM	M	Inner_1RB_Left	25.07	28.01	30	pass
N78-3700-3800	30	50	DFT-16QAM	M	Inner_1RB_Right	24.91	27.85	30	pass
N78-3700-3800	30	50	DFT-16QAM	M	Outer_Full	23.94	26.88	30	pass
N78-3700-3800	30	50	DFT-64QAM	M	Inner_1RB_Left	23.59	26.53	30	pass
N78-3700-3800	30	50	DFT-64QAM	M	Inner_1RB_Right	23.42	26.36	30	pass
N78-3700-3800	30	50	DFT-64QAM	M	Outer_Full	23.46	26.4	30	pass
N78-3700-3800	30	50	DFT-256QAM	M	Inner_1RB_Left	21.66	24.6	30	pass
N78-3700-3800	30	50	DFT-256QAM	M	Inner_1RB_Right	21.34	24.28	30	pass
N78-3700-3800	30	50	DFT-256QAM	M	Outer_Full	21.49	24.43	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	H	Inner_1RB_Left	25.86	28.8	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	H	Inner_1RB_Right	26.11	29.05	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	H	Outer_Full	25.45	28.39	30	pass
N78-3700-3800	30	50	DFT-QPSK	H	Inner_1RB_Left	25.79	28.73	30	pass
N78-3700-3800	30	50	DFT-QPSK	H	Inner_1RB_Right	26.06	29	30	pass
N78-3700-3800	30	50	DFT-QPSK	H	Outer_Full	24.94	27.88	30	pass
N78-3700-3800	30	50	DFT-16QAM	H	Inner_1RB_Left	24.83	27.77	30	pass
N78-3700-3800	30	50	DFT-16QAM	H	Inner_1RB_Right	24.98	27.92	30	pass
N78-3700-3800	30	50	DFT-16QAM	H	Outer_Full	23.91	26.85	30	pass
N78-3700-3800	30	50	DFT-64QAM	H	Inner_1RB_Left	23.32	26.26	30	pass
N78-3700-3800	30	50	DFT-64QAM	H	Inner_1RB_Right	23.47	26.41	30	pass
N78-3700-3800	30	50	DFT-64QAM	H	Outer_Full	23.43	26.37	30	pass



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N78-3700-3800	30	50	DFT-256QAM	H	Inner_1RB_Left	21.39	24.33	30	pass
N78-3700-3800	30	50	DFT-256QAM	H	Inner_1RB_Right	21.67	24.61	30	pass
N78-3700-3800	30	50	DFT-256QAM	H	Outer_Full	21.36	24.3	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	L	Inner_1RB_Left	26.04	28.98	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	L	Inner_1RB_Right	26.01	28.95	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	L	Outer_Full	25.66	28.6	30	pass
N78-3700-3800	30	60	DFT-QPSK	L	Inner_1RB_Left	26.03	28.97	30	pass
N78-3700-3800	30	60	DFT-QPSK	L	Inner_1RB_Right	26	28.94	30	pass
N78-3700-3800	30	60	DFT-QPSK	L	Outer_Full	25.15	28.09	30	pass
N78-3700-3800	30	60	DFT-16QAM	L	Inner_1RB_Left	25.05	27.99	30	pass
N78-3700-3800	30	60	DFT-16QAM	L	Inner_1RB_Right	24.91	27.85	30	pass
N78-3700-3800	30	60	DFT-16QAM	L	Outer_Full	24.15	27.09	30	pass
N78-3700-3800	30	60	DFT-64QAM	L	Inner_1RB_Left	23.55	26.49	30	pass
N78-3700-3800	30	60	DFT-64QAM	L	Inner_1RB_Right	23.57	26.51	30	pass
N78-3700-3800	30	60	DFT-64QAM	L	Outer_Full	23.65	26.59	30	pass
N78-3700-3800	30	60	DFT-256QAM	L	Inner_1RB_Left	21.43	24.37	30	pass
N78-3700-3800	30	60	DFT-256QAM	L	Inner_1RB_Right	21.46	24.4	30	pass
N78-3700-3800	30	60	DFT-256QAM	L	Outer_Full	21.68	24.62	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	M	Inner_1RB_Left	26.08	29.02	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	M	Inner_1RB_Right	26.01	28.95	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	M	Outer_Full	25.57	28.51	30	pass
N78-3700-3800	30	60	DFT-QPSK	M	Inner_1RB_Left	26.09	29.03	30	pass
N78-3700-3800	30	60	DFT-QPSK	M	Inner_1RB_Right	25.98	28.92	30	pass
N78-3700-3800	30	60	DFT-QPSK	M	Outer_Full	24.97	27.91	30	pass
N78-3700-3800	30	60	DFT-16QAM	M	Inner_1RB_Left	25.03	27.97	30	pass
N78-3700-3800	30	60	DFT-16QAM	M	Inner_1RB_Right	24.94	27.88	30	pass
N78-3700-3800	30	60	DFT-16QAM	M	Outer_Full	24.01	26.95	30	pass
N78-3700-3800	30	60	DFT-64QAM	M	Inner_1RB_Left	23.58	26.52	30	pass
N78-3700-3800	30	60	DFT-64QAM	M	Inner_1RB_Right	23.43	26.37	30	pass
N78-3700-3800	30	60	DFT-64QAM	M	Outer_Full	23.47	26.41	30	pass
N78-3700-3800	30	60	DFT-256QAM	M	Inner_1RB_Left	21.53	24.47	30	pass
N78-3700-3800	30	60	DFT-256QAM	M	Inner_1RB_Right	21.51	24.45	30	pass
N78-3700-3800	30	60	DFT-256QAM	M	Outer_Full	21.43	24.37	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	H	Inner_1RB_Left	25.91	28.85	30	pass



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N78-3700-3800	30	60	DFT-PI2BPSK	H	Inner_1RB_Right	26.19	29.13	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	H	Outer_Full	25.48	28.42	30	pass
N78-3700-3800	30	60	DFT-QPSK	H	Inner_1RB_Left	25.99	28.93	30	pass
N78-3700-3800	30	60	DFT-QPSK	H	Inner_1RB_Right	26.19	29.13	30	pass
N78-3700-3800	30	60	DFT-QPSK	H	Outer_Full	24.96	27.9	30	pass
N78-3700-3800	30	60	DFT-16QAM	H	Inner_1RB_Left	24.8	27.74	30	pass
N78-3700-3800	30	60	DFT-16QAM	H	Inner_1RB_Right	25.19	28.13	30	pass
N78-3700-3800	30	60	DFT-16QAM	H	Outer_Full	23.89	26.83	30	pass
N78-3700-3800	30	60	DFT-64QAM	H	Inner_1RB_Left	23.3	26.24	30	pass
N78-3700-3800	30	60	DFT-64QAM	H	Inner_1RB_Right	23.76	26.7	30	pass
N78-3700-3800	30	60	DFT-64QAM	H	Outer_Full	23.43	26.37	30	pass
N78-3700-3800	30	60	DFT-256QAM	H	Inner_1RB_Left	21.42	24.36	30	pass
N78-3700-3800	30	60	DFT-256QAM	H	Inner_1RB_Right	21.68	24.62	30	pass
N78-3700-3800	30	60	DFT-256QAM	H	Outer_Full	21.46	24.4	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	L	Inner_1RB_Left	25.93	28.87	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	L	Inner_1RB_Right	25.94	28.88	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	L	Outer_Full	25.59	28.53	30	pass
N78-3700-3800	30	70	DFT-QPSK	L	Inner_1RB_Left	25.92	28.86	30	pass
N78-3700-3800	30	70	DFT-QPSK	L	Inner_1RB_Right	25.95	28.89	30	pass
N78-3700-3800	30	70	DFT-QPSK	L	Outer_Full	25.07	28.01	30	pass
N78-3700-3800	30	70	DFT-16QAM	L	Inner_1RB_Left	24.88	27.82	30	pass
N78-3700-3800	30	70	DFT-16QAM	L	Inner_1RB_Right	24.87	27.81	30	pass
N78-3700-3800	30	70	DFT-16QAM	L	Outer_Full	24.08	27.02	30	pass
N78-3700-3800	30	70	DFT-64QAM	L	Inner_1RB_Left	23.4	26.34	30	pass
N78-3700-3800	30	70	DFT-64QAM	L	Inner_1RB_Right	23.28	26.22	30	pass
N78-3700-3800	30	70	DFT-64QAM	L	Outer_Full	23.54	26.48	30	pass
N78-3700-3800	30	70	DFT-256QAM	L	Inner_1RB_Left	21.48	24.42	30	pass
N78-3700-3800	30	70	DFT-256QAM	L	Inner_1RB_Right	21.49	24.43	30	pass
N78-3700-3800	30	70	DFT-256QAM	L	Outer_Full	21.51	24.45	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	M	Inner_1RB_Left	26.02	28.96	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	M	Inner_1RB_Right	25.85	28.79	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	M	Outer_Full	25.48	28.42	30	pass
N78-3700-3800	30	70	DFT-QPSK	M	Inner_1RB_Left	26.05	28.99	30	pass
N78-3700-3800	30	70	DFT-QPSK	M	Inner_1RB_Right	25.87	28.81	30	pass



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N78-3700-3800	30	70	DFT-QPSK	M	Outer_Full	24.97	27.91	30	pass
N78-3700-3800	30	70	DFT-16QAM	M	Inner_1RB_Left	24.96	27.9	30	pass
N78-3700-3800	30	70	DFT-16QAM	M	Inner_1RB_Right	24.86	27.8	30	pass
N78-3700-3800	30	70	DFT-16QAM	M	Outer_Full	23.94	26.88	30	pass
N78-3700-3800	30	70	DFT-64QAM	M	Inner_1RB_Left	23.44	26.38	30	pass
N78-3700-3800	30	70	DFT-64QAM	M	Inner_1RB_Right	23.24	26.18	30	pass
N78-3700-3800	30	70	DFT-64QAM	M	Outer_Full	23.47	26.41	30	pass
N78-3700-3800	30	70	DFT-256QAM	M	Inner_1RB_Left	21.59	24.53	30	pass
N78-3700-3800	30	70	DFT-256QAM	M	Inner_1RB_Right	21.35	24.29	30	pass
N78-3700-3800	30	70	DFT-256QAM	M	Outer_Full	21.45	24.39	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	H	Inner_1RB_Left	26.06	29	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	H	Inner_1RB_Right	26.16	29.1	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	H	Outer_Full	25.52	28.46	30	pass
N78-3700-3800	30	70	DFT-QPSK	H	Inner_1RB_Left	26.03	28.97	30	pass
N78-3700-3800	30	70	DFT-QPSK	H	Inner_1RB_Right	26.2	29.14	30	pass
N78-3700-3800	30	70	DFT-QPSK	H	Outer_Full	25.01	27.95	30	pass
N78-3700-3800	30	70	DFT-16QAM	H	Inner_1RB_Left	24.97	27.91	30	pass
N78-3700-3800	30	70	DFT-16QAM	H	Inner_1RB_Right	25.14	28.08	30	pass
N78-3700-3800	30	70	DFT-16QAM	H	Outer_Full	23.97	26.91	30	pass
N78-3700-3800	30	70	DFT-64QAM	H	Inner_1RB_Left	23.48	26.42	30	pass
N78-3700-3800	30	70	DFT-64QAM	H	Inner_1RB_Right	23.58	26.52	30	pass
N78-3700-3800	30	70	DFT-64QAM	H	Outer_Full	23.45	26.39	30	pass
N78-3700-3800	30	70	DFT-256QAM	H	Inner_1RB_Left	21.45	24.39	30	pass
N78-3700-3800	30	70	DFT-256QAM	H	Inner_1RB_Right	21.59	24.53	30	pass
N78-3700-3800	30	70	DFT-256QAM	H	Outer_Full	21.53	24.47	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	L	Inner_1RB_Left	25.86	28.8	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	L	Inner_1RB_Right	25.9	28.84	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	L	Outer_Full	25.52	28.46	30	pass
N78-3700-3800	30	80	DFT-QPSK	L	Inner_1RB_Left	25.88	28.82	30	pass
N78-3700-3800	30	80	DFT-QPSK	L	Inner_1RB_Right	25.94	28.88	30	pass
N78-3700-3800	30	80	DFT-QPSK	L	Outer_Full	24.99	27.93	30	pass
N78-3700-3800	30	80	DFT-16QAM	L	Inner_1RB_Left	24.77	27.71	30	pass
N78-3700-3800	30	80	DFT-16QAM	L	Inner_1RB_Right	25.01	27.95	30	pass
N78-3700-3800	30	80	DFT-16QAM	L	Outer_Full	24.01	26.95	30	pass



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N78-3700-3800	30	80	DFT-64QAM	L	Inner_1RB_Left	23.34	26.28	30	pass
N78-3700-3800	30	80	DFT-64QAM	L	Inner_1RB_Right	23.27	26.21	30	pass
N78-3700-3800	30	80	DFT-64QAM	L	Outer_Full	23.52	26.46	30	pass
N78-3700-3800	30	80	DFT-256QAM	L	Inner_1RB_Left	21.37	24.31	30	pass
N78-3700-3800	30	80	DFT-256QAM	L	Inner_1RB_Right	21.35	24.29	30	pass
N78-3700-3800	30	80	DFT-256QAM	L	Outer_Full	21.46	24.4	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	M	Inner_1RB_Left	25.97	28.91	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	M	Inner_1RB_Right	25.94	28.88	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	M	Outer_Full	25.43	28.37	30	pass
N78-3700-3800	30	80	DFT-QPSK	M	Inner_1RB_Left	26	28.94	30	pass
N78-3700-3800	30	80	DFT-QPSK	M	Inner_1RB_Right	25.99	28.93	30	pass
N78-3700-3800	30	80	DFT-QPSK	M	Outer_Full	24.93	27.87	30	pass
N78-3700-3800	30	80	DFT-16QAM	M	Inner_1RB_Left	24.83	27.77	30	pass
N78-3700-3800	30	80	DFT-16QAM	M	Inner_1RB_Right	24.8	27.74	30	pass
N78-3700-3800	30	80	DFT-16QAM	M	Outer_Full	23.93	26.87	30	pass
N78-3700-3800	30	80	DFT-64QAM	M	Inner_1RB_Left	23.44	26.38	30	pass
N78-3700-3800	30	80	DFT-64QAM	M	Inner_1RB_Right	23.4	26.34	30	pass
N78-3700-3800	30	80	DFT-64QAM	M	Outer_Full	23.43	26.37	30	pass
N78-3700-3800	30	80	DFT-256QAM	M	Inner_1RB_Left	21.28	24.22	30	pass
N78-3700-3800	30	80	DFT-256QAM	M	Inner_1RB_Right	21.41	24.35	30	pass
N78-3700-3800	30	80	DFT-256QAM	M	Outer_Full	21.47	24.41	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	H	Inner_1RB_Left	25.95	28.89	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	H	Inner_1RB_Right	26.15	29.09	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	H	Outer_Full	25.53	28.47	30	pass
N78-3700-3800	30	80	DFT-QPSK	H	Inner_1RB_Left	26.09	29.03	30	pass
N78-3700-3800	30	80	DFT-QPSK	H	Inner_1RB_Right	26.14	29.08	30	pass
N78-3700-3800	30	80	DFT-QPSK	H	Outer_Full	25	27.94	30	pass
N78-3700-3800	30	80	DFT-16QAM	H	Inner_1RB_Left	24.9	27.84	30	pass
N78-3700-3800	30	80	DFT-16QAM	H	Inner_1RB_Right	25.09	28.03	30	pass
N78-3700-3800	30	80	DFT-16QAM	H	Outer_Full	24.02	26.96	30	pass
N78-3700-3800	30	80	DFT-64QAM	H	Inner_1RB_Left	23.39	26.33	30	pass
N78-3700-3800	30	80	DFT-64QAM	H	Inner_1RB_Right	23.58	26.52	30	pass
N78-3700-3800	30	80	DFT-64QAM	H	Outer_Full	23.51	26.45	30	pass
N78-3700-3800	30	80	DFT-256QAM	H	Inner_1RB_Left	21.58	24.52	30	pass



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N78-3700-3800	30	80	DFT-256QAM	H	Inner_1RB_Right	21.61	24.55	30	pass
N78-3700-3800	30	80	DFT-256QAM	H	Outer_Full	21.52	24.46	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	L	Inner_1RB_Left	25.89	28.83	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	L	Inner_1RB_Right	26.09	29.03	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	L	Outer_Full	25.5	28.44	30	pass
N78-3700-3800	30	90	DFT-QPSK	L	Inner_1RB_Left	25.91	28.85	30	pass
N78-3700-3800	30	90	DFT-QPSK	L	Inner_1RB_Right	26.07	29.01	30	pass
N78-3700-3800	30	90	DFT-QPSK	L	Outer_Full	24.99	27.93	30	pass
N78-3700-3800	30	90	DFT-16QAM	L	Inner_1RB_Left	24.89	27.83	30	pass
N78-3700-3800	30	90	DFT-16QAM	L	Inner_1RB_Right	25	27.94	30	pass
N78-3700-3800	30	90	DFT-16QAM	L	Outer_Full	23.98	26.92	30	pass
N78-3700-3800	30	90	DFT-64QAM	L	Inner_1RB_Left	23.27	26.21	30	pass
N78-3700-3800	30	90	DFT-64QAM	L	Inner_1RB_Right	23.48	26.42	30	pass
N78-3700-3800	30	90	DFT-64QAM	L	Outer_Full	23.51	26.45	30	pass
N78-3700-3800	30	90	DFT-256QAM	L	Inner_1RB_Left	21.31	24.25	30	pass
N78-3700-3800	30	90	DFT-256QAM	L	Inner_1RB_Right	21.52	24.46	30	pass
N78-3700-3800	30	90	DFT-256QAM	L	Outer_Full	21.5	24.44	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	M	Inner_1RB_Left	25.91	28.85	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	M	Inner_1RB_Right	26.13	29.07	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	M	Outer_Full	25.48	28.42	30	pass
N78-3700-3800	30	90	DFT-QPSK	M	Inner_1RB_Left	25.9	28.84	30	pass
N78-3700-3800	30	90	DFT-QPSK	M	Inner_1RB_Right	26.12	29.06	30	pass
N78-3700-3800	30	90	DFT-QPSK	M	Outer_Full	24.97	27.91	30	pass
N78-3700-3800	30	90	DFT-16QAM	M	Inner_1RB_Left	24.88	27.82	30	pass
N78-3700-3800	30	90	DFT-16QAM	M	Inner_1RB_Right	25.17	28.11	30	pass
N78-3700-3800	30	90	DFT-16QAM	M	Outer_Full	23.99	26.93	30	pass
N78-3700-3800	30	90	DFT-64QAM	M	Inner_1RB_Left	23.32	26.26	30	pass
N78-3700-3800	30	90	DFT-64QAM	M	Inner_1RB_Right	23.52	26.46	30	pass
N78-3700-3800	30	90	DFT-64QAM	M	Outer_Full	23.48	26.42	30	pass
N78-3700-3800	30	90	DFT-256QAM	M	Inner_1RB_Left	21.24	24.18	30	pass
N78-3700-3800	30	90	DFT-256QAM	M	Inner_1RB_Right	21.66	24.6	30	pass
N78-3700-3800	30	90	DFT-256QAM	M	Outer_Full	21.45	24.39	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	H	Inner_1RB_Left	25.95	28.89	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	H	Inner_1RB_Right	26.18	29.12	30	pass



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N78-3700-3800	30	90	DFT-PI2BPSK	H	Outer_Full	25.42	28.36	30	pass
N78-3700-3800	30	90	DFT-QPSK	H	Inner_1RB_Left	25.94	28.88	30	pass
N78-3700-3800	30	90	DFT-QPSK	H	Inner_1RB_Right	26.18	29.12	30	pass
N78-3700-3800	30	90	DFT-QPSK	H	Outer_Full	24.89	27.83	30	pass
N78-3700-3800	30	90	DFT-16QAM	H	Inner_1RB_Left	24.79	27.73	30	pass
N78-3700-3800	30	90	DFT-16QAM	H	Inner_1RB_Right	25.18	28.12	30	pass
N78-3700-3800	30	90	DFT-16QAM	H	Outer_Full	23.92	26.86	30	pass
N78-3700-3800	30	90	DFT-64QAM	H	Inner_1RB_Left	23.29	26.23	30	pass
N78-3700-3800	30	90	DFT-64QAM	H	Inner_1RB_Right	23.67	26.61	30	pass
N78-3700-3800	30	90	DFT-64QAM	H	Outer_Full	23.4	26.34	30	pass
N78-3700-3800	30	90	DFT-256QAM	H	Inner_1RB_Left	21.46	24.4	30	pass
N78-3700-3800	30	90	DFT-256QAM	H	Inner_1RB_Right	21.71	24.65	30	pass
N78-3700-3800	30	90	DFT-256QAM	H	Outer_Full	21.36	24.3	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	L	Inner_1RB_Left	25.82	28.76	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	L	Inner_1RB_Right	26.25	29.19	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	25.49	28.43	30	pass
N78-3700-3800	30	100	DFT-QPSK	L	Inner_1RB_Left	25.85	28.79	30	pass
N78-3700-3800	30	100	DFT-QPSK	L	Inner_1RB_Right	26.27	29.21	30	pass
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	24.97	27.91	30	pass
N78-3700-3800	30	100	DFT-16QAM	L	Inner_1RB_Left	24.82	27.76	30	pass
N78-3700-3800	30	100	DFT-16QAM	L	Inner_1RB_Right	25.1	28.04	30	pass
N78-3700-3800	30	100	DFT-16QAM	L	Outer_Full	23.96	26.9	30	pass
N78-3700-3800	30	100	DFT-64QAM	L	Inner_1RB_Left	23.33	26.27	30	pass
N78-3700-3800	30	100	DFT-64QAM	L	Inner_1RB_Right	23.7	26.64	30	pass
N78-3700-3800	30	100	DFT-64QAM	L	Outer_Full	23.45	26.39	30	pass
N78-3700-3800	30	100	DFT-256QAM	L	Inner_1RB_Left	21.31	24.25	30	pass
N78-3700-3800	30	100	DFT-256QAM	L	Inner_1RB_Right	21.75	24.69	30	pass
N78-3700-3800	30	100	DFT-256QAM	L	Outer_Full	21.53	24.47	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	M	Inner_1RB_Left	25.85	28.79	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	M	Inner_1RB_Right	26.27	29.21	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	25.49	28.43	30	pass
N78-3700-3800	30	100	DFT-QPSK	M	Inner_1RB_Left	25.84	28.78	30	pass
N78-3700-3800	30	100	DFT-QPSK	M	Inner_1RB_Right	26.28	29.22	30	pass
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	24.95	27.89	30	pass



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N78-3700-3800	30	100	DFT-16QAM	M	Inner_1RB_Left	24.83	27.77	30	pass
N78-3700-3800	30	100	DFT-16QAM	M	Inner_1RB_Right	25.24	28.18	30	pass
N78-3700-3800	30	100	DFT-16QAM	M	Outer_Full	24.03	26.97	30	pass
N78-3700-3800	30	100	DFT-64QAM	M	Inner_1RB_Left	23.37	26.31	30	pass
N78-3700-3800	30	100	DFT-64QAM	M	Inner_1RB_Right	23.73	26.67	30	pass
N78-3700-3800	30	100	DFT-64QAM	M	Outer_Full	23.45	26.39	30	pass
N78-3700-3800	30	100	DFT-256QAM	M	Inner_1RB_Left	21.41	24.35	30	pass
N78-3700-3800	30	100	DFT-256QAM	M	Inner_1RB_Right	21.76	24.7	30	pass
N78-3700-3800	30	100	DFT-256QAM	M	Outer_Full	21.49	24.43	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	H	Inner_1RB_Left	25.83	28.77	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	H	Inner_1RB_Right	26.27	29.21	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	25.49	28.43	30	pass
N78-3700-3800	30	100	DFT-QPSK	H	Inner_1RB_Left	25.85	28.79	30	pass
N78-3700-3800	30	100	DFT-QPSK	H	Inner_1RB_Right	26.28	29.22	30	pass
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	24.95	27.89	30	pass
N78-3700-3800	30	100	DFT-16QAM	H	Inner_1RB_Left	24.82	27.76	30	pass
N78-3700-3800	30	100	DFT-16QAM	H	Inner_1RB_Right	25.17	28.11	30	pass
N78-3700-3800	30	100	DFT-16QAM	H	Outer_Full	23.98	26.92	30	pass
N78-3700-3800	30	100	DFT-64QAM	H	Inner_1RB_Left	23.28	26.22	30	pass
N78-3700-3800	30	100	DFT-64QAM	H	Inner_1RB_Right	23.81	26.75	30	pass
N78-3700-3800	30	100	DFT-64QAM	H	Outer_Full	23.48	26.42	30	pass
N78-3700-3800	30	100	DFT-256QAM	H	Inner_1RB_Left	21.35	24.29	30	pass
N78-3700-3800	30	100	DFT-256QAM	H	Inner_1RB_Right	21.74	24.68	30	pass
N78-3700-3800	30	100	DFT-256QAM	H	Outer_Full	21.42	24.36	30	pass



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Effective (Isotropic) Radiated Power Output Data for UL MIMO

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Power	EIRP	LIMIT	verdict
N78-3700-3800	30	10	DFT-PI2BPSK	L	Inner_1RB_Left	25.98	28.92	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	L	Inner_1RB_Right	26.11	29.05	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	L	Outer_Full	25.57	28.51	30	pass
N78-3700-3800	30	10	DFT-QPSK	L	Inner_1RB_Left	26.03	28.97	30	pass
N78-3700-3800	30	10	DFT-QPSK	L	Inner_1RB_Right	26.03	28.97	30	pass
N78-3700-3800	30	10	DFT-QPSK	L	Outer_Full	25.05	27.99	30	pass
N78-3700-3800	30	10	DFT-16QAM	L	Inner_1RB_Left	25.03	27.97	30	pass
N78-3700-3800	30	10	DFT-16QAM	L	Inner_1RB_Right	24.98	27.92	30	pass
N78-3700-3800	30	10	DFT-16QAM	L	Outer_Full	24.04	26.98	30	pass
N78-3700-3800	30	10	DFT-64QAM	L	Inner_1RB_Left	23.37	26.31	30	pass
N78-3700-3800	30	10	DFT-64QAM	L	Inner_1RB_Right	23.45	26.39	30	pass
N78-3700-3800	30	10	DFT-64QAM	L	Outer_Full	23.53	26.47	30	pass
N78-3700-3800	30	10	DFT-256QAM	L	Inner_1RB_Left	21.57	24.51	30	pass
N78-3700-3800	30	10	DFT-256QAM	L	Inner_1RB_Right	21.53	24.47	30	pass
N78-3700-3800	30	10	DFT-256QAM	L	Outer_Full	21.56	24.5	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	M	Inner_1RB_Left	25.94	28.88	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	M	Inner_1RB_Right	26.01	28.95	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	M	Outer_Full	25.57	28.51	30	pass
N78-3700-3800	30	10	DFT-QPSK	M	Inner_1RB_Left	26.07	29.01	30	pass
N78-3700-3800	30	10	DFT-QPSK	M	Inner_1RB_Right	26.01	28.95	30	pass
N78-3700-3800	30	10	DFT-QPSK	M	Outer_Full	25.01	27.95	30	pass
N78-3700-3800	30	10	DFT-16QAM	M	Inner_1RB_Left	24.9	27.84	30	pass
N78-3700-3800	30	10	DFT-16QAM	M	Inner_1RB_Right	24.98	27.92	30	pass
N78-3700-3800	30	10	DFT-16QAM	M	Outer_Full	24.07	27.01	30	pass
N78-3700-3800	30	10	DFT-64QAM	M	Inner_1RB_Left	23.38	26.32	30	pass
N78-3700-3800	30	10	DFT-64QAM	M	Inner_1RB_Right	23.46	26.4	30	pass
N78-3700-3800	30	10	DFT-64QAM	M	Outer_Full	23.48	26.42	30	pass
N78-3700-3800	30	10	DFT-256QAM	M	Inner_1RB_Left	21.29	24.23	30	pass
N78-3700-3800	30	10	DFT-256QAM	M	Inner_1RB_Right	21.61	24.55	30	pass



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N78-3700-3800	30	10	DFT-256QAM	M	Outer_Full	21.46	24.4	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	H	Inner_1RB_Left	26.27	29.21	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	H	Inner_1RB_Right	26.37	29.31	30	pass
N78-3700-3800	30	10	DFT-PI2BPSK	H	Outer_Full	25.89	28.83	30	pass
N78-3700-3800	30	10	DFT-QPSK	H	Inner_1RB_Left	26.35	29.29	30	pass
N78-3700-3800	30	10	DFT-QPSK	H	Inner_1RB_Right	26.42	29.36	30	pass
N78-3700-3800	30	10	DFT-QPSK	H	Outer_Full	25.38	28.32	30	pass
N78-3700-3800	30	10	DFT-16QAM	H	Inner_1RB_Left	25.18	28.12	30	pass
N78-3700-3800	30	10	DFT-16QAM	H	Inner_1RB_Right	25.37	28.31	30	pass
N78-3700-3800	30	10	DFT-16QAM	H	Outer_Full	24.32	27.26	30	pass
N78-3700-3800	30	10	DFT-64QAM	H	Inner_1RB_Left	23.68	26.62	30	pass
N78-3700-3800	30	10	DFT-64QAM	H	Inner_1RB_Right	23.92	26.86	30	pass
N78-3700-3800	30	10	DFT-64QAM	H	Outer_Full	23.86	26.8	30	pass
N78-3700-3800	30	10	DFT-256QAM	H	Inner_1RB_Left	21.68	24.62	30	pass
N78-3700-3800	30	10	DFT-256QAM	H	Inner_1RB_Right	21.92	24.86	30	pass
N78-3700-3800	30	10	DFT-256QAM	H	Outer_Full	21.85	24.79	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	L	Inner_1RB_Left	26.21	29.15	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	L	Inner_1RB_Right	26.28	29.22	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	L	Outer_Full	25.73	28.67	30	pass
N78-3700-3800	30	15	DFT-QPSK	L	Inner_1RB_Left	26.18	29.12	30	pass
N78-3700-3800	30	15	DFT-QPSK	L	Inner_1RB_Right	26.23	29.17	30	pass
N78-3700-3800	30	15	DFT-QPSK	L	Outer_Full	25.2	28.14	30	pass
N78-3700-3800	30	15	DFT-16QAM	L	Inner_1RB_Left	25.23	28.17	30	pass
N78-3700-3800	30	15	DFT-16QAM	L	Inner_1RB_Right	25.23	28.17	30	pass
N78-3700-3800	30	15	DFT-16QAM	L	Outer_Full	24.22	27.16	30	pass
N78-3700-3800	30	15	DFT-64QAM	L	Inner_1RB_Left	23.62	26.56	30	pass
N78-3700-3800	30	15	DFT-64QAM	L	Inner_1RB_Right	23.67	26.61	30	pass
N78-3700-3800	30	15	DFT-64QAM	L	Outer_Full	23.75	26.69	30	pass
N78-3700-3800	30	15	DFT-256QAM	L	Inner_1RB_Left	21.64	24.58	30	pass
N78-3700-3800	30	15	DFT-256QAM	L	Inner_1RB_Right	21.67	24.61	30	pass
N78-3700-3800	30	15	DFT-256QAM	L	Outer_Full	21.73	24.67	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	M	Inner_1RB_Left	26.12	29.06	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	M	Inner_1RB_Right	26.17	29.11	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	M	Outer_Full	25.58	28.52	30	pass



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N78-3700-3800	30	15	DFT-QPSK	M	Inner_1RB_Left	26.15	29.09	30	pass
N78-3700-3800	30	15	DFT-QPSK	M	Inner_1RB_Right	26.17	29.11	30	pass
N78-3700-3800	30	15	DFT-QPSK	M	Outer_Full	25.07	28.01	30	pass
N78-3700-3800	30	15	DFT-16QAM	M	Inner_1RB_Left	25.11	28.05	30	pass
N78-3700-3800	30	15	DFT-16QAM	M	Inner_1RB_Right	25.11	28.05	30	pass
N78-3700-3800	30	15	DFT-16QAM	M	Outer_Full	24.02	26.96	30	pass
N78-3700-3800	30	15	DFT-64QAM	M	Inner_1RB_Left	23.59	26.53	30	pass
N78-3700-3800	30	15	DFT-64QAM	M	Inner_1RB_Right	23.41	26.35	30	pass
N78-3700-3800	30	15	DFT-64QAM	M	Outer_Full	23.55	26.49	30	pass
N78-3700-3800	30	15	DFT-256QAM	M	Inner_1RB_Left	21.63	24.57	30	pass
N78-3700-3800	30	15	DFT-256QAM	M	Inner_1RB_Right	21.72	24.66	30	pass
N78-3700-3800	30	15	DFT-256QAM	M	Outer_Full	21.59	24.53	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	H	Inner_1RB_Left	26.22	29.16	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	H	Inner_1RB_Right	26.47	29.41	30	pass
N78-3700-3800	30	15	DFT-PI2BPSK	H	Outer_Full	25.84	28.78	30	pass
N78-3700-3800	30	15	DFT-QPSK	H	Inner_1RB_Left	26.19	29.13	30	pass
N78-3700-3800	30	15	DFT-QPSK	H	Inner_1RB_Right	26.45	29.39	30	pass
N78-3700-3800	30	15	DFT-QPSK	H	Outer_Full	25.29	28.23	30	pass
N78-3700-3800	30	15	DFT-16QAM	H	Inner_1RB_Left	25.22	28.16	30	pass
N78-3700-3800	30	15	DFT-16QAM	H	Inner_1RB_Right	25.4	28.34	30	pass
N78-3700-3800	30	15	DFT-16QAM	H	Outer_Full	24.35	27.29	30	pass
N78-3700-3800	30	15	DFT-64QAM	H	Inner_1RB_Left	23.75	26.69	30	pass
N78-3700-3800	30	15	DFT-64QAM	H	Inner_1RB_Right	23.86	26.8	30	pass
N78-3700-3800	30	15	DFT-64QAM	H	Outer_Full	23.83	26.77	30	pass
N78-3700-3800	30	15	DFT-256QAM	H	Inner_1RB_Left	21.78	24.72	30	pass
N78-3700-3800	30	15	DFT-256QAM	H	Inner_1RB_Right	22.05	24.99	30	pass
N78-3700-3800	30	15	DFT-256QAM	H	Outer_Full	21.84	24.78	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	L	Inner_1RB_Left	26.14	29.08	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	L	Inner_1RB_Right	26.21	29.15	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	L	Outer_Full	25.66	28.6	30	pass
N78-3700-3800	30	20	DFT-QPSK	L	Inner_1RB_Left	26.14	29.08	30	pass
N78-3700-3800	30	20	DFT-QPSK	L	Inner_1RB_Right	26.13	29.07	30	pass
N78-3700-3800	30	20	DFT-QPSK	L	Outer_Full	25.19	28.13	30	pass
N78-3700-3800	30	20	DFT-16QAM	L	Inner_1RB_Left	25.05	27.99	30	pass



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N78-3700-3800	30	20	DFT-16QAM	L	Inner_1RB_Right	25.19	28.13	30	pass
N78-3700-3800	30	20	DFT-16QAM	L	Outer_Full	24.17	27.11	30	pass
N78-3700-3800	30	20	DFT-64QAM	L	Inner_1RB_Left	23.57	26.51	30	pass
N78-3700-3800	30	20	DFT-64QAM	L	Inner_1RB_Right	23.64	26.58	30	pass
N78-3700-3800	30	20	DFT-64QAM	L	Outer_Full	23.68	26.62	30	pass
N78-3700-3800	30	20	DFT-256QAM	L	Inner_1RB_Left	21.6	24.54	30	pass
N78-3700-3800	30	20	DFT-256QAM	L	Inner_1RB_Right	21.75	24.69	30	pass
N78-3700-3800	30	20	DFT-256QAM	L	Outer_Full	21.69	24.63	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	M	Inner_1RB_Left	26.2	29.14	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	M	Inner_1RB_Right	26.14	29.08	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	M	Outer_Full	25.58	28.52	30	pass
N78-3700-3800	30	20	DFT-QPSK	M	Inner_1RB_Left	26.14	29.08	30	pass
N78-3700-3800	30	20	DFT-QPSK	M	Inner_1RB_Right	26.18	29.12	30	pass
N78-3700-3800	30	20	DFT-QPSK	M	Outer_Full	25.07	28.01	30	pass
N78-3700-3800	30	20	DFT-16QAM	M	Inner_1RB_Left	25.12	28.06	30	pass
N78-3700-3800	30	20	DFT-16QAM	M	Inner_1RB_Right	25.11	28.05	30	pass
N78-3700-3800	30	20	DFT-16QAM	M	Outer_Full	24.07	27.01	30	pass
N78-3700-3800	30	20	DFT-64QAM	M	Inner_1RB_Left	23.61	26.55	30	pass
N78-3700-3800	30	20	DFT-64QAM	M	Inner_1RB_Right	23.49	26.43	30	pass
N78-3700-3800	30	20	DFT-64QAM	M	Outer_Full	23.55	26.49	30	pass
N78-3700-3800	30	20	DFT-256QAM	M	Inner_1RB_Left	21.88	24.82	30	pass
N78-3700-3800	30	20	DFT-256QAM	M	Inner_1RB_Right	21.64	24.58	30	pass
N78-3700-3800	30	20	DFT-256QAM	M	Outer_Full	21.59	24.53	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	H	Inner_1RB_Left	26.23	29.17	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	H	Inner_1RB_Right	26.48	29.42	30	pass
N78-3700-3800	30	20	DFT-PI2BPSK	H	Outer_Full	25.84	28.78	30	pass
N78-3700-3800	30	20	DFT-QPSK	H	Inner_1RB_Left	26.2	29.14	30	pass
N78-3700-3800	30	20	DFT-QPSK	H	Inner_1RB_Right	26.48	29.42	30	pass
N78-3700-3800	30	20	DFT-QPSK	H	Outer_Full	25.32	28.26	30	pass
N78-3700-3800	30	20	DFT-16QAM	H	Inner_1RB_Left	25.25	28.19	30	pass
N78-3700-3800	30	20	DFT-16QAM	H	Inner_1RB_Right	25.41	28.35	30	pass
N78-3700-3800	30	20	DFT-16QAM	H	Outer_Full	24.28	27.22	30	pass
N78-3700-3800	30	20	DFT-64QAM	H	Inner_1RB_Left	23.66	26.6	30	pass
N78-3700-3800	30	20	DFT-64QAM	H	Inner_1RB_Right	23.94	26.88	30	pass



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N78-3700-3800	30	20	DFT-64QAM	H	Outer_Full	23.81	26.75	30	pass
N78-3700-3800	30	20	DFT-256QAM	H	Inner_1RB_Left	21.78	24.72	30	pass
N78-3700-3800	30	20	DFT-256QAM	H	Inner_1RB_Right	21.84	24.78	30	pass
N78-3700-3800	30	20	DFT-256QAM	H	Outer_Full	21.83	24.77	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	L	Inner_1RB_Left	26.22	29.16	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	L	Inner_1RB_Right	26.26	29.2	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	L	Outer_Full	25.7	28.64	30	pass
N78-3700-3800	30	30	DFT-QPSK	L	Inner_1RB_Left	26.23	29.17	30	pass
N78-3700-3800	30	30	DFT-QPSK	L	Inner_1RB_Right	26.28	29.22	30	pass
N78-3700-3800	30	30	DFT-QPSK	L	Outer_Full	25.68	28.62	30	pass
N78-3700-3800	30	30	DFT-16QAM	L	Inner_1RB_Left	25.14	28.08	30	pass
N78-3700-3800	30	30	DFT-16QAM	L	Inner_1RB_Right	25.28	28.22	30	pass
N78-3700-3800	30	30	DFT-16QAM	L	Outer_Full	24.16	27.1	30	pass
N78-3700-3800	30	30	DFT-64QAM	L	Inner_1RB_Left	23.67	26.61	30	pass
N78-3700-3800	30	30	DFT-64QAM	L	Inner_1RB_Right	23.66	26.6	30	pass
N78-3700-3800	30	30	DFT-64QAM	L	Outer_Full	23.65	26.59	30	pass
N78-3700-3800	30	30	DFT-256QAM	L	Inner_1RB_Left	21.64	24.58	30	pass
N78-3700-3800	30	30	DFT-256QAM	L	Inner_1RB_Right	21.72	24.66	30	pass
N78-3700-3800	30	30	DFT-256QAM	L	Outer_Full	21.68	24.62	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	M	Inner_1RB_Left	26.35	29.29	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	M	Inner_1RB_Right	26.26	29.2	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	M	Outer_Full	25.72	28.66	30	pass
N78-3700-3800	30	30	DFT-QPSK	M	Inner_1RB_Left	26.37	29.31	30	pass
N78-3700-3800	30	30	DFT-QPSK	M	Inner_1RB_Right	26.27	29.21	30	pass
N78-3700-3800	30	30	DFT-QPSK	M	Outer_Full	25.19	28.13	30	pass
N78-3700-3800	30	30	DFT-16QAM	M	Inner_1RB_Left	25.25	28.19	30	pass
N78-3700-3800	30	30	DFT-16QAM	M	Inner_1RB_Right	25.23	28.17	30	pass
N78-3700-3800	30	30	DFT-16QAM	M	Outer_Full	24.16	27.1	30	pass
N78-3700-3800	30	30	DFT-64QAM	M	Inner_1RB_Left	23.84	26.78	30	pass
N78-3700-3800	30	30	DFT-64QAM	M	Inner_1RB_Right	23.72	26.66	30	pass
N78-3700-3800	30	30	DFT-64QAM	M	Outer_Full	23.7	26.64	30	pass
N78-3700-3800	30	30	DFT-256QAM	M	Inner_1RB_Left	21.81	24.75	30	pass
N78-3700-3800	30	30	DFT-256QAM	M	Inner_1RB_Right	21.8	24.74	30	pass
N78-3700-3800	30	30	DFT-256QAM	M	Outer_Full	21.74	24.68	30	pass



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N78-3700-3800	30	30	DFT-PI2BPSK	H	Inner_1RB_Left	26.37	29.31	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	H	Inner_1RB_Right	26.64	29.58	30	pass
N78-3700-3800	30	30	DFT-PI2BPSK	H	Outer_Full	25.96	28.9	30	pass
N78-3700-3800	30	30	DFT-QPSK	H	Inner_1RB_Left	26.42	29.36	30	pass
N78-3700-3800	30	30	DFT-QPSK	H	Inner_1RB_Right	26.62	29.56	30	pass
N78-3700-3800	30	30	DFT-QPSK	H	Outer_Full	25.42	28.36	30	pass
N78-3700-3800	30	30	DFT-16QAM	H	Inner_1RB_Left	25.32	28.26	30	pass
N78-3700-3800	30	30	DFT-16QAM	H	Inner_1RB_Right	25.66	28.6	30	pass
N78-3700-3800	30	30	DFT-16QAM	H	Outer_Full	24.42	27.36	30	pass
N78-3700-3800	30	30	DFT-64QAM	H	Inner_1RB_Left	23.87	26.81	30	pass
N78-3700-3800	30	30	DFT-64QAM	H	Inner_1RB_Right	23.98	26.92	30	pass
N78-3700-3800	30	30	DFT-64QAM	H	Outer_Full	23.84	26.78	30	pass
N78-3700-3800	30	30	DFT-256QAM	H	Inner_1RB_Left	21.87	24.81	30	pass
N78-3700-3800	30	30	DFT-256QAM	H	Inner_1RB_Right	22.21	25.15	30	pass
N78-3700-3800	30	30	DFT-256QAM	H	Outer_Full	21.93	24.87	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	L	Inner_1RB_Left	26.3	29.24	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	L	Inner_1RB_Right	26.38	29.32	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	L	Outer_Full	25.69	28.63	30	pass
N78-3700-3800	30	40	DFT-QPSK	L	Inner_1RB_Left	26.29	29.23	30	pass
N78-3700-3800	30	40	DFT-QPSK	L	Inner_1RB_Right	26.39	29.33	30	pass
N78-3700-3800	30	40	DFT-QPSK	L	Outer_Full	25.23	28.17	30	pass
N78-3700-3800	30	40	DFT-16QAM	L	Inner_1RB_Left	25.23	28.17	30	pass
N78-3700-3800	30	40	DFT-16QAM	L	Inner_1RB_Right	25.27	28.21	30	pass
N78-3700-3800	30	40	DFT-16QAM	L	Outer_Full	24.2	27.14	30	pass
N78-3700-3800	30	40	DFT-64QAM	L	Inner_1RB_Left	23.84	26.78	30	pass
N78-3700-3800	30	40	DFT-64QAM	L	Inner_1RB_Right	23.74	26.68	30	pass
N78-3700-3800	30	40	DFT-64QAM	L	Outer_Full	23.77	26.71	30	pass
N78-3700-3800	30	40	DFT-256QAM	L	Inner_1RB_Left	21.78	24.72	30	pass
N78-3700-3800	30	40	DFT-256QAM	L	Inner_1RB_Right	21.72	24.66	30	pass
N78-3700-3800	30	40	DFT-256QAM	L	Outer_Full	21.72	24.66	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	M	Inner_1RB_Left	26.52	29.46	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	M	Inner_1RB_Right	26.34	29.28	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	M	Outer_Full	25.73	28.67	30	pass
N78-3700-3800	30	40	DFT-QPSK	M	Inner_1RB_Left	26.43	29.37	30	pass



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N78-3700-3800	30	40	DFT-QPSK	M	Inner_1RB_Right	26.34	29.28	30	pass
N78-3700-3800	30	40	DFT-QPSK	M	Outer_Full	25.23	28.17	30	pass
N78-3700-3800	30	40	DFT-16QAM	M	Inner_1RB_Left	25.39	28.33	30	pass
N78-3700-3800	30	40	DFT-16QAM	M	Inner_1RB_Right	25.25	28.19	30	pass
N78-3700-3800	30	40	DFT-16QAM	M	Outer_Full	24.23	27.17	30	pass
N78-3700-3800	30	40	DFT-64QAM	M	Inner_1RB_Left	23.84	26.78	30	pass
N78-3700-3800	30	40	DFT-64QAM	M	Inner_1RB_Right	23.69	26.63	30	pass
N78-3700-3800	30	40	DFT-64QAM	M	Outer_Full	23.74	26.68	30	pass
N78-3700-3800	30	40	DFT-256QAM	M	Inner_1RB_Left	21.95	24.89	30	pass
N78-3700-3800	30	40	DFT-256QAM	M	Inner_1RB_Right	21.74	24.68	30	pass
N78-3700-3800	30	40	DFT-256QAM	M	Outer_Full	21.75	24.69	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	H	Inner_1RB_Left	26.27	29.21	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	H	Inner_1RB_Right	26.43	29.37	30	pass
N78-3700-3800	30	40	DFT-PI2BPSK	H	Outer_Full	26.63	29.57	30	pass
N78-3700-3800	30	40	DFT-QPSK	H	Inner_1RB_Left	26.26	29.2	30	pass
N78-3700-3800	30	40	DFT-QPSK	H	Inner_1RB_Right	26.46	29.4	30	pass
N78-3700-3800	30	40	DFT-QPSK	H	Outer_Full	25.15	28.09	30	pass
N78-3700-3800	30	40	DFT-16QAM	H	Inner_1RB_Left	25.29	28.23	30	pass
N78-3700-3800	30	40	DFT-16QAM	H	Inner_1RB_Right	25.4	28.34	30	pass
N78-3700-3800	30	40	DFT-16QAM	H	Outer_Full	24.15	27.09	30	pass
N78-3700-3800	30	40	DFT-64QAM	H	Inner_1RB_Left	23.64	26.58	30	pass
N78-3700-3800	30	40	DFT-64QAM	H	Inner_1RB_Right	23.98	26.92	30	pass
N78-3700-3800	30	40	DFT-64QAM	H	Outer_Full	23.61	26.55	30	pass
N78-3700-3800	30	40	DFT-256QAM	H	Inner_1RB_Left	21.8	24.74	30	pass
N78-3700-3800	30	40	DFT-256QAM	H	Inner_1RB_Right	21.94	24.88	30	pass
N78-3700-3800	30	40	DFT-256QAM	H	Outer_Full	21.64	24.58	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	L	Inner_1RB_Left	26.15	29.09	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	L	Inner_1RB_Right	26.01	28.95	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	L	Outer_Full	25.69	28.63	30	pass
N78-3700-3800	30	50	DFT-QPSK	L	Inner_1RB_Left	26.11	29.05	30	pass
N78-3700-3800	30	50	DFT-QPSK	L	Inner_1RB_Right	25.92	28.86	30	pass
N78-3700-3800	30	50	DFT-QPSK	L	Outer_Full	25.19	28.13	30	pass
N78-3700-3800	30	50	DFT-16QAM	L	Inner_1RB_Left	25.04	27.98	30	pass
N78-3700-3800	30	50	DFT-16QAM	L	Inner_1RB_Right	24.89	27.83	30	pass



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N78-3700-3800	30	50	DFT-16QAM	L	Outer_Full	24.19	27.13	30	pass
N78-3700-3800	30	50	DFT-64QAM	L	Inner_1RB_Left	23.61	26.55	30	pass
N78-3700-3800	30	50	DFT-64QAM	L	Inner_1RB_Right	23.29	26.23	30	pass
N78-3700-3800	30	50	DFT-64QAM	L	Outer_Full	23.69	26.63	30	pass
N78-3700-3800	30	50	DFT-256QAM	L	Inner_1RB_Left	21.7	24.64	30	pass
N78-3700-3800	30	50	DFT-256QAM	L	Inner_1RB_Right	21.45	24.39	30	pass
N78-3700-3800	30	50	DFT-256QAM	L	Outer_Full	21.73	24.67	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	M	Inner_1RB_Left	26.16	29.1	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	M	Inner_1RB_Right	25.97	28.91	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	M	Outer_Full	25.52	28.46	30	pass
N78-3700-3800	30	50	DFT-QPSK	M	Inner_1RB_Left	26.14	29.08	30	pass
N78-3700-3800	30	50	DFT-QPSK	M	Inner_1RB_Right	26.01	28.95	30	pass
N78-3700-3800	30	50	DFT-QPSK	M	Outer_Full	24.99	27.93	30	pass
N78-3700-3800	30	50	DFT-16QAM	M	Inner_1RB_Left	25.07	28.01	30	pass
N78-3700-3800	30	50	DFT-16QAM	M	Inner_1RB_Right	24.91	27.85	30	pass
N78-3700-3800	30	50	DFT-16QAM	M	Outer_Full	23.94	26.88	30	pass
N78-3700-3800	30	50	DFT-64QAM	M	Inner_1RB_Left	23.59	26.53	30	pass
N78-3700-3800	30	50	DFT-64QAM	M	Inner_1RB_Right	23.42	26.36	30	pass
N78-3700-3800	30	50	DFT-64QAM	M	Outer_Full	23.46	26.4	30	pass
N78-3700-3800	30	50	DFT-256QAM	M	Inner_1RB_Left	21.66	24.6	30	pass
N78-3700-3800	30	50	DFT-256QAM	M	Inner_1RB_Right	21.34	24.28	30	pass
N78-3700-3800	30	50	DFT-256QAM	M	Outer_Full	21.49	24.43	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	H	Inner_1RB_Left	25.86	28.8	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	H	Inner_1RB_Right	26.11	29.05	30	pass
N78-3700-3800	30	50	DFT-PI2BPSK	H	Outer_Full	25.45	28.39	30	pass
N78-3700-3800	30	50	DFT-QPSK	H	Inner_1RB_Left	25.79	28.73	30	pass
N78-3700-3800	30	50	DFT-QPSK	H	Inner_1RB_Right	26.06	29	30	pass
N78-3700-3800	30	50	DFT-QPSK	H	Outer_Full	24.94	27.88	30	pass
N78-3700-3800	30	50	DFT-16QAM	H	Inner_1RB_Left	24.83	27.77	30	pass
N78-3700-3800	30	50	DFT-16QAM	H	Inner_1RB_Right	24.98	27.92	30	pass
N78-3700-3800	30	50	DFT-16QAM	H	Outer_Full	23.91	26.85	30	pass
N78-3700-3800	30	50	DFT-64QAM	H	Inner_1RB_Left	23.32	26.26	30	pass
N78-3700-3800	30	50	DFT-64QAM	H	Inner_1RB_Right	23.47	26.41	30	pass
N78-3700-3800	30	50	DFT-64QAM	H	Outer_Full	23.43	26.37	30	pass



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N78-3700-3800	30	50	DFT-256QAM	H	Inner_1RB_Left	21.39	24.33	30	pass
N78-3700-3800	30	50	DFT-256QAM	H	Inner_1RB_Right	21.67	24.61	30	pass
N78-3700-3800	30	50	DFT-256QAM	H	Outer_Full	21.36	24.3	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	L	Inner_1RB_Left	26.04	28.98	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	L	Inner_1RB_Right	26.01	28.95	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	L	Outer_Full	25.66	28.6	30	pass
N78-3700-3800	30	60	DFT-QPSK	L	Inner_1RB_Left	26.03	28.97	30	pass
N78-3700-3800	30	60	DFT-QPSK	L	Inner_1RB_Right	26	28.94	30	pass
N78-3700-3800	30	60	DFT-QPSK	L	Outer_Full	25.15	28.09	30	pass
N78-3700-3800	30	60	DFT-16QAM	L	Inner_1RB_Left	25.05	27.99	30	pass
N78-3700-3800	30	60	DFT-16QAM	L	Inner_1RB_Right	24.91	27.85	30	pass
N78-3700-3800	30	60	DFT-16QAM	L	Outer_Full	24.15	27.09	30	pass
N78-3700-3800	30	60	DFT-64QAM	L	Inner_1RB_Left	23.55	26.49	30	pass
N78-3700-3800	30	60	DFT-64QAM	L	Inner_1RB_Right	23.57	26.51	30	pass
N78-3700-3800	30	60	DFT-64QAM	L	Outer_Full	23.65	26.59	30	pass
N78-3700-3800	30	60	DFT-256QAM	L	Inner_1RB_Left	21.43	24.37	30	pass
N78-3700-3800	30	60	DFT-256QAM	L	Inner_1RB_Right	21.46	24.4	30	pass
N78-3700-3800	30	60	DFT-256QAM	L	Outer_Full	21.68	24.62	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	M	Inner_1RB_Left	26.08	29.02	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	M	Inner_1RB_Right	26.01	28.95	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	M	Outer_Full	25.57	28.51	30	pass
N78-3700-3800	30	60	DFT-QPSK	M	Inner_1RB_Left	26.09	29.03	30	pass
N78-3700-3800	30	60	DFT-QPSK	M	Inner_1RB_Right	25.98	28.92	30	pass
N78-3700-3800	30	60	DFT-QPSK	M	Outer_Full	24.97	27.91	30	pass
N78-3700-3800	30	60	DFT-16QAM	M	Inner_1RB_Left	25.03	27.97	30	pass
N78-3700-3800	30	60	DFT-16QAM	M	Inner_1RB_Right	24.94	27.88	30	pass
N78-3700-3800	30	60	DFT-16QAM	M	Outer_Full	24.01	26.95	30	pass
N78-3700-3800	30	60	DFT-64QAM	M	Inner_1RB_Left	23.58	26.52	30	pass
N78-3700-3800	30	60	DFT-64QAM	M	Inner_1RB_Right	23.43	26.37	30	pass
N78-3700-3800	30	60	DFT-64QAM	M	Outer_Full	23.47	26.41	30	pass
N78-3700-3800	30	60	DFT-256QAM	M	Inner_1RB_Left	21.53	24.47	30	pass
N78-3700-3800	30	60	DFT-256QAM	M	Inner_1RB_Right	21.51	24.45	30	pass
N78-3700-3800	30	60	DFT-256QAM	M	Outer_Full	21.43	24.37	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	H	Inner_1RB_Left	25.91	28.85	30	pass



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N78-3700-3800	30	60	DFT-PI2BPSK	H	Inner_1RB_Right	26.19	29.13	30	pass
N78-3700-3800	30	60	DFT-PI2BPSK	H	Outer_Full	25.48	28.42	30	pass
N78-3700-3800	30	60	DFT-QPSK	H	Inner_1RB_Left	25.99	28.93	30	pass
N78-3700-3800	30	60	DFT-QPSK	H	Inner_1RB_Right	26.19	29.13	30	pass
N78-3700-3800	30	60	DFT-QPSK	H	Outer_Full	24.96	27.9	30	pass
N78-3700-3800	30	60	DFT-16QAM	H	Inner_1RB_Left	24.8	27.74	30	pass
N78-3700-3800	30	60	DFT-16QAM	H	Inner_1RB_Right	25.19	28.13	30	pass
N78-3700-3800	30	60	DFT-16QAM	H	Outer_Full	23.89	26.83	30	pass
N78-3700-3800	30	60	DFT-64QAM	H	Inner_1RB_Left	23.3	26.24	30	pass
N78-3700-3800	30	60	DFT-64QAM	H	Inner_1RB_Right	23.76	26.7	30	pass
N78-3700-3800	30	60	DFT-64QAM	H	Outer_Full	23.43	26.37	30	pass
N78-3700-3800	30	60	DFT-256QAM	H	Inner_1RB_Left	21.42	24.36	30	pass
N78-3700-3800	30	60	DFT-256QAM	H	Inner_1RB_Right	21.68	24.62	30	pass
N78-3700-3800	30	60	DFT-256QAM	H	Outer_Full	21.46	24.4	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	L	Inner_1RB_Left	25.93	28.87	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	L	Inner_1RB_Right	25.94	28.88	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	L	Outer_Full	25.59	28.53	30	pass
N78-3700-3800	30	70	DFT-QPSK	L	Inner_1RB_Left	25.92	28.86	30	pass
N78-3700-3800	30	70	DFT-QPSK	L	Inner_1RB_Right	25.95	28.89	30	pass
N78-3700-3800	30	70	DFT-QPSK	L	Outer_Full	25.07	28.01	30	pass
N78-3700-3800	30	70	DFT-16QAM	L	Inner_1RB_Left	24.88	27.82	30	pass
N78-3700-3800	30	70	DFT-16QAM	L	Inner_1RB_Right	24.87	27.81	30	pass
N78-3700-3800	30	70	DFT-16QAM	L	Outer_Full	24.08	27.02	30	pass
N78-3700-3800	30	70	DFT-64QAM	L	Inner_1RB_Left	23.4	26.34	30	pass
N78-3700-3800	30	70	DFT-64QAM	L	Inner_1RB_Right	23.28	26.22	30	pass
N78-3700-3800	30	70	DFT-64QAM	L	Outer_Full	23.54	26.48	30	pass
N78-3700-3800	30	70	DFT-256QAM	L	Inner_1RB_Left	21.48	24.42	30	pass
N78-3700-3800	30	70	DFT-256QAM	L	Inner_1RB_Right	21.49	24.43	30	pass
N78-3700-3800	30	70	DFT-256QAM	L	Outer_Full	21.51	24.45	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	M	Inner_1RB_Left	26.02	28.96	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	M	Inner_1RB_Right	25.85	28.79	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	M	Outer_Full	25.48	28.42	30	pass
N78-3700-3800	30	70	DFT-QPSK	M	Inner_1RB_Left	26.05	28.99	30	pass
N78-3700-3800	30	70	DFT-QPSK	M	Inner_1RB_Right	25.87	28.81	30	pass



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N78-3700-3800	30	70	DFT-QPSK	M	Outer_Full	24.97	27.91	30	pass
N78-3700-3800	30	70	DFT-16QAM	M	Inner_1RB_Left	24.96	27.9	30	pass
N78-3700-3800	30	70	DFT-16QAM	M	Inner_1RB_Right	24.86	27.8	30	pass
N78-3700-3800	30	70	DFT-16QAM	M	Outer_Full	23.94	26.88	30	pass
N78-3700-3800	30	70	DFT-64QAM	M	Inner_1RB_Left	23.44	26.38	30	pass
N78-3700-3800	30	70	DFT-64QAM	M	Inner_1RB_Right	23.24	26.18	30	pass
N78-3700-3800	30	70	DFT-64QAM	M	Outer_Full	23.47	26.41	30	pass
N78-3700-3800	30	70	DFT-256QAM	M	Inner_1RB_Left	21.59	24.53	30	pass
N78-3700-3800	30	70	DFT-256QAM	M	Inner_1RB_Right	21.35	24.29	30	pass
N78-3700-3800	30	70	DFT-256QAM	M	Outer_Full	21.45	24.39	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	H	Inner_1RB_Left	26.06	29	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	H	Inner_1RB_Right	26.16	29.1	30	pass
N78-3700-3800	30	70	DFT-PI2BPSK	H	Outer_Full	25.52	28.46	30	pass
N78-3700-3800	30	70	DFT-QPSK	H	Inner_1RB_Left	26.03	28.97	30	pass
N78-3700-3800	30	70	DFT-QPSK	H	Inner_1RB_Right	26.2	29.14	30	pass
N78-3700-3800	30	70	DFT-QPSK	H	Outer_Full	25.01	27.95	30	pass
N78-3700-3800	30	70	DFT-16QAM	H	Inner_1RB_Left	24.97	27.91	30	pass
N78-3700-3800	30	70	DFT-16QAM	H	Inner_1RB_Right	25.14	28.08	30	pass
N78-3700-3800	30	70	DFT-16QAM	H	Outer_Full	23.97	26.91	30	pass
N78-3700-3800	30	70	DFT-64QAM	H	Inner_1RB_Left	23.48	26.42	30	pass
N78-3700-3800	30	70	DFT-64QAM	H	Inner_1RB_Right	23.58	26.52	30	pass
N78-3700-3800	30	70	DFT-64QAM	H	Outer_Full	23.45	26.39	30	pass
N78-3700-3800	30	70	DFT-256QAM	H	Inner_1RB_Left	21.45	24.39	30	pass
N78-3700-3800	30	70	DFT-256QAM	H	Inner_1RB_Right	21.59	24.53	30	pass
N78-3700-3800	30	70	DFT-256QAM	H	Outer_Full	21.53	24.47	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	L	Inner_1RB_Left	25.86	28.8	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	L	Inner_1RB_Right	25.9	28.84	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	L	Outer_Full	25.52	28.46	30	pass
N78-3700-3800	30	80	DFT-QPSK	L	Inner_1RB_Left	25.88	28.82	30	pass
N78-3700-3800	30	80	DFT-QPSK	L	Inner_1RB_Right	25.94	28.88	30	pass
N78-3700-3800	30	80	DFT-QPSK	L	Outer_Full	24.99	27.93	30	pass
N78-3700-3800	30	80	DFT-16QAM	L	Inner_1RB_Left	24.77	27.71	30	pass
N78-3700-3800	30	80	DFT-16QAM	L	Inner_1RB_Right	25.01	27.95	30	pass
N78-3700-3800	30	80	DFT-16QAM	L	Outer_Full	24.01	26.95	30	pass



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N78-3700-3800	30	80	DFT-64QAM	L	Inner_1RB_Left	23.34	26.28	30	pass
N78-3700-3800	30	80	DFT-64QAM	L	Inner_1RB_Right	23.27	26.21	30	pass
N78-3700-3800	30	80	DFT-64QAM	L	Outer_Full	23.52	26.46	30	pass
N78-3700-3800	30	80	DFT-256QAM	L	Inner_1RB_Left	21.37	24.31	30	pass
N78-3700-3800	30	80	DFT-256QAM	L	Inner_1RB_Right	21.35	24.29	30	pass
N78-3700-3800	30	80	DFT-256QAM	L	Outer_Full	21.46	24.4	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	M	Inner_1RB_Left	25.97	28.91	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	M	Inner_1RB_Right	25.94	28.88	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	M	Outer_Full	25.43	28.37	30	pass
N78-3700-3800	30	80	DFT-QPSK	M	Inner_1RB_Left	26	28.94	30	pass
N78-3700-3800	30	80	DFT-QPSK	M	Inner_1RB_Right	25.99	28.93	30	pass
N78-3700-3800	30	80	DFT-QPSK	M	Outer_Full	24.93	27.87	30	pass
N78-3700-3800	30	80	DFT-16QAM	M	Inner_1RB_Left	24.83	27.77	30	pass
N78-3700-3800	30	80	DFT-16QAM	M	Inner_1RB_Right	24.8	27.74	30	pass
N78-3700-3800	30	80	DFT-16QAM	M	Outer_Full	23.93	26.87	30	pass
N78-3700-3800	30	80	DFT-64QAM	M	Inner_1RB_Left	23.44	26.38	30	pass
N78-3700-3800	30	80	DFT-64QAM	M	Inner_1RB_Right	23.4	26.34	30	pass
N78-3700-3800	30	80	DFT-64QAM	M	Outer_Full	23.43	26.37	30	pass
N78-3700-3800	30	80	DFT-256QAM	M	Inner_1RB_Left	21.28	24.22	30	pass
N78-3700-3800	30	80	DFT-256QAM	M	Inner_1RB_Right	21.41	24.35	30	pass
N78-3700-3800	30	80	DFT-256QAM	M	Outer_Full	21.47	24.41	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	H	Inner_1RB_Left	25.95	28.89	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	H	Inner_1RB_Right	26.15	29.09	30	pass
N78-3700-3800	30	80	DFT-PI2BPSK	H	Outer_Full	25.53	28.47	30	pass
N78-3700-3800	30	80	DFT-QPSK	H	Inner_1RB_Left	26.09	29.03	30	pass
N78-3700-3800	30	80	DFT-QPSK	H	Inner_1RB_Right	26.14	29.08	30	pass
N78-3700-3800	30	80	DFT-QPSK	H	Outer_Full	25	27.94	30	pass
N78-3700-3800	30	80	DFT-16QAM	H	Inner_1RB_Left	24.9	27.84	30	pass
N78-3700-3800	30	80	DFT-16QAM	H	Inner_1RB_Right	25.09	28.03	30	pass
N78-3700-3800	30	80	DFT-16QAM	H	Outer_Full	24.02	26.96	30	pass
N78-3700-3800	30	80	DFT-64QAM	H	Inner_1RB_Left	23.39	26.33	30	pass
N78-3700-3800	30	80	DFT-64QAM	H	Inner_1RB_Right	23.58	26.52	30	pass
N78-3700-3800	30	80	DFT-64QAM	H	Outer_Full	23.51	26.45	30	pass
N78-3700-3800	30	80	DFT-256QAM	H	Inner_1RB_Left	21.58	24.52	30	pass



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N78-3700-3800	30	80	DFT-256QAM	H	Inner_1RB_Right	21.61	24.55	30	pass
N78-3700-3800	30	80	DFT-256QAM	H	Outer_Full	21.52	24.46	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	L	Inner_1RB_Left	25.89	28.83	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	L	Inner_1RB_Right	26.09	29.03	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	L	Outer_Full	25.5	28.44	30	pass
N78-3700-3800	30	90	DFT-QPSK	L	Inner_1RB_Left	25.91	28.85	30	pass
N78-3700-3800	30	90	DFT-QPSK	L	Inner_1RB_Right	26.07	29.01	30	pass
N78-3700-3800	30	90	DFT-QPSK	L	Outer_Full	24.99	27.93	30	pass
N78-3700-3800	30	90	DFT-16QAM	L	Inner_1RB_Left	24.89	27.83	30	pass
N78-3700-3800	30	90	DFT-16QAM	L	Inner_1RB_Right	25	27.94	30	pass
N78-3700-3800	30	90	DFT-16QAM	L	Outer_Full	23.98	26.92	30	pass
N78-3700-3800	30	90	DFT-64QAM	L	Inner_1RB_Left	23.27	26.21	30	pass
N78-3700-3800	30	90	DFT-64QAM	L	Inner_1RB_Right	23.48	26.42	30	pass
N78-3700-3800	30	90	DFT-64QAM	L	Outer_Full	23.51	26.45	30	pass
N78-3700-3800	30	90	DFT-256QAM	L	Inner_1RB_Left	21.31	24.25	30	pass
N78-3700-3800	30	90	DFT-256QAM	L	Inner_1RB_Right	21.52	24.46	30	pass
N78-3700-3800	30	90	DFT-256QAM	L	Outer_Full	21.5	24.44	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	M	Inner_1RB_Left	25.91	28.85	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	M	Inner_1RB_Right	26.13	29.07	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	M	Outer_Full	25.48	28.42	30	pass
N78-3700-3800	30	90	DFT-QPSK	M	Inner_1RB_Left	25.9	28.84	30	pass
N78-3700-3800	30	90	DFT-QPSK	M	Inner_1RB_Right	26.12	29.06	30	pass
N78-3700-3800	30	90	DFT-QPSK	M	Outer_Full	24.97	27.91	30	pass
N78-3700-3800	30	90	DFT-16QAM	M	Inner_1RB_Left	24.88	27.82	30	pass
N78-3700-3800	30	90	DFT-16QAM	M	Inner_1RB_Right	25.17	28.11	30	pass
N78-3700-3800	30	90	DFT-16QAM	M	Outer_Full	23.99	26.93	30	pass
N78-3700-3800	30	90	DFT-64QAM	M	Inner_1RB_Left	23.32	26.26	30	pass
N78-3700-3800	30	90	DFT-64QAM	M	Inner_1RB_Right	23.52	26.46	30	pass
N78-3700-3800	30	90	DFT-64QAM	M	Outer_Full	23.48	26.42	30	pass
N78-3700-3800	30	90	DFT-256QAM	M	Inner_1RB_Left	21.24	24.18	30	pass
N78-3700-3800	30	90	DFT-256QAM	M	Inner_1RB_Right	21.66	24.6	30	pass
N78-3700-3800	30	90	DFT-256QAM	M	Outer_Full	21.45	24.39	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	H	Inner_1RB_Left	25.95	28.89	30	pass
N78-3700-3800	30	90	DFT-PI2BPSK	H	Inner_1RB_Right	26.18	29.12	30	pass



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N78-3700-3800	30	90	DFT-PI2BPSK	H	Outer_Full	25.42	28.36	30	pass
N78-3700-3800	30	90	DFT-QPSK	H	Inner_1RB_Left	25.94	28.88	30	pass
N78-3700-3800	30	90	DFT-QPSK	H	Inner_1RB_Right	26.18	29.12	30	pass
N78-3700-3800	30	90	DFT-QPSK	H	Outer_Full	24.89	27.83	30	pass
N78-3700-3800	30	90	DFT-16QAM	H	Inner_1RB_Left	24.79	27.73	30	pass
N78-3700-3800	30	90	DFT-16QAM	H	Inner_1RB_Right	25.18	28.12	30	pass
N78-3700-3800	30	90	DFT-16QAM	H	Outer_Full	23.92	26.86	30	pass
N78-3700-3800	30	90	DFT-64QAM	H	Inner_1RB_Left	23.29	26.23	30	pass
N78-3700-3800	30	90	DFT-64QAM	H	Inner_1RB_Right	23.67	26.61	30	pass
N78-3700-3800	30	90	DFT-64QAM	H	Outer_Full	23.4	26.34	30	pass
N78-3700-3800	30	90	DFT-256QAM	H	Inner_1RB_Left	21.46	24.4	30	pass
N78-3700-3800	30	90	DFT-256QAM	H	Inner_1RB_Right	21.71	24.65	30	pass
N78-3700-3800	30	90	DFT-256QAM	H	Outer_Full	21.36	24.3	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	L	Inner_1RB_Left	25.82	28.76	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	L	Inner_1RB_Right	26.25	29.19	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	L	Outer_Full	25.49	28.43	30	pass
N78-3700-3800	30	100	DFT-QPSK	L	Inner_1RB_Left	25.85	28.79	30	pass
N78-3700-3800	30	100	DFT-QPSK	L	Inner_1RB_Right	26.27	29.21	30	pass
N78-3700-3800	30	100	DFT-QPSK	L	Outer_Full	24.97	27.91	30	pass
N78-3700-3800	30	100	DFT-16QAM	L	Inner_1RB_Left	24.82	27.76	30	pass
N78-3700-3800	30	100	DFT-16QAM	L	Inner_1RB_Right	25.1	28.04	30	pass
N78-3700-3800	30	100	DFT-16QAM	L	Outer_Full	23.96	26.9	30	pass
N78-3700-3800	30	100	DFT-64QAM	L	Inner_1RB_Left	23.33	26.27	30	pass
N78-3700-3800	30	100	DFT-64QAM	L	Inner_1RB_Right	23.7	26.64	30	pass
N78-3700-3800	30	100	DFT-64QAM	L	Outer_Full	23.45	26.39	30	pass
N78-3700-3800	30	100	DFT-256QAM	L	Inner_1RB_Left	21.31	24.25	30	pass
N78-3700-3800	30	100	DFT-256QAM	L	Inner_1RB_Right	21.75	24.69	30	pass
N78-3700-3800	30	100	DFT-256QAM	L	Outer_Full	21.53	24.47	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	M	Inner_1RB_Left	25.85	28.79	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	M	Inner_1RB_Right	26.27	29.21	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	M	Outer_Full	25.49	28.43	30	pass
N78-3700-3800	30	100	DFT-QPSK	M	Inner_1RB_Left	25.84	28.78	30	pass
N78-3700-3800	30	100	DFT-QPSK	M	Inner_1RB_Right	26.28	29.22	30	pass
N78-3700-3800	30	100	DFT-QPSK	M	Outer_Full	24.95	27.89	30	pass



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N78-3700-3800	30	100	DFT-16QAM	M	Inner_1RB_Left	24.83	27.77	30	pass
N78-3700-3800	30	100	DFT-16QAM	M	Inner_1RB_Right	25.24	28.18	30	pass
N78-3700-3800	30	100	DFT-16QAM	M	Outer_Full	24.03	26.97	30	pass
N78-3700-3800	30	100	DFT-64QAM	M	Inner_1RB_Left	23.37	26.31	30	pass
N78-3700-3800	30	100	DFT-64QAM	M	Inner_1RB_Right	23.73	26.67	30	pass
N78-3700-3800	30	100	DFT-64QAM	M	Outer_Full	23.45	26.39	30	pass
N78-3700-3800	30	100	DFT-256QAM	M	Inner_1RB_Left	21.41	24.35	30	pass
N78-3700-3800	30	100	DFT-256QAM	M	Inner_1RB_Right	21.76	24.7	30	pass
N78-3700-3800	30	100	DFT-256QAM	M	Outer_Full	21.49	24.43	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	H	Inner_1RB_Left	25.83	28.77	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	H	Inner_1RB_Right	26.27	29.21	30	pass
N78-3700-3800	30	100	DFT-PI2BPSK	H	Outer_Full	25.49	28.43	30	pass
N78-3700-3800	30	100	DFT-QPSK	H	Inner_1RB_Left	25.85	28.79	30	pass
N78-3700-3800	30	100	DFT-QPSK	H	Inner_1RB_Right	26.28	29.22	30	pass
N78-3700-3800	30	100	DFT-QPSK	H	Outer_Full	24.95	27.89	30	pass
N78-3700-3800	30	100	DFT-16QAM	H	Inner_1RB_Left	24.82	27.76	30	pass
N78-3700-3800	30	100	DFT-16QAM	H	Inner_1RB_Right	25.17	28.11	30	pass
N78-3700-3800	30	100	DFT-16QAM	H	Outer_Full	23.98	26.92	30	pass
N78-3700-3800	30	100	DFT-64QAM	H	Inner_1RB_Left	23.28	26.22	30	pass
N78-3700-3800	30	100	DFT-64QAM	H	Inner_1RB_Right	23.81	26.75	30	pass
N78-3700-3800	30	100	DFT-64QAM	H	Outer_Full	23.48	26.42	30	pass
N78-3700-3800	30	100	DFT-256QAM	H	Inner_1RB_Left	21.35	24.29	30	pass
N78-3700-3800	30	100	DFT-256QAM	H	Inner_1RB_Right	21.74	24.68	30	pass
N78-3700-3800	30	100	DFT-256QAM	H	Outer_Full	21.42	24.36	30	pass



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Field Strength of Spurious Radiation

Test Band = 5G NR SA Band78_ TM1
Test Channel = Low 3700-3800

Final Data List										
NO.	Frequency [MHz]	Reading [dBμV]	Factor [dB]	AF[dB/m]	Level [dBm]	Limit [dBm]	Margin [dB]	Height [cm]	Angle [°]	Polarity
1	7310.1800	40.39	-43.27	36.54	-61.60	-13.00	48.60	236	330	Horizontal
2	8689.6	40.44	-41.29	37.90	-58.22	-13.00	45.22	256	356	Horizontal
3	9807.975	39.97	-39.07	38.23	-56.13	-13.00	43.13	142	316	Horizontal
4	10965.2700	35.25	-37.67	39.10	-58.58	-13.00	45.58	185	256	Horizontal
5	12876.75	35.58	-36.81	39.71	-56.79	-13.00	43.79	146	16	Horizontal
6	14620.3600	32.84	-35.23	39.91	-57.74	-13.00	44.74	103	73	Horizontal

Final Data List										
NO.	Frequency [MHz]	Reading [dBμV]	Factor [dB]	AF[dB/m]	Level [dBm]	Limit [dBm]	Margin [dB]	Height [cm]	Angle [°]	Polarity
1	7310.1800	40.73	-43.27	36.54	-61.26	-13.00	48.26	256	73	Vertical
2	8346.325	41.35	-41.64	37.26	-58.29	-13.00	45.29	148	28	Vertical
3	10965.2700	34.30	-37.67	39.10	-59.53	-13.00	46.53	159	134	Vertical
4	12578.325	35.82	-37.15	39.26	-57.33	-13.00	44.33	132	346	Vertical
5	14620.3600	32.92	-35.23	39.91	-57.66	-13.00	44.66	266	316	Vertical
6	16887.95	35.20	-34.11	38.44	-55.74	-13.00	42.74	251	0	Vertical



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Rev.: 01

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Test Band = 5G NR SA Band78_ TM1
Test Channel = Mid 3700-3800
Final Data List

NO.	Frequency [MHz]	Reading [dBμV]	Factor [dB]	AF[dB/m]	Level [dBm]	Limit [dBm]	Margin [dB]	Height [cm]	Angle [°]	Polarity
1	7410.1800	41.17	-42.83	36.48	-60.43	-13.00	47.43	245	227	Horizontal
2	8347.475	41.41	-41.63	37.26	-58.22	-13.00	45.22	125	17	Horizontal
3	9807.975	38.83	-39.07	38.23	-57.27	-13.00	44.27	136	331	Horizontal
4	11115.2700	34.55	-37.57	39.09	-59.19	-13.00	46.19	259	0	Horizontal
5	12811.775	35.22	-36.65	39.64	-57.05	-13.00	44.05	211	210	Horizontal
6	14820.3600	33.17	-35.03	39.61	-57.50	-13.00	44.50	199	302	Horizontal

Final Data List

NO.	Frequency [MHz]	Reading [dBμV]	Factor [dB]	AF[dB/m]	Level [dBm]	Limit [dBm]	Margin [dB]	Height [cm]	Angle [°]	Polarity
1	7410.1800	40.66	-42.83	36.48	-60.94	-13.00	47.94	211	91	Vertical
2	8338.85	41.18	-41.68	37.25	-58.50	-13.00	45.50	154	3	Vertical
3	9807.975	37.99	-39.07	38.23	-58.11	-13.00	45.11	163	105	Vertical
4	11115.2700	34.79	-37.57	39.09	-58.95	-13.00	45.95	258	1	Vertical
5	12815.225	35.23	-36.66	39.65	-57.04	-13.00	44.04	147	3	Vertical
6	14820.3600	33.22	-35.03	39.61	-57.45	-13.00	44.45	103	151	Vertical



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Test Band = 5G NR SA Band78_ TM1
Test Channel = High 3700-3800

Final Data List										
NO.	Frequency [MHz]	Reading [dBμV]	Factor [dB]	AF[dB/m]	Level [dBm]	Limit [dBm]	Margin [dB]	Height [cm]	Angle [°]	Polarity
1	7510.1800	40.26	-43.13	36.43	-61.69	-13.00	48.69	255	285	Horizontal
2	8381.4	40.95	-41.46	37.31	-58.47	-13.00	45.47	142	74	Horizontal
3	10609.525	36.24	-37.96	39.07	-57.91	-13.00	44.91	153	238	Horizontal
4	11265.2700	34.80	-37.01	39.11	-58.36	-13.00	45.36	269	209	Horizontal
5	13377	34.44	-36.29	40.12	-56.98	-13.00	43.98	258	118	Horizontal
6	15020.3600	31.99	-34.43	39.19	-58.51	-13.00	45.51	140	313	Horizontal

Final Data List										
NO.	Frequency [MHz]	Reading [dBμV]	Factor [dB]	AF[dB/m]	Level [dBm]	Limit [dBm]	Margin [dB]	Height [cm]	Angle [°]	Polarity
1	7510.1800	40.01	-43.13	36.43	-61.94	-13.00	48.94	130	32	Vertical
2	8401.525	40.91	-41.38	37.33	-58.39	-13.00	45.39	254	0	Vertical
3	9397.425	38.89	-39.92	38.49	-57.81	-13.00	44.81	165	168	Vertical
4	11265.2700	35.02	-37.01	39.11	-58.14	-13.00	45.14	128	199	Vertical
5	12845.125	35.44	-36.73	39.68	-56.88	-13.00	43.88	142	182	Vertical
6	15020.3600	32.34	-34.43	39.19	-58.16	-13.00	45.16	133	3	Vertical



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Test Band = NSA 25A-n78A_TM1
Test Channel = Low 3700-3800
Final Data List

NO.	Frequency [MHz]	Reading [dBμV]	Factor [dB]	AF[dB/m]	Level [dBm]	Limit [dBm]	Margin [dB]	Height [cm]	Angle [°]	Polarity
1	7310.1800	40.47	-43.27	36.54	-61.52	-13.00	48.52	245	255	Horizontal
2	8345.175	41.67	-41.64	37.26	-57.97	-13.00	44.97	152	60	Horizontal
3	9807.975	38.83	-39.07	38.23	-57.27	-13.00	44.27	136	317	Horizontal
4	10965.2700	34.61	-37.67	39.10	-59.22	-13.00	46.22	250	239	Horizontal
5	13452.9	35.29	-36.38	40.15	-56.20	-13.00	43.20	168	331	Horizontal
6	14620.3600	33.61	-35.23	39.91	-56.97	-13.00	43.97	192	331	Horizontal

Final Data List

NO.	Frequency [MHz]	Reading [dBμV]	Factor [dB]	AF[dB/m]	Level [dBm]	Limit [dBm]	Margin [dB]	Height [cm]	Angle [°]	Polarity
1	7310.1800	40.30	-43.27	36.54	-61.69	-13.00	48.69	245	360	Vertical
2	8055.375	41.10	-41.69	37.03	-58.82	-13.00	45.82	175	207	Vertical
3	9407.2	39.12	-39.92	38.49	-57.57	-13.00	44.57	196	87	Vertical
4	10965.2700	34.91	-37.67	39.10	-58.92	-13.00	45.92	142	102	Vertical
5	13447.15	35.36	-36.34	40.15	-56.09	-13.00	43.09	173	283	Vertical
6	14620.3600	33.41	-35.23	39.91	-57.17	-13.00	44.17	251	343	Vertical



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Test Band = NSA 25A-n78A_TM1
Test Channel = Mid 3700-3800
Final Data List

NO.	Frequency [MHz]	Reading [dBμV]	Factor [dB]	AF[dB/m]	Level [dBm]	Limit [dBm]	Margin [dB]	Height [cm]	Angle [°]	Polarity
1	7410.1800	40.86	-42.83	36.48	-60.74	-13.00	47.74	245	268	Horizontal
2	8390.6	41.18	-41.42	37.32	-58.18	-13.00	45.18	123	4	Horizontal
3	9319.225	39.23	-40.04	38.43	-57.64	-13.00	44.64	251	28	Horizontal
4	11115.2700	35.04	-37.57	39.09	-58.70	-13.00	45.70	186	194	Horizontal
5	13456.35	35.19	-36.40	40.15	-56.32	-13.00	43.32	236	360	Horizontal
6	14820.3600	33.47	-35.03	39.61	-57.20	-13.00	44.20	199	328	Horizontal

Final Data List

NO.	Frequency [MHz]	Reading [dBμV]	Factor [dB]	AF[dB/m]	Level [dBm]	Limit [dBm]	Margin [dB]	Height [cm]	Angle [°]	Polarity
1	7410.1800	41.16	-42.83	36.48	-60.44	-13.00	47.44	245	43	Vertical
2	8818.975	40.23	-41.10	38.09	-58.03	-13.00	45.03	175	360	Vertical
3	9261.15	39.59	-40.02	38.39	-57.31	-13.00	44.31	142	166	Vertical
4	11115.2700	34.48	-37.57	39.09	-59.26	-13.00	46.26	135	214	Vertical
5	12231.025	36.85	-36.96	39.12	-56.25	-13.00	43.25	268	105	Vertical
6	14820.3600	33.06	-35.03	39.61	-57.61	-13.00	44.61	192	43	Vertical



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Test Band = NSA 25A-n78A_TM1
Test Channel = High 3700-3800

Final Data List										
NO.	Frequency [MHz]	Reading [dBμV]	Factor [dB]	AF[dB/m]	Level [dBm]	Limit [dBm]	Margin [dB]	Height [cm]	Angle [°]	Polarity
1	7510.1800	40.72	-43.13	36.43	-61.23	-13.00	48.23	246	120	Horizontal
2	8379.675	41.26	-41.47	37.30	-58.17	-13.00	45.17	215	302	Horizontal
3	9807.975	38.63	-39.07	38.23	-57.47	-13.00	44.47	185	331	Horizontal
4	11265.2700	35.56	-37.01	39.11	-57.60	-13.00	44.60	193	89	Horizontal
5	13694.4	35.71	-35.99	40.06	-55.48	-13.00	42.48	235	179	Horizontal
6	15020.3600	32.54	-34.43	39.19	-57.96	-13.00	44.96	170	210	Horizontal

Final Data List										
NO.	Frequency [MHz]	Reading [dBμV]	Factor [dB]	AF[dB/m]	Level [dBm]	Limit [dBm]	Margin [dB]	Height [cm]	Angle [°]	Polarity
1	7510.1800	40.53	-43.13	36.43	-61.42	-13.00	48.42	241	273	Vertical
2	8409	41.33	-41.41	37.35	-57.99	-13.00	44.99	175	210	Vertical
3	9274.95	39.78	-40.04	38.40	-57.12	-13.00	44.12	142	227	Vertical
4	11265.2700	35.02	-37.01	39.11	-58.14	-13.00	45.14	136	210	Vertical
5	12679.525	36.00	-36.91	39.42	-56.75	-13.00	43.75	255	353	Vertical
6	15020.3600	32.94	-34.43	39.19	-57.56	-13.00	44.56	105	210	Vertical

Remark:

1) The field strength is calculated by adding the Antenna Factor, Cable Factor & AMP. The basic equation with a sample calculation is as follows:

AF = Antenna Factor(dB/m)

Factor = Cable Factor(dB) - Preamplifier (dB)

Level = Reading Level + AF + Factor -95.26

Margin = Limit – Level

---End of Attachment---

