

## **GP430 Circuit Description**

This block diagram is SiRF GPS solutions. SiRFatlas IV is a high performance DSP processor, which responsible coordination work for external device.

Signal flow: GPS antenna receive satellite signals, amplified and filtered into the decoder, decode the signal out into the main control processing, processing is complete, the final get latitude and longitude and height data. By the data, the maps software can display real time map for users through the LCD.

Memory is 128M DDR, its function is the data operation.

Storage is 4G flash, its function is storage program and user data.

Audio processor function is audio decoding and speaker driver.

Power chip management charge and discharge for the battery.