BlueMod+P24/G2

BlueMod+P25/G2

Hardware Reference

Release r06

Note

This product was developed for the purpose of communication. It is intended solely for our clients for integration into their own technical products after careful examination by experienced technical personnel for its suitability for the intended purpose. The product was not developed for or intended for use in any specific customer application. It may have to be adapted to the specific intended modalities of use or even replaced by other components in order to ensure flawless function in the respective areas of application. Performance data (range, power requirements, footprint, etc.) may depend on the operating environment, the area of application, the configuration, and method of control, as well as on other conditions of use; these may deviate from the technical specifications, the Design Guide specifications, or other product documentation. The actual performance characteristics can be determined only by measurements subsequent to integration in the target environment. Variations in the performance data of mass-produced devices may occur due to individual differences between such devices. Product samples were tested in a reference environment for compliance with the legal requirements applicable to the reference environment. No representation is made regarding the compliance with legal, regulatory, or other requirements in other environments. No representation can be made and no warranty can be assumed regarding the suitability of the product for a specific purpose. Stollmann reserves the right to make changes to the product without prior notice or to replace the product with a successor model. Of course, any changes to the product for which we have entered into a supply agreement with our customers will be made only if, and only to the extent that, such changes can reasonably be expected to be acceptable to our customers. No general commitment will be made regarding periods of availability; these must be subject to individual agreement. All agreements are subject to our Terms and Conditions for Deliveries and Payments, a copy of which is available from Stollmann.

Trademarks

The Bluetooth® word mark and logos are owned by the Bluetooth SIG, Inc.. The NFC word mark and logos are owned by the NFC Forum. Any use of such marks by Stollmann E+V GmbH is under license. Other trademarks and trade names are those of their respective owners.

Table of contents

1 Introduction

1.1 Feature Summary

Bluetooth specification v2.0+EDR (**E**nhanced **D**ata **R**ate) or v3.0+EDR[1]

CSR BlueCore6-ROM inside

Complete Co-location and Co-existence with IEEE 802.11 (AWMA, AFH and SFH)

Fast Connection Setup

Extended SCO Link

RF output power class 2 with power control

Supply Voltage 2.8V to 3.6V

Internal crystal oscillator (26MHz, 14.7456 MHz and optional 32 kHz for deep sleep)

Surface mount type

BlueMod+P24/G2: 13.5 x 18.75 x 2.85 mm

BlueMod+P25/G2: 13.5 x 22.75 x 2.85 mm

Shielded to be compliant to FCC

Full Bluetooth data rate up to 2178kbps asymmetric

Support for all Bluetooth power saving modes (Park, Sniff, Hold)

Support for very low-power modes (deep sleep and power down)

Optional support for ultra-low-power mode. Standby with Battery-Backup

PCM Interface Master / Slave supporting 13 or 16 bit linear, 8 bit µ-law or A-law Codecs and CVSD transcoders on up to 3 SCO channels

Full 8- to 128-bit encryption

High sensitivity design (-86 dBm typ.)

3 UART, USB, I2C and 2 SPI Interfaces

18 GPIO’s for individual usage for your embedded software

3 Channel ADC and 1 Channel DAC

Cortex-M3 ST32F103 core for embedded profiles or application software

Manufactured in conformance with RoHS

[1] depending on firmware variant

1.2 Applications

All Embedded Wireless Applications

- Access Points
- Printer Adapters
- Printers
- Scanners
- Wireless Sensors
- Cable Replacement
- Personal Digital Assistants (PDAs)
- Access Points
- Computers and Peripherals
- Industrial Control Applications

2 Block Diagram

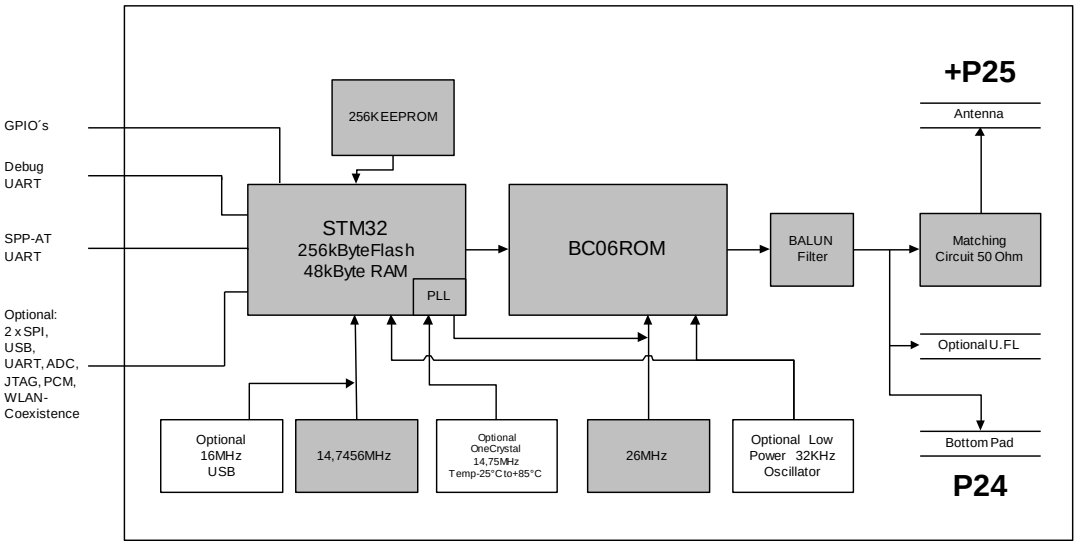


Figure 2.1 BlueMod+P2x/G2 Block-Diagram

3 Application Interface

3.1 Power Supply

Both BlueMod+P24/G2 and BlueMod+P25/G2 require a power supply with the following characteristics:

- Typical : 3.3VDC, min.: 2.8VDC – max.: 3.6VDC, low noise (≤10mV), >80mA peak

Due to the technological requirements and the pulsed radio transmission the supply needs to be fed by an ultra fast (response time ≤20µs) linear regulator placed as close as possible to the VSUP pin (16). Functionality has been verified with the following types: TOREX: XC6204x332xx or XC6401xx42xx

It is also recommended to place a low ESR capacitor with at least 10µF as close as possible to the VSUP pin (16).

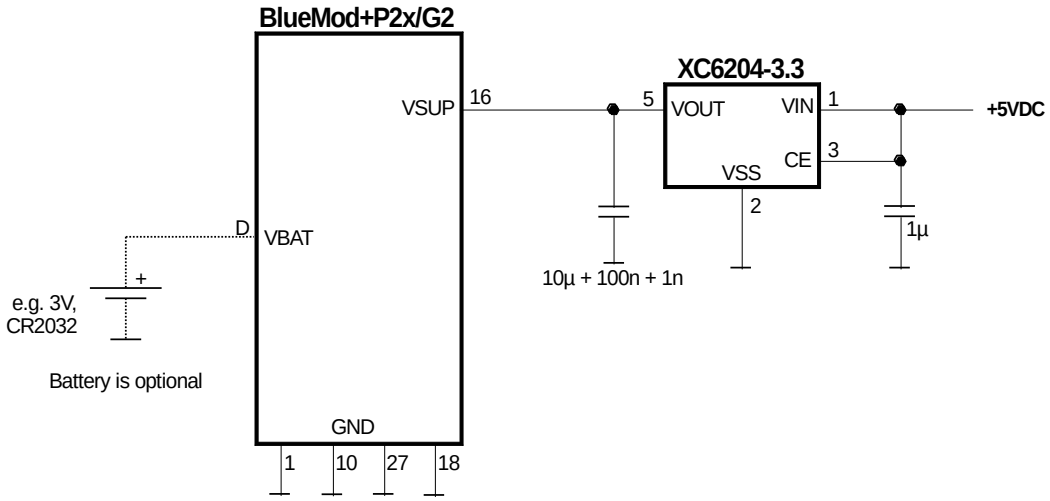


Figure 3.1 BlueMod+P2x/G2 Example Power Supply

3.1.1 Implementation in a Battery Power Host System

Please be sure that an Under Voltage Lockout Circuit to prevent deep discharge of the battery and to prevent operation outside of the specified Voltage Range, must be realized in the Host System.

3.2 Power-up / -down slew-rate

Parameter	Min	Max	Unit
V_{SUP} rise time rate	0	∞	µs/V
V_{SUP} fall time rate	20	∞	

3.3 Optional Battery-Backup[2,3]

An optional ultra-low-power mode including a Battery-Backup Function is supported by Pin D. The voltage range at VBAT should be 1.8V – 3.6V and depends on the optional Crystal oscillator voltage range (e.g. 2.8V +/-10% for a 3V lithium coin cell).

3.4 Reset

Both BlueMod+P24/G2and BlueMod+P25/G2 are equipped with circuitry for generating Power On/-Off Reset from the internal voltage VSUP. A reset is generated when the VSUP falls below typically 1,88V and is released when it rises above typically 1,92V. In case of Power-On, Power-Off, Watchdog, Low-Power or Software, RESET# acts as an output by holding RESET# at ≤ 0.3V for min. 1,5ms and max. 3,5ms.

In an application with an **external** Reset as an input, for example external Reset-Controller, we recommend to use Open-Drain-Output for this circuit. An external reset shall be generated at RESET# ≤ 0.3V for ≥ 5ms after VSUP has stabilized in the recommended voltage range.

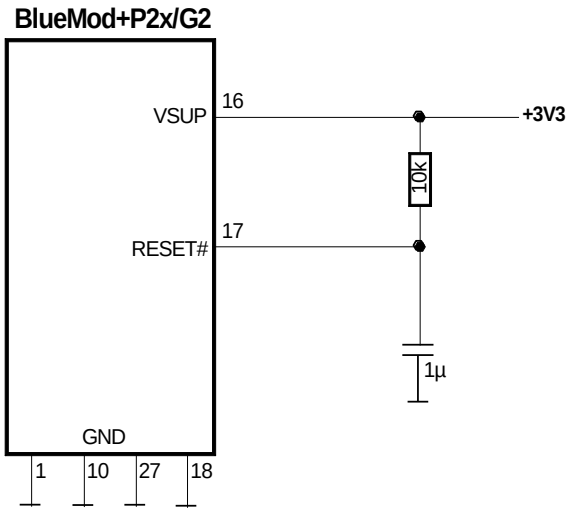
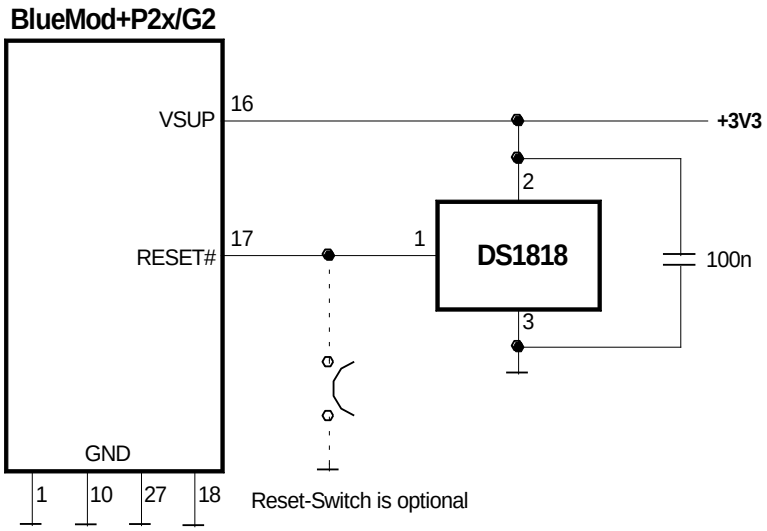


Figure 3.2 Example RC Type Reset Circuit (Acceptable Performance)

[2] subject to firmware support, contact Stollmann for current status

[3] subject to hardware support, contact Stollmann for availability of variants



Please Note: DS1818 features an open-drain output and 5k5 internal pullup

Figure 3.3 Example Reset Circuit with optional Reset Switch (Good Performance)

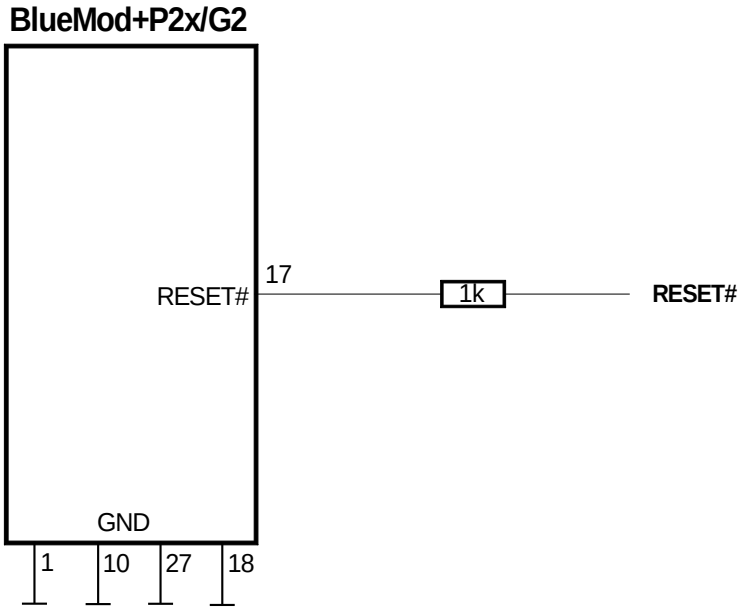


Figure 3.4 BlueMod+P2x/G2 Example Reset, External Reset

3.4.1 Pin State on reset

The following table shows the pin states of BlueMod+P2x/G2 on reset.

Pin Name	State: BlueMod+P2x/G2
UART_RTS#	Input Floating
UART_TXD	Input Floating
UART_RXD	Input Floating
UART_CTS#	Input Pull-Up
GPIO[0] – GPIO[13], GPIO[16], GPIO[17]	Input Floating
GPIO[14]	Input Pull-Up
GPIO[15]	Input Pull-Up
Boot0	Input Pull-Down
ATRST	Input Pull-Up
ATDI	Input Pull-Up
ATMS	Input Pull-Up
ATDO	Input Floating
ATCK	Input Pull-Down
USB_DM	Input Floating
USB_DP	Input Floating

3.5 Serial Interface

The functionality of the interface corresponds to the V.24 / RS-232 standard on TTL-level.

- Transmission speeds:
  - 1200 bps
  - 2400 bps
  - 4800 bps
  - 9600 bps
  - 19200 bps
  - 38400 bps
  - 57600 bps
  - 115200 bps
  - 230400 bps
  - 460800 bps
  - 921200 bps
- Character representation:
  - 8 bit, even/odd/no parity, 1 or 2 stop bits
  - 7 bit, even/odd parity, 1 or 2 stop bits
  - 7 bit no parity, 2 stop bits
- Hardware flow-control with UART_RTS and UART_CTS (active low)

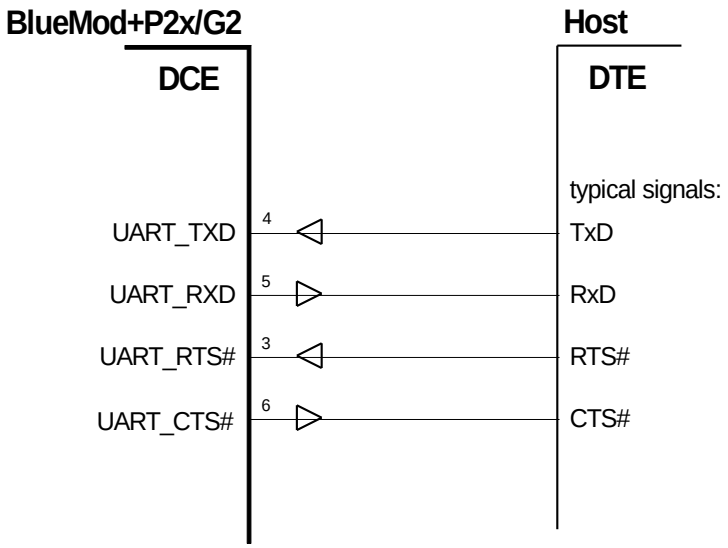


Figure 3.5 BlueMod+P2x/G2 Serial Interface

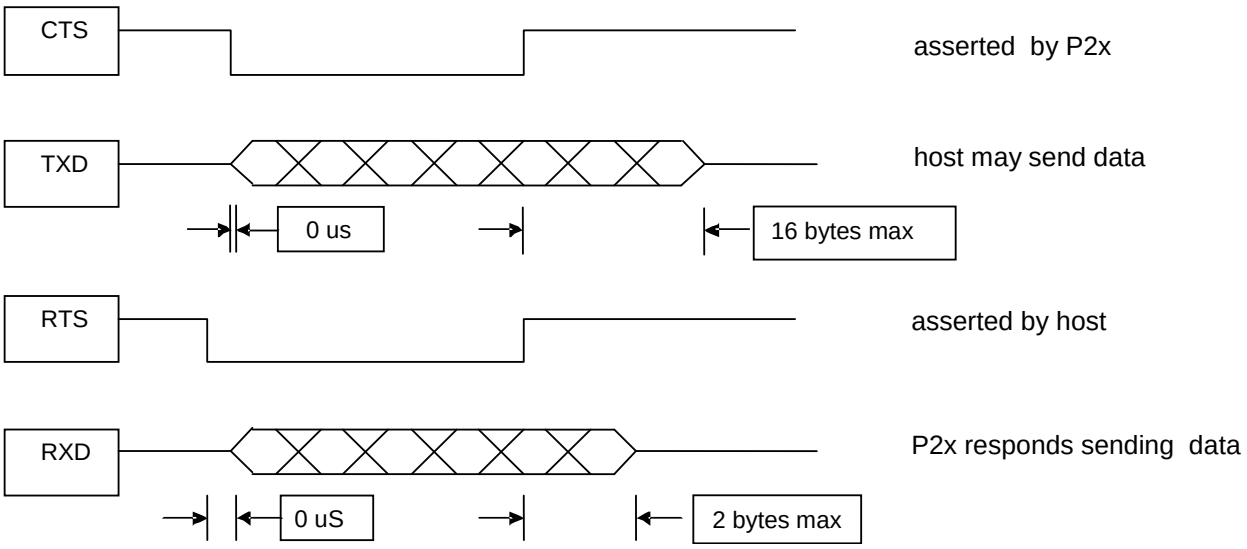


Figure 3.6 BlueMod+P2x/G2 UART Timing Diagram

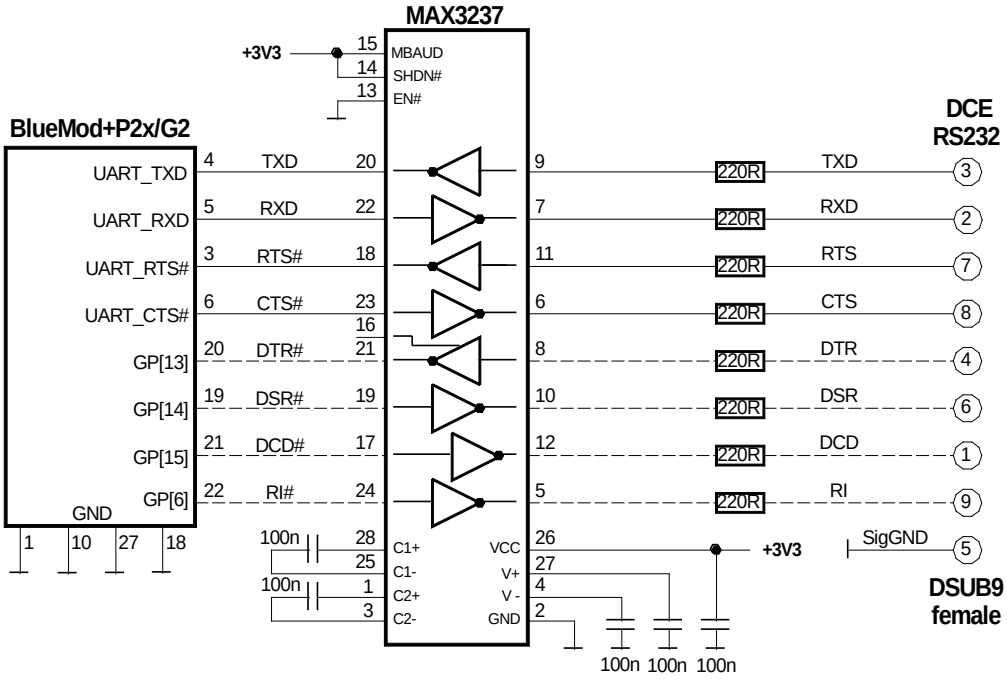


Figure 3.7 BlueMod+P2x/G2 Example RS232 Interface (DCE type)

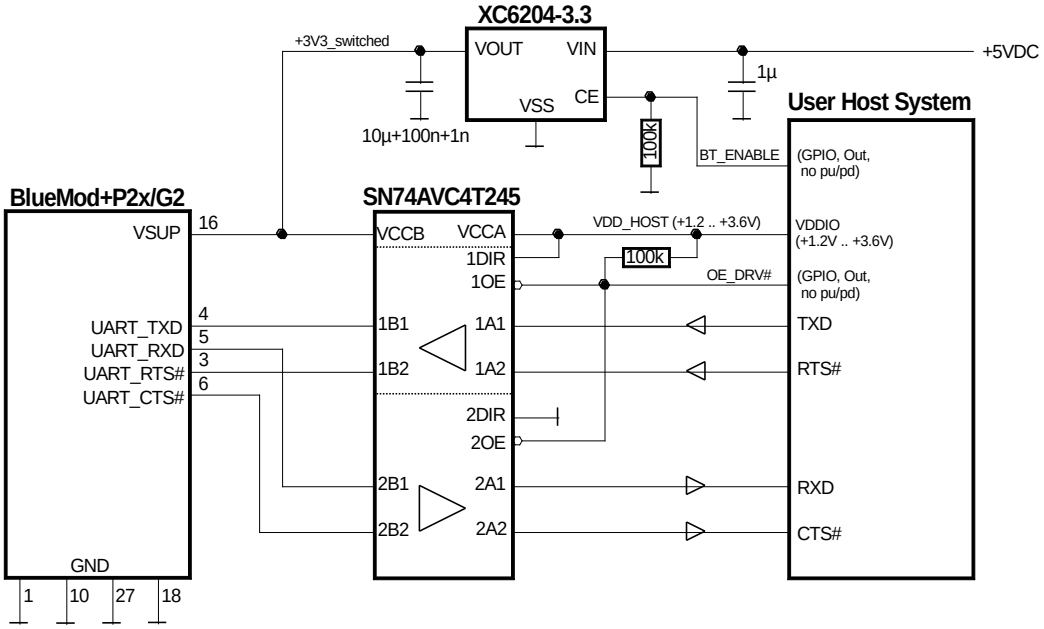


Figure 3.8 Example of Mixed Signal Level Interface (Host Side 1.4 .. 3.6V)

3.5.1 3-wire Serial Interface

When using only GND and UART_Rx, UART_Tx serial lines, you may leave UART_RTS# and UART_CTS# open.

Note: Not using flow control can result in loss of data.

3.5.2 Implementing a Test and Firmware Update Interface

In case tracing the Host to module interface, an in circuit production test or firmware update functionality is required. We recommend to provide access to the following signals:

UART_TXD, UART_RXD, UART_RTS#, UART_CTS#, RESET#, VCC and GND

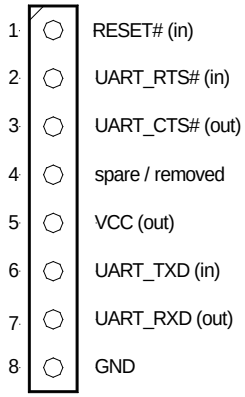


Figure 3.9 Example test connector: 8pin Single Row

3.6 GPIO Interface

It is possible to use the programmable digital I/Os GPIO[0:17]. The Current from GPIO[17] is limited to 3mA max.

For 5V tolerant GPIO´s refer to *chapter 4.2.2.*

Please note that for the sake of lowest possible power consumption all IO pins shall be at logical H- or L-level at nearly any time. This also applies to "unused" GPIOs. So, external pullup- or pulldown resistors may be required.

According to chapter 3.4.1, nearly all GPIOs are floating inputs during reset.

3.7 ADC[4]

It is possible to use the programmable I/Os PIO[0:3,6,7,13:15] as ADC.

The 12-bit ADC is a successive approximation analog-to-digital converter. A/D conversion can be performed in single, continuous, scan or discontinuous mode.

3.8 DAC[5]

It is possible to use the programmable I/Os PIO[2,3] as DAC.

The DAC module is a 12-bit, voltage output digital-to-analog converter. The DAC can be configured in 8- or 12-bit mode and may be used in conjunction with the DMA controller.

3.9 Bluetooth Radio Interface

The BlueMod+P24/G2 presents no integrated ceramic antenna whereas provides a 50Ω RF Interface.

The BlueMod+P25/G2 presents an integrated ceramic antenna.

Note: It is highly recommended that you follow the design rule given in this document chapter 6.4 Antenna Issues and the Stollmann Application Note on Antenna design [1].

3.10 PCM Interface[6]

PCM or Pulse Code Modulation is a sampling technique for digitising analogue signals.

The PCM interface for voice applications is provided via the PCM_OUT, PCM_IN, PCM_CLK and PCM_SYNC pins.

The PCM interface can act as master or as slave device.

In master mode, clock frequencies of 128kHz, 256kHz or 512kHz can be generated, when using the internal 4MHz clock. In slave mode, clock frequencies up to 2048kHz are accepted.

The Frame Clock is 8kHz. Long and Short Frame Sync are supported.

Both BlueMod+P24/G2 and BlueMod+P25/G2 interface directly to PCM audio devices including the following:

[4] subject to firmware support, contact Stollmann for current status

[5] subject to firmware support, contact Stollmann for current status

[6] subject to firmware support, contact Stollmann for current status

OKI MSM7705 four channel A-law and µ-law codec

Motorola MC145481 8-bit A-law and µ-law codec

Motorola MC145483 13-bit linear codec

WINBOND W681310 8-bit A-law and µ-law codec

WINBOND W681360 13-bit linear codec

STW 5093 5094 14-bit linear codec

BlueMod+P2x/G2 is also compatible with the Motorola SSI interface

3.11 USB Interface[7,8]

3.11.1 USB_DP, USB_DN

BlueMod+P2x/G2 contain a full speed USB version 2.0 compliant interface capable of directly driving an USB cable. The BlueMod+P2x/G2 operates as a USB peripheral and responds to requests from a USB master host controller.

3.11.2 USB Self-Powered Mode

In USB self-powered mode, the BlueMod+P2x/G2 is powered from its own power supply and not from the USB Vbus line. In order to detect when the USB Vbus line is powered up, the USB Vbus line is monitored by PIO[3] through a voltage divider.

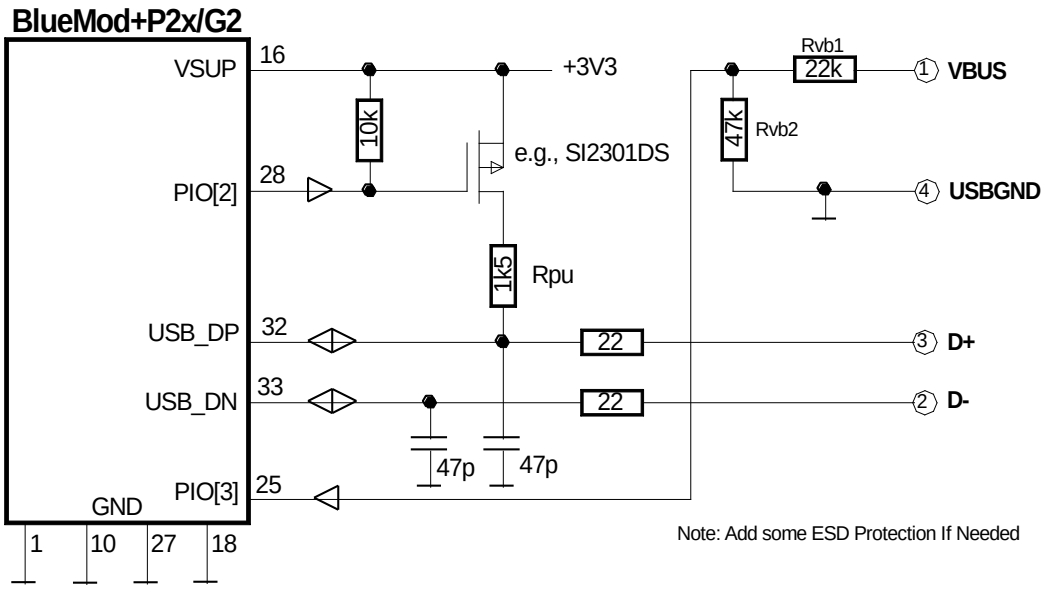


Figure 3.10 BlueMod+P2x/G2 USB Self-Powered Mode

An 1.5KΩ pull up resistor needs to be switched between VDD and the USB D+ line, by means of PIO[2]. A low-level on PIO[2] turns the P-channel MOSFET on and pulls the USB D+ line high when the BlueMod+P2x/G2 is ready for enumeration, signaling to the host controller that the BlueMod+P2x/G2 a full speed (12Mbps) USB device.

[7] subject to firmware support, contact Stollmann for current status.

[8] subject to hardware support, contact Stollmann for availability of variants

3.11.3 USB Bus-Powered Mode

In USB bus-powered mode, the BlueMod+P2x/G2 is powered from the USB Vbus line by means of a Low Drop Out (LDO) Voltage Regulator. When choosing the LDO Voltage Regulator for supplying the +3.3V power to the BlueMod+P2x/G2, some factors that need to be considered are:

The voltage specification for the USB Vbus line is +4.75V to +5.25V.

The total current required (average and peak) for the design.

The voltage regulator's drop out voltage vs. output current.

The voltage regulator's power dissipation over the operating temperature range.

Filtering requirements on the USB Vbus line to attenuate noise above the voltage regulator's bandwidth.

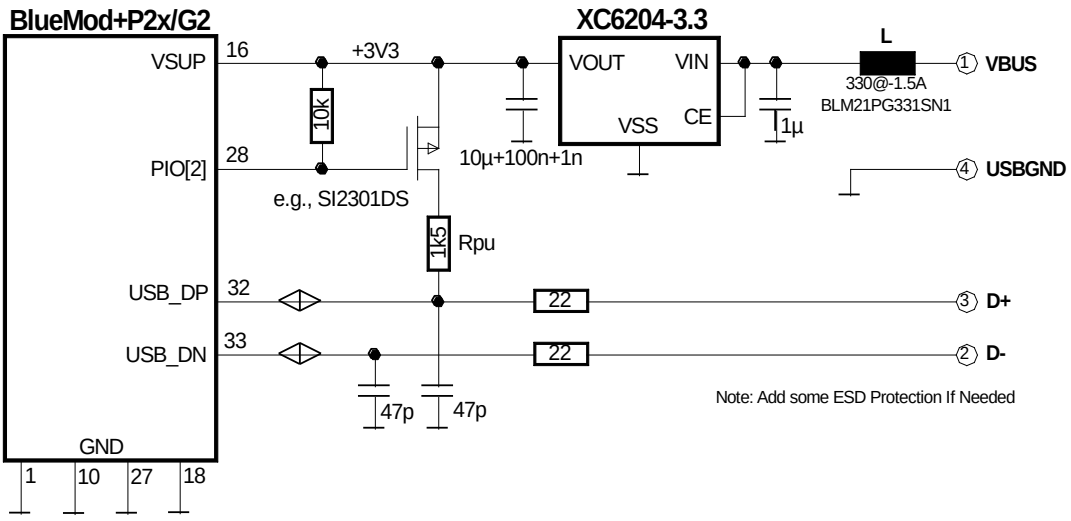


Figure 3.11 BlueMod+P2x/G2 USB Bus-Powered Mode

For details see related Software- or Interface- description.

3.12 Serial Peripheral Interface[9]

The serial peripheral interface (SPI) allows half/full-duplex, synchronous, serial communication with external devices. The interface can be configured as the master and in this case it provides the communication clock (SCK) to the external slave device. The interface is also capable of operating in multi master configuration.

It may be used for a variety of purposes, including simplex synchronous transfer on two lines with a possible bidirectional data line or reliable communication using CRC checking.

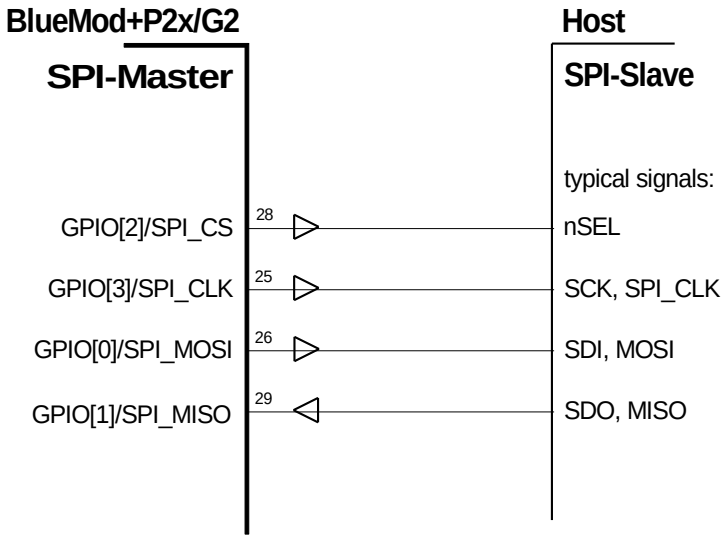


Figure 3.12 BlueMod+P2x/G2 SPI Interface e.g. in Master Mode

[9] subject to firmware support, contact Stollmann for current status.

3.13 I2C Interface[10]

The I2C bus interface serves as an interface between the internal microcontroller an the serial I2C bus. It provides multimaster capability, and controls all I2C bus specific sequencing, protocol, arbitration and timing. It supports standard (100kHz) and fast (400kHz) speed modes.

GPIO[11]/I2C SDA and GPIO[12]/I2C SCL can be used to form an I^2C interface. It is recommended to connect 4k7Ω Pull-up resistors on GPIO[12]/I2C_SCL and GPIO[11]/I2C_SDA.

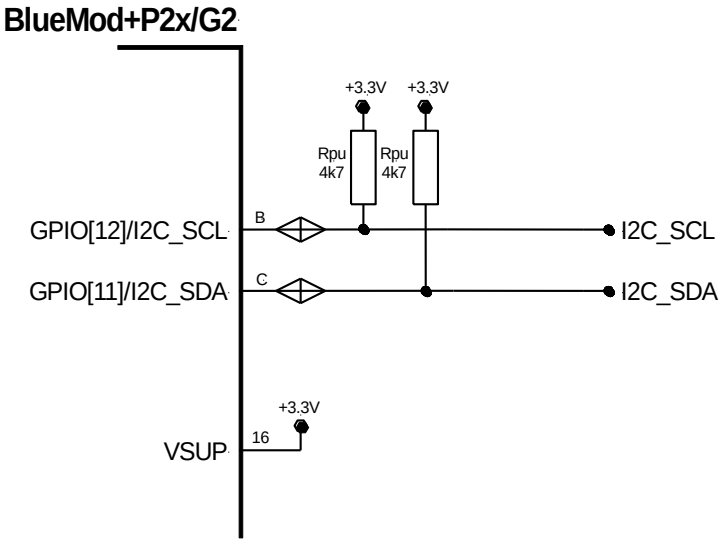


Figure 3.13 BlueMod+P2x/G2 I2C Interface

[10] subject to firmware support, contact Stollmann for current status.

3.14 Operating in a Power-Switched Environment

Nearly all signals of BlueMod+P2x/G2 application interface are ***not*** 5V tolerant. This is the cause for a potential "backfeeding" problem which may arise, if the module is operated in an environment where its power supply (VSUP) is switched off by the application. This might be to save some power in times Bluetooth is not needed.

As stated in chapter 5.1, the voltage on any I/O pin must not exceed VSUP by more than 0,3V at any time. Otherwise some current I_{INJECT} flows through the internal protection diodes. This may damage the module.

There is no problem if the application circuit design and programming can assure that all signals directed towards BlueMod+P2x/G2 are set to low (U < 0,3V) before and while VSUP is turned off. If this is not guaranteed, at least a series resistor (about 1k) must be inserted into the signal path. This does protect the module but obviously cannot prevent from an unwanted, additional current flow in case of such signal being at high-level.

A typical situation is the interfacing of BlueMod+P2x/G2 to an application processor using the serial UART lines (RXD+TXD in its most simple form). The UART transmitter (TXD) of the application processor can usually not be set to low but is at high-level when idle. This would lead to backfeeding.

According to the drive capability of the application processor the current into pin 4 (UART_TXD) may reach values that can damage the module (I > 5mA).

The application example shown in Figure 3.10 makes use of a CMOS AND-gate (HC08 type, may be single gate) to keep UART_TXD at the BlueMod+P2x/G2 low when VSUP is switched off. Please note that for protection reasons there is a 1k series resistor to limit current during switch over time.

The 100k-pullup resistor to VSUP, at UART_RXD, assures that this line is kept at low-level when VSUP of the module is turned off. There should be no other pullup or pulldown resistor at the application processor side**.**

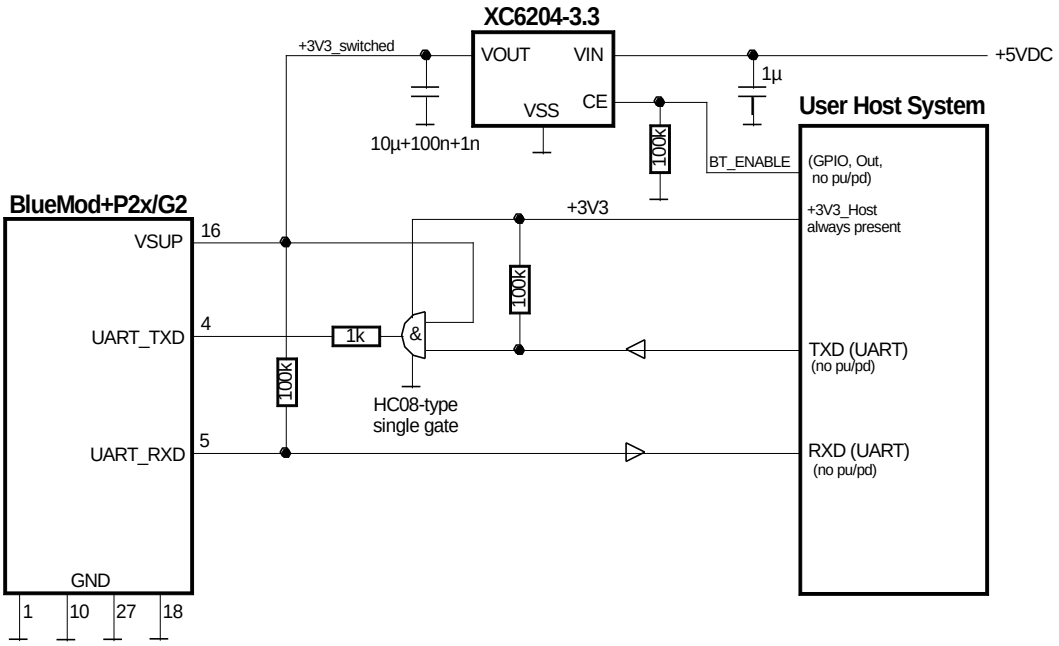


Figure 3.14 BlueMod+P2x/G2 I2C With Switched Power Supply

Note 1:

Each signal directed towards BlueMod+P2x/G2 must be gated, or set to low by application system when module power is switched off. If not gated, use a series resistor (about 1k) to protect the module from damage if I/O line is not set to low.

Note 2:

To assure power consumption as low as possible, make sure that TXD(UART) is always close to VSS or +3V3 when idle (consider delta-ICC of HCMOS device).

If additional connectivity is needed, same rules apply. As an alternative, you can think of a bidirectional level translator featuring separated supply pins for both sides, like SN74AVC4T245.

4 Pin Description

4.1 Pin Numbering

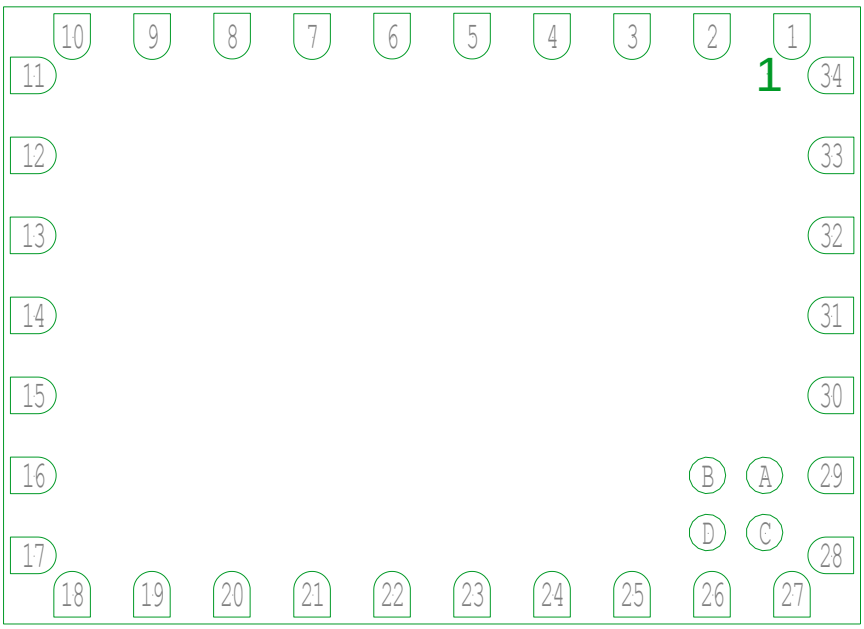


Figure 4.1 BlueMod+P24/G2 Pin Numbering

(Bottom-View)

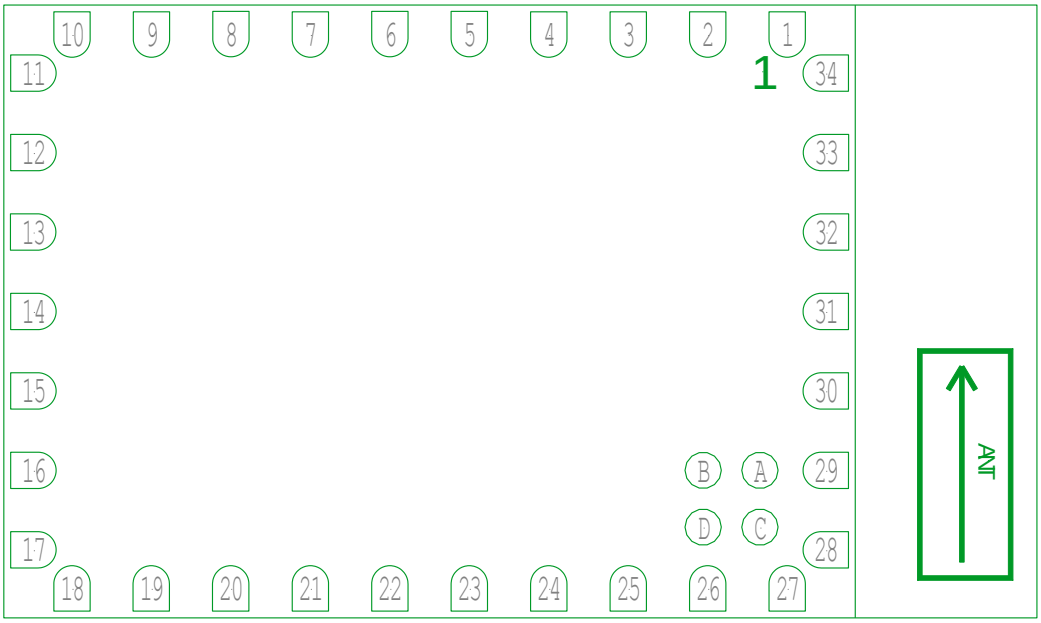


Figure 4.2 BlueMod+P25/G2 Pin Numbering

(Bottom-View)

4.2 Pin Description

4.2.1 General Pin Description

No	Pin Name	Type	PU/PD	Active	Description
1	GND1	P	-	-	Connect to Ground
2	ANT	I/O	-	-	Antenna Connector
3	UART_RTS#[1]	I	PD	H	UART Request To Send (active low)
4	UART_TXD[1]	I	PD	L	UART Data Input, weak internal Pull-Down
5	UART_RXD[1]	O	-	L	UART Data Output
6	UART_CTS#[1]	O	PU	H	UART Clear To Send, internal Pull-Up
7	GPIO[9]/PCM_CLK	I/O	PU	L	General Purpose Input/Output, weak internal Pull-Up / PCM Data Clock Output
8	GPIO[10]/PCM_O/TXD3	I/O	PU	L	General Purpose Input/Output, weak internal Pull-Up / PCM Data Output/ UART3 DATA Output
9	GPIO[7]/PCM_SYNC	I/O	PD	H	General Purpose Input/Output, weak internal Pull-Down / PCM Data Sync Output
10	GND2	P	-	-	Connect to Ground
11	ATRST/SPI2_MISO	I/O (FT)	PU	H	JTAG reset, internal Pull-Up / SPI2 Master In - Slave Out
12	GPIO[8]/PCM_I/RXD3	I/O	PU	L	General Purpose Input/Output, weak internal Pull-Up / PCM Data Input / UART3 DATA Input
13	ATDI/SPI2_CS	I/O (FT)	PU	-	JTAG test data input, weak internal Pull-Up / SPI2 Chip Select
14	ATMS	I (FT)	PU	-	JTAG mode select, weak internal Pull-Up
15	RESERVED	I	PD	-	for Debug-Use only, leave open
16	VCC	P	-	-	VCC
17	RESET#	I/O	PU	L	Reset input/output
18	GND3	P	-	-	Connect to Ground
19	GPIO[14]	I/O	PU	L	General Purpose Input/Output, weak internal Pull-Up
20	GPIO[13]	I/O	PD	H	General Purpose Input/Output, weak internal Pull-Down
21	GPIO[15]	I/O	PU	L	General Purpose Input/Output, weak internal Pull-Up
22	GPIO[6]/ADC	I/O	PD	H	General Purpose Input/Output, weak internal Pull-Up / ADC Input
23	ATDO/SPI2_SCK	O (FT)	PU	-	JTAG test data output
24	ATCK	O (FT)	PD	-	JTAG clock, weak internal Pull-Down
25	GPIO[3]/SPI_CLK	I/O	PD	H	General Purpose Input/Output, weak internal Pull-Down / SPI Clock
26	GPIO[0]/SPI_MOSI	I/O	PD	H	General Purpose Input/Output, weak internal Pull-Down / SPI Master Out – Slave In
27	GND4	P	-	-	Connect to Ground
28	GPIO[2]/SPI_CS	I/O	PD	H	General Purpose Input/Output, weak internal Pull-Down / SPI Chip Select
29	GPIO[1]/SPI_MISO	I/O	PD	H	General Purpose Input/Output, weak internal Pull-Down / SPI Master In – Slave Out
30	GPIO[5]/TXD2	I/O (FT)	PD	H	General Purpose Input/Output, weak internal Pull-Down / UART2 DATA Output
31	GPIO[4]/RXD2	I/O (FT)	PD	H	General Purpose Input/Output, weak internal Pull-Down / UART2 DATA Input
32	USB_DN	I/O (FT)	PD	-	USB Data Minus Terminal
33	USB_DP	I/O (FT)	PD	-	USB Data Plus Terminal
34	GPIO[17]/TAMPER	I/O	PD	-	General Purpose Input/Output, weak internal Pull-Down, do not use as current source! / TAMPER

No	Pin Name	Type	PU/PD	Active	Description
					In
A	GPIO[16]/SPI2_MOSI	I/O	PU	L	General Purpose Input/Output, weak internal Pull-Up / SPI2 Master Out – Slave In
B	GPIO[12]/I2C_SCL	I/O	PU	L	General Purpose Input/Output, weak internal Pull-Up / I2C Serial Clock Line
C	GPIO[11]/I2C_SDA	I/O	PU	L	General Purpose Input/Output, weak internal Pull-Up / I2C Serial Data Line
D	VBAT	P	-	-	Optional Battery Backup

Type: PU - Pulled up; PD – pulled down; P – Power; I – Input; O – Output; I/O – bidirectional (FT) - 5V tolerant

[1]DCE-Style Naming Convention

4.2.2 Application Specific Pin Description

4.2.2.1 SPP PIN Configuration DCE Mode

No	Pin Name	SPP-Function	Type	PU/PD	Active	Description
1	GND1	Power	P	-	-	Connect to Ground
2	ANT	Antenna	I/O	-	-	Antenna Connector
3	UART_RTS#[1]	/RTS	I	PD	L	UART Request To Send
4	UART_TXD[1]	TXD	I	PD	H	UART Data Input
5	UART_RXD[1]	RXD	O		H	UART Data Output
6	UART_CTS#[1]	/CTS	O	PU	L	UART Clear To Send
7	GPIO[9]/PCM_CLK	/LED2	O		L	Bluetooth connected. Active if a Bluetooth connection exists. Inactive in idle state. Flashes during startup.
8	GPIO[10]/PCM_O/TXD3	UA2	O			User Output 2
9	GPIO[7]/PCM_SYNC	/LED1	O		L	Device Ready
10	GND2	Power	-	-	-	Connect to Ground
11	ATRST/SPI2_MISO	reserved	I/O (FT)	PU	H	leave open
12	GPIO[8]/PCM_I/RXD3	/UE1	I	PU	L	User Input 1
13	ATDI/SPI2_CS	reserved	I/O (FT)	PU	-	leave open
14	ATMS	reserved	I (FT)	PU	-	leave open
15	RESERVED	reserved	I	PD	H	for Debug-Use only, leave open
16	VCC	Power	-	-	-	VCC
17	RESET#	/RESET	I/O	PU	L	Reset input/output
18	GND3	Power	-		-	Connect to Ground
19	GPIO[14]	/RTC-OUT	O	PU	L	DSR in DCE mode, DTR in DTE mode
20	GPIO[13]	/RTC-IN	I	PD	L	DTR in DCE mode, DSR in DTE mode
21	GPIO[15]	/DCD or /DCD-DTE	I/O	PU	L	Data Carrier Detect , Input in DTE mode Output in DCE mode
22	GPIO[6]/ADC	/RI	I/O	PU	L	Ring Indicator, Input in DTE mode Output in DCE mode
23	ATDO/SPI2_SCK	reserved	O (FT)	PU	-	JTAG
24	ATCK	reserved	O (FT)	PD	-	JTAG
25	GPIO[3]/SPI_CLK	reserved	I/O	PD	H	leave open
26	GPIO[0]/SPI_MOSI	reserved	I/O	PD	H	leave open
27	GND4	Power	-		-	Connect to Ground
28	GPIO[2]/SPI_CS	reserved	O			leave open
29	GPIO[1]/SPI_MISO	/UE2	I	PD	H	User Input 2, Break Detect (1)
30	GPIO[5]/TXD2	reserved	O (FT)			leave open
31	GPIO[4]/RXD2	DTE-/DCE Select	I (FT)	PD	H	DTE (high) DCE (low) mode selector
32	USB_DN	reserved	I/O (FT)	PD	-	leave open
33	USB_DP	reserved	I/O (FT)	PD	-	leave open
34	GPIO[17]/TAMPER	reserved	I/O	PD	-	leave open
A	GPIO[16]/SPI2_MOSI	reserved	I/O	PU	L	leave open
B	GPIO[12]/I2C_SCL	reserved	I/O	PU	L	leave open
C	GPIO[11]/I2C_SDA	reserved	I/O	PU	L	leave open
D	VBAT	Power	-		-	Optional Battery Backup

Type: PU - Pulled up; PD – pulled down; P – Power; I – Input; O – Output; I/O – bidirectional; (FT) - 5V tolerant

[1]DCE-Style Naming Convention

4.2.2.2 HDP PIN Configuration (Firmware v1.552 and newer)

No	Pin Name	HDP-Function	Type	PU/PD	Active	Description
1	GND1	Power	P	-	-	Connect to Ground
2	ANT	Antenna	I/O	-	-	Antenna Connector
3	UART_RTS#[1]	/RTS	I	PU	L	UART Request To Send
4	UART_TXD[1]	TXD	I	PU	L	UART Data Input
5	UART_RXD[1]	RXD	O		L	UART Data Output
6	UART_CTS#[1]	/CTS	O	PU	L	UART Clear To Send
7	GPIO[9]/PCM_CLK	/BT_CONNECTED	O		L	Bluetooth connected. Active if a Bluetooth connection exists. Inactive in idle state. Flashes during startup.
8	GPIO[10]/PCM_O/TXD3	reserved	I	PD		leave open
9	GPIO[7]/PCM_SYNC	/READY	O		L	Device initialized, Ready for Bluetooth usage
10	GND2	Power	P	-	-	Connect to Ground
11	ATRST/SPI2_MISO	reserved	I/O (FT)	PU	H	leave open
12	GPIO[8]/PCM_I/RXD3	/TEST	I	PU	L	Do not connect
13	ATDI/SPI2_CS	reserved	I (FT)	PU	-	JTAG
14	ATMS	reserved	I (FT)	PU	-	leave open
15	RESERVED	reserved	I	PD	H	for Debug-Use only, leave open
16	VCC	Power	P	-	-	VCC
17	RESET#	/RESET	I/O	PU	L	Reset input/output
18	GND3	Power	P	-	-	Connect to Ground
19	GPIO[14]	/RTC-OUT	O	PU	L	DSR
20	GPIO[13]	/RTC-IN	I	PU	L	DTR
21	GPIO[15]	reserved	I	PU	L	leave open
22	GPIO[6]/ADC	reserved	I	PU	-	leave open
23	ATDO/SPI2_SCK	reserved	I (FT)	PD	-	JTAG
24	ATCK	reserved	I (FT)	PD	-	JTAG
25	GPIO[3]/SPI_CLK	reserved	I	PD	-	leave open
26	GPIO[0]/SPI_MOSI	reserved	I	PD	-	leave open
27	GND4	Power	P	-	-	Connect to Ground
28	GPIO[2]/SPI_CS	reserved	I	PD		leave open
29	GPIO[1]/SPI_MISO	reserved	I	PD	-	leave open
30	GPIO[5]/TXD2	reserved	O (FT)	-	L	leave open
31	GPIO[4]/RXD2	reserved	I (FT)	PU	-	leave open
32	USB_DN	reserved	I (FT)	PD	-	leave open
33	USB_DP	reserved	I (FT)	PD	-	leave open
34	GPIO[17]/TAMPER	reserved	I	PD	-	leave open
A	GPIO[16]/SPI2_MOSI	reserved	I	PD	-	leave open
B	GPIO[12]/I2C_SCL	reserved	I	PD	-	leave open
C	GPIO[11]/I2C_SDA	reserved	I	PD	-	leave open
D	VBAT	Power	-		-	Optional Battery Backup

Type: PU - Pulled up; PD – pulled down; P – Power; I – Input; O – Output; I/O – bidirectional; (FT) - 5V tolerant;

[1]DCE-Style Naming Convention

Note: For PIN configuration of older firmware versions please contact Stollmann.

5 Electrical Characteristics

5.1 Absolute Maximum Ratings

Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “Electrical Requirements” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

Item	Symbol	Absolute Maximum Ratings	Unit
Supply Voltage	V_{SUP}	-0.4 to +3.7	V
Batterie Backup Voltage	V_{BAT}	-0.4 to +1.8 … +3.7 (depends on the optional crystal oscillator)	V
Voltage on any pin	V_{Pin}	GND -0.3 to VSUP +0.3	V
Voltage on 5V tolerant pin	V_{Pin}	GND -0.3 to +5.5V	V

5.2 Electrical Requirements

VSUP = 3.3V, T_{amb} = 25°C if nothing else stated

Item	Condition	Limit			Unit
		Min	Typ	Max	
Frequency Range		2400		2483.5	MHz
Load impedance	Measured with network analyzer in the frequency range at antenna pin		50		Ohm
Output return loss	Receive Mode to 50Ω load Transmit Mode to 50Ω load	-10 -10			dBm
Supply voltage VSUP	The typical voltage is recommended VSUP at voltage pin	2.8	3.3	3.6	Vdc
Ripple on Vcc	Ripple frequency ≤10MHz			10	mVrms

5.3 Environmental Requirements

Item	Symbol	Absolute Maximum Ratings	Unit
Storage temperature range	T_{stg}	-40 to +105	°C
Operating temperature range	T_{op}	-40 to +85	°C

5.4 Digital I/O including RESET#

VSUP = 3.3V, T_{amb} = 25°C

Symbol	Item	Condition	Limit			Unit
			Min	Typ	Max	
V_{IL}	Low-Level Input Voltage	VSUP = 3.3V	- 0.5	-	0.8	V
V_{IH}	High-Level Input Voltage		2.0	-	VSUP +0.3	V
V_{OL}	Low-Level Output Voltage	I_{OL} = 4mA	-	-	0.2	V
V_{OH}	High-Level Output Voltage	I_{OH} = -4mA	VSUP-0.4	-	-	V
I_{OL}	Low -Level Output Current (except GPIO[17])	V_{OL} = 0.4V	-	-	8	mA
I_{OH}	High-Level Output Current (except GPIO[17])	V_{OH} = 2.4V	-	-	8	mA
I_{OL} (GPIO[17])	Low -Level Output Current at GPIO[17]	V_{OL} = 0.4V	-	-	3	mA
I_{OH} (GPIO[17])	High-Level Output Current at GPIO[17]	V_{OH} = 2.4V	-	-	3	mA
I_{wp-u}	Input-current	Weak pull-up typ. 40kΩ	–5.0	–1.0	–0.2	µA
I_{wp-d}	Input-current	Weak pull-down typ. 40kΩ	+0,2	+1.0	+5.0	µA
I_{lc}	I/O pad leakage current	Standard I/Os	–1.0	0	+1.0	µA
I_{lc}	I/O pad leakage current	Vin = 5V	-3,0	0	+3,0	µA
C_I	Input Capacitance		-	5	-	pF

5.5 USB-Interface

VSUP = 3.3V, T_{amb} = 25°C

Item			Unit
	Min	Max	
Input logic level low	-	0.3xVSUP	V
Input logic level high	0.7xVSUP	5.5	V
Output logic level low)*	0	0.2	V
Output logic level high)*	2.8	VSUP	V

)* connected to correctly terminated USB cable

5.6 Power consumption and power down modes

5.6.1 SPP configuration

5.6.1.1 Deep Sleep state

The Bluetooth RF is completely deactivated, no paging requests from other Bluetooth devices will be recognized. Only setting control line DTR low (active) will activate the BlueMod+P2x/G2 and may initiate a Bluetooth link dependent on other parameters.

Note: In Deep Sleep state the AT command set and the UART interface are deactivated, CTS line is high.

5.6.1.2 Power down state

The Bluetooth RF is activated every 1.25 seconds, paging requests from other Bluetooth devices will be recognized after that intervals and accepted if allowed. Additionally setting control line DTR low (active) will activate the BlueMod+P24/P25 and may initiate a Bluetooth link dependent on other parameters.

Note: In Power down state the AT command set and the UART interface are deactivated, CTS line is high.

5.6.1.3 Idle state

No power down mode activated.

All functionality is available immediately including connection control using AT command set.

5.6.1.4 Power consumption, SPP

The following values are approximate power consumption values in the different states:

VSUP = 3.3V, T_{amb} = 25°C

Condition	Current Consumption		Unit
	I_{MEAN} average	I_{PEAK}	
Idle, no page scan, no inquiry scan, UART baud rate: 1200 baud	7.5	15	mA
Idle, all functions available, no Bluetooth link, page scan & inquiry scan interval 1.28s	8	56	mA
Idle, all functions available, no Bluetooth link, continuous page scans	50	60	mA
Bluetooth connected, no data traffic – close range (Slave)	10	56	mA
Bluetooth connected, data traffic 115 kbit/s – close range (Slave)	33	61	mA
ACL connected DH5 max PWR, shortest Poll Period (Slave)	43	68	mA
ACL connected DH5 min PWR, shortest Poll Period (Slave)	42	67	mA
ACL connected DH1 max PWR, shortest Poll Period (Slave)	43	69	mA
ACL connected DH1 min PWR, shortest Poll Period (Slave)	41	67	mA
Power Down	7.5	--	mA
Device in reset	1.5	--	mA

5.6.1.5 Power consumption, HDP

The following values are power consumption values in the different states:

VSUP = 3.3V, T_{amb} = 25°C, UART_RXD, UART_TXD, UART_RTS#, UART_CTS#, GPIO[13](DTR), GPIO[14](DSR) connected. All other Pins left open. In UICP mode UART_TXD, UART_RTS# and GPIO[13] are set to high-level.

Condition	Current Consumption		Unit
	I_{MEAN} average	I_{PEAK}	
Idle, no page scan, no inquiry scan, UICP disabled	3.5	13	mA
Idle, no page scan, no inquiry scan, UICP enabled	0.081	12	mA
Idle, page scan enabled, UICP disabled	3.9	55	mA
Idle, page scan enabled, UICP enabled	0.5	52	mA
Bluetooth connected, page scan enabled, no data traffic, UICP disabled	8.5	56	mA
Bluetooth connected, page scan enabled, no data traffic, UICP enabled	5	62	mA
Bluetooth connected, page scan enabled, full data traffic, UICP disabled	23	66	mA
Bluetooth connected, page scan enabled, full data traffic, UICP enabled	23	67	mA

5.7 Power-up time

The time until the BlueMod+P2x/G2 is able to accept link requests or serial data depends on the firmware version and the software parameters RSTTIM and RSTMSG. In the SPP firmware version 1.001 the module is command ready after at least 0.3s (RSTTIM=3 / RSTMSG=0) and 2.16s (RSTTIM=40 / RSTMSG=1) maximum. Bluetooth links are accepted 2.3s after reset.

Note: For further information refer to the document BlueMod+_startup_timing_r03.

5.8 RF performance

Vcc = 3.0V to 3.6V, T_{amb} = - 40°C to +85°C

Receiver	Frequency [GHz]	Limit			BT	Unit
		Min	Typ	Max	Spec	
Sensitivity at 0.1% BER DH1	2.402	-70.0	-85.3		≤-70	dBm
	2.441	-70.0	-85.4			
	2.480	-70.0	-86.0			
Sensitivity at 0.1% BER DH5	2.402	-70.0	-84.4		≤-70	dBm
	2.441	-70.0	-84.9			
	2.480	-70.0	-85.6			
Sensitivity at 0.1% BER EDR2, PI/4 DQPSK	2.402	-70.0	-88.1		≤-70	dBm
	2.441	-70.0	-88.3			
	2.480	-70.0	-88.1			
Sensitivity at 0.1% BER EDR3, 8DPSK	2.402	-70.0	-82.0		≤-70	dBm
	2.441	-70.0	-82.7			
	2.480	-70.0	-82.4			
Maximum received signal at 0.1% BER with DH1		-20.0	>10		≥-20	dBm
Maximum received signal at 0.1% BER with DH5		-20.0	>10		≥-20	dBm
Maximum received signal at 0.1% BER with EDR2, PI/4 DQPSK		-20.0	>0		≥-20	dBm
Maximum received signal at 0.1% BER with EDR3, 8DPSK		-20.0	>0		≥-20	dBm
C/I co-channel [a]			6	11	≤ 11	dB
Adjacent channel selectivity C/I f = f_0 + 1MHz [a]			-5	0	≤ 0	dB
Adjacent channel selectivity C/I f = f_0 - 1MHz [a]			-5	0	≤ 0	dB
Adjacent channel selectivity C/I f ≥ f_0 + 2MHz [a]			-38	-30	≤ -30	dB
Adjacent channel selectivity C/I f ≤ f_0 - 2MHz [a]			-22	-20	≤ -20	dB
Adjacent channel selectivity C/I f ≥ f_0 + 3MHz [a]			-42	-40	≤ -40	dB
Adjacent channel selectivity C/I f ≤ f_0 - 5MHz [a]			-45	-40	≤ -40	dB
Adjacent channel selectivity C/I f = f_{image} [a]			-15	-9	≤ -9	dB

Notes: a) For BER less than 0.1%. Applies according to BT Test Specification Ver. 1.2/2.0/2.0 + EDR only for Tamb = 20°C

Vcc = 3.0V to 3.6V, Tamb = - 40°C to +85°C

Transmitter	Frequency [GHz]	Limit			BT	Unit
		Min	Typ	Max	Spec	
RF transmit power 50 Ω load, at antenna Class 1 device GFSK [b)]	2.402	2.3	3.9	3,9		dBm
	2.441	2.5	3.2	4,0		
	2.480	2.2	3.0	4,0		
RF transmit power 50 Ω load, at antenna Class 1 device EDR2, PI/4 DQPSK [b)]	2.402 GFSK		2.7			dBm
	2.402 DPSK		1.6			
	2.441 GFSK		2.7			
	2.441 DPSK		1.6			
	2.480 GFSK		2.2			
	2.480 DPSK		1.0			
RF transmit power 50 Ω load, at antenna Class 1 device EDR3, 8DPSK [b)]	2.402 GFSK		2.6			dBm
	2.402 DPSK		1.6			
	2.441 GFSK		2.7			
	2.441 DPSK		1.6			
	2.480 GFSK		2.2			
	2.480 DPSK		1.0			
RF power control range		-21	-	4.0		dB
RF power range control resolution		3.2	3.9	4.6	2 to 8	dB
20 dB bandwidth for modulated carrier		920	924	1000	≤1000	kHz
Initial carrier frequency tolerance		-75	-10	75	≤ ±75	kHz
Carrier frequency drift (packet DH1)		-25	11	25	≤ ±25	kHz
Drift Rate		-20	0	20	20	kHz/ 50µs
$\Delta f1_{avg}$ “Maximum Modulation”		140	166	175	≥140 to ≤175	kHz
$\Delta f2_{avg}$ “Minimum Modulation”		115	155	-	≥ 115	kHz

Notes: b) excluding +2dBi antenna gain

6 Mechanical Characteristics

6.1 Dimensions

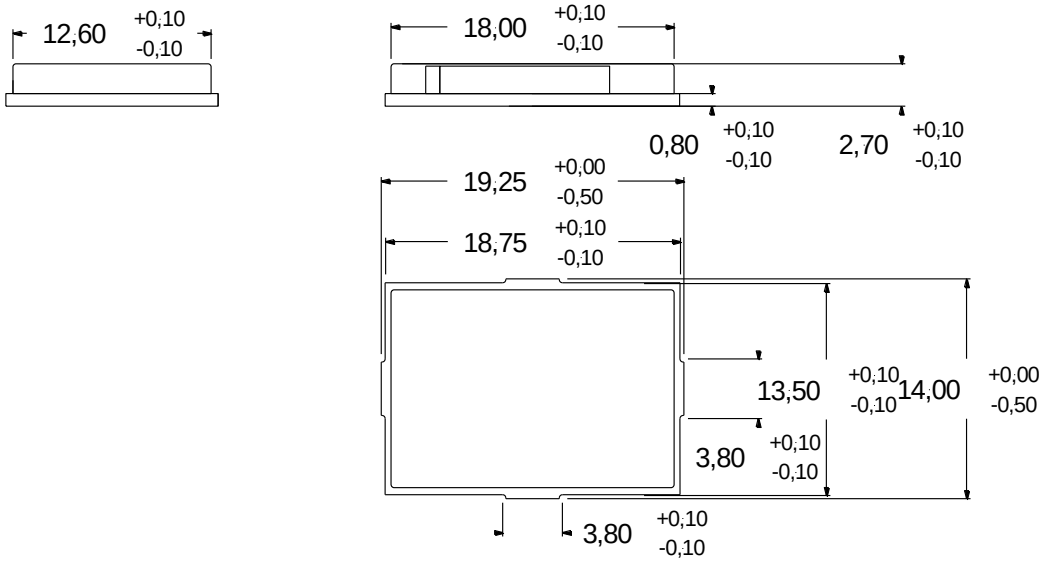


Figure 6.1 BlueMod+P24/G2 dimensions (not in scale, dimension in mm)

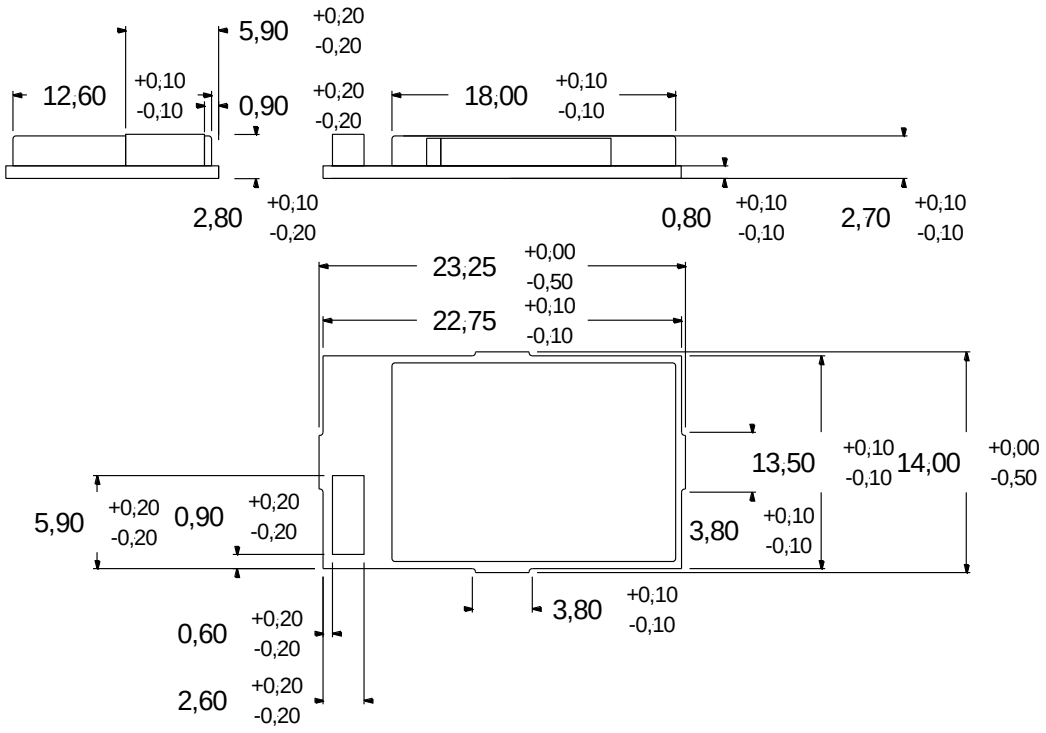


Figure 6.2 BlueMod+P25/G2 dimensions (not in scale, dimension in mm)

6.2 Recommended Land pattern

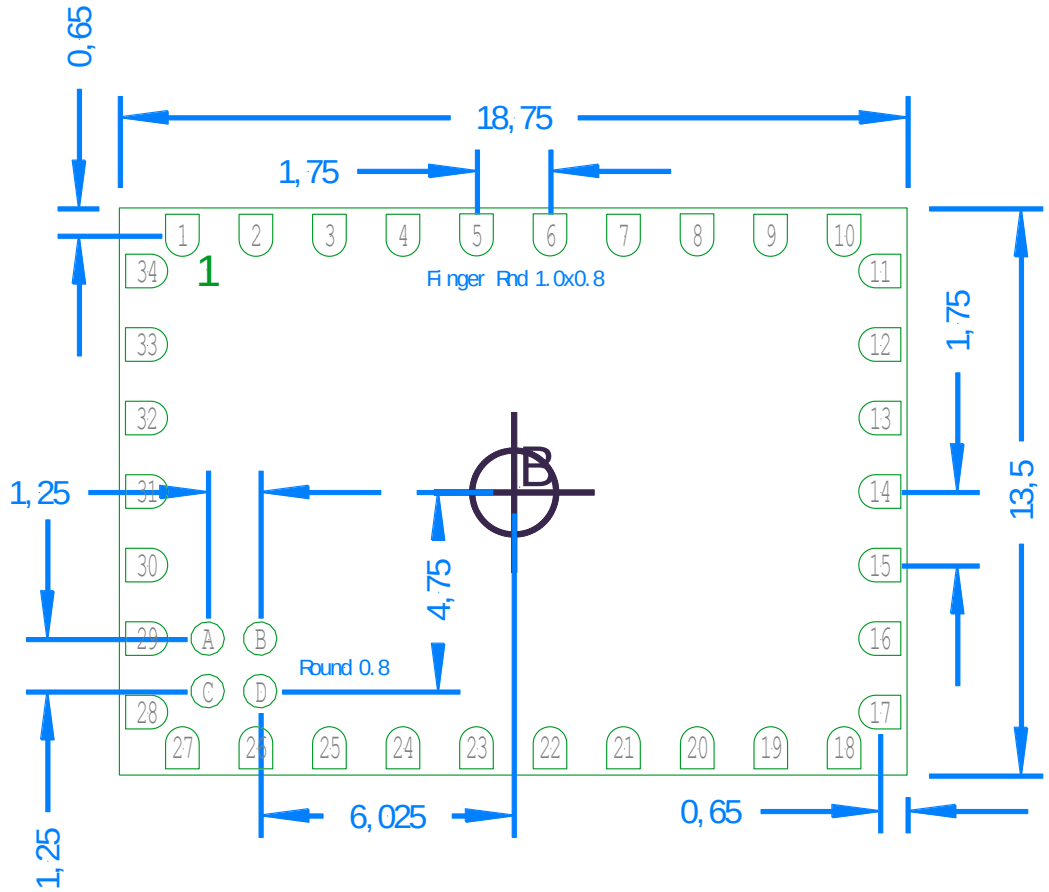


Figure 6.3 BlueMod+P24/G2 Recommended Land pattern

(Top-View, not in scale, dimension in mm)

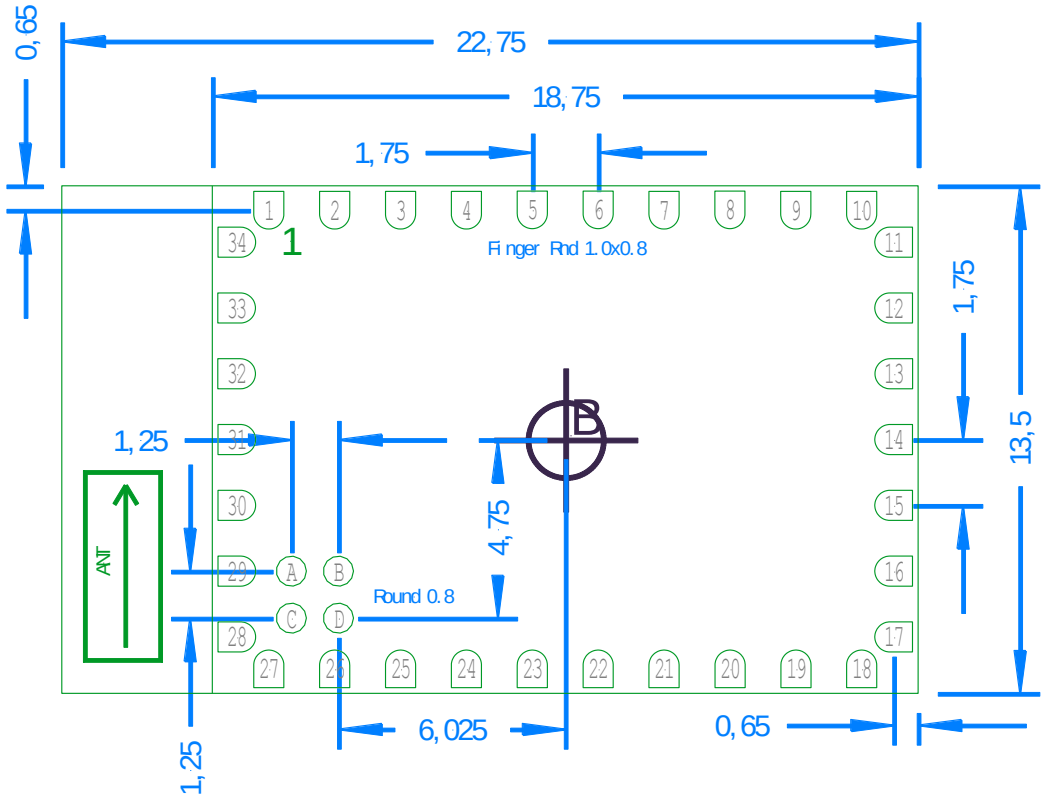


Figure 6.4 BlueMod+P25/G2 Recommended Land pattern

(Top-View, not in scale, dimension in mm)

6.3 Housing Guidelines

The individual case must be checked to decide whether a specific housing is suitable for the use of the internal antenna. A plastic housing must at least fulfill the following requirements:

- Non-conductive material, non-RF-blocking plastics
- No metallic coating
- ABS is suggested

6.4 Antenna Issues

BlueMod+P25/G2 comprises a ceramic antenna which as a component is soldered to the circuit board. This is functional for a BlueMod+P25/G2 integrated into a plastic housing. No additional antenna is required.

For an external antenna to be set in, e.g. because the BlueMod+P24/G2 is integrated into a metal housing, the ceramic antenna is replaced.

BlueMod+P24/G2 routes the antenna signal to pin 2.

The gain of the external antenna shall not exceed +2dB$_i$.

When using an external antenna the antenna is fixed and cannot be removed or replaced by the end user. The performance of the internal antenna respectively the external antenna has in any case to be checked within the final integration environment. Adjacent PCBs, components, cables, housings etc. could otherwise influence the radiation pattern or be influenced by the radio wave energy.

It must be ensured that the antenna is not co-located or operating in conjunction with any other antennas, transmitters, cables or connectors. When the internal ceramic antenna is used, certain restrictions are to be considered.

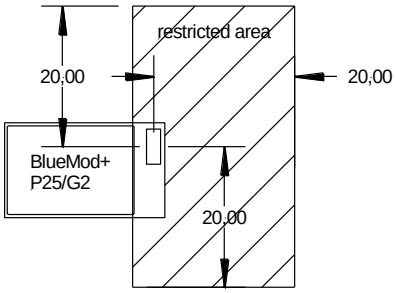


Figure 6.5 BlueMod+P25/G2 Recommended Restricted Area(not in scale, dimension in mm)

To give an optimized antenna performance the restricted area having no ground or power planes, traces or parts should be widened. The following dimensions should be implemented, depending on your possible space.

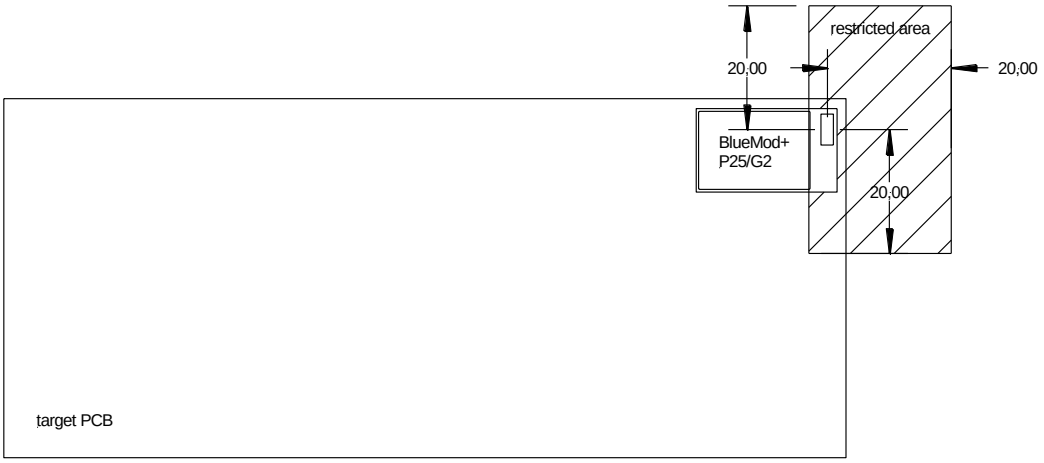


Figure 6.6 Optimal placement (not in scale, dimension in mm)

The best position to place the BlueMod+P25/G2 on the target PCB is in the upper right corner. This position is optimal concerning antenna interference; radiation pattern and PCB space that has to be keep free for the restricted area.

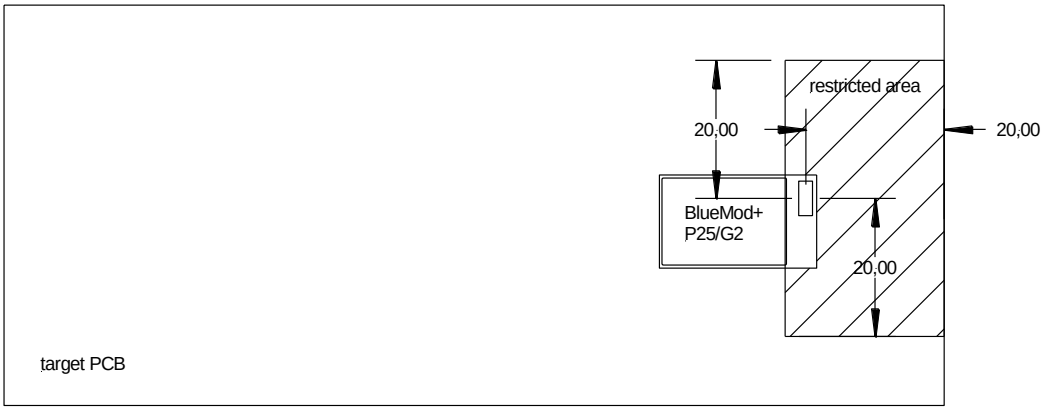


Figure 6.7 Recommendable placement (not in scale, dimension in mm)

When placing the BlueMod+P25/G2 at the right edge of the PCB ensure that the restricted area on the target PCB is free of planes, traces and parts.

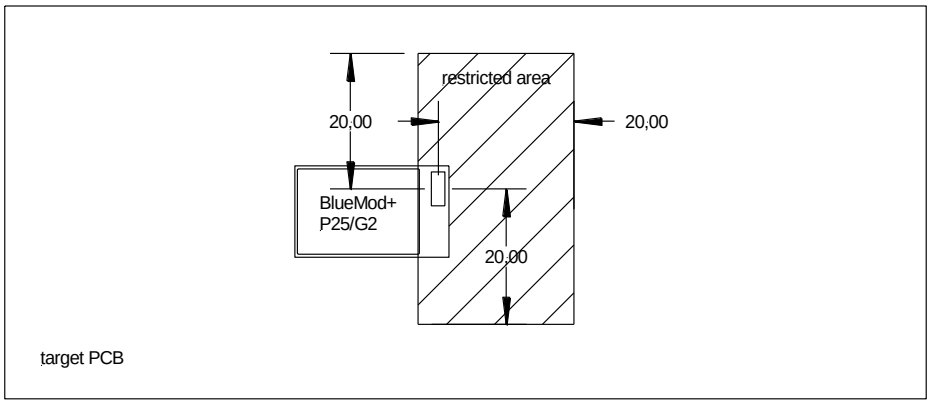


Figure 6.8 Acceptable, but not optimal placement (not in scale, dimension in mm)

When placing the BlueMod+P25/G2 on other positions than the right side the complete restricted area should be kept free of planes, traces and parts.

6.5 Safety Guidelines

According to SAR regulation EN 50371-2002 the BlueMod+P24/G2 and BlueMod+P25/G2 are not intended to be used in close proximity to the human body. Please refer to above-mentioned regulation for more specific information.

In respect to the safety regulation EN 60950-1: 2001 all conductive parts of the BlueMod+P24/G2 and BlueMod+P25/G2 are to be classified as SELV circuitry. OEM's implementing the modules in their products should follow the isolation rules given in regulation EN60950-1: 2001.

The PCB material of the BlueMod+P24/G2 and BlueMod+P25/G2 are classified UL-94V0.

6.6 Re-flow Temperature / Time Profile

The data here is given only for guidance on solder and has to be adopted to your process and other re-flow parameters for example the used solder paste. The paste manufacturer provides a re-flow profile recommendation for his product.

For lead-free solder

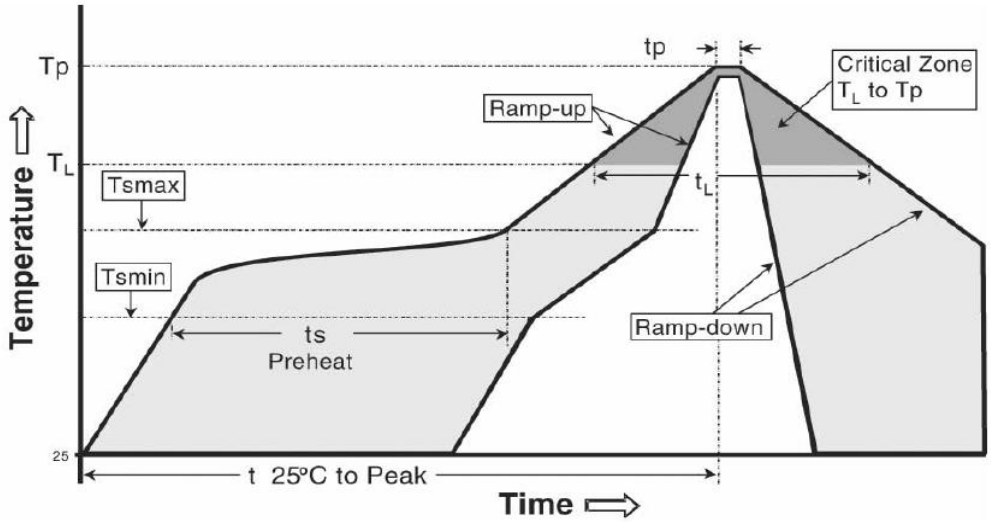


Figure 6.9 Soldering Temperature-Time Profile (for reflow soldering)

Preheat		**Main Heat**		**Peak**	
tsmax		tLmax		tpmax	
Temperature	Time	Temperature	Time	Temperature	Time
[°C]	[sec]	[°C]	[sec]	[°C]	[sec]
150	100	217	90	260	10
		230	50		
Average ramp-up rate		[°C / sec]	3		
Average ramp-down rate		[°C / sec]	6		
Max. Time 25°C to Peak Temperature		[min.]	8		

Opposite side re-flow is prohibited due to module weight.

Devices will withstand the specified profile and will withstand up to 1 lead-free re-flows to a maximum temperature of 260°C.

7 Approvals/Certifications

7.1 R&TTE Declaration of conformity

Konformitätserklärung gemäß dem Gesetz über Funkanlagen und Telekommunikationsendeinrichtungen (FTEG) und der Richtlinie 1999/5/EG (R&TTE)

Declaration of Conformity in accordance with Radio and Telecommunications Terminal Equipment Act (FTEG) and Directive 1999/5/EC (R&TTE Directive)

Stollmann Entwicklungs- und Vertriebs-GmbH, Mendelssohnstr. 15d, D-22761 Hamburg,
Jens Jensen
Hersteller/Verantwortliche Person // The manufacturer / responsible person

erklärt, daß die Produktfamilie
declares that the product family

BlueMod+P24/G2 (with external antenna maximal gain +2dBi)
BlueMod+P25/G2 (with internal Chip antenna)

Telekommunikationendeinrichtung mit Verwendungszweck: Bluetooth Modul
Telecommunications terminal equipment with intended purpose: Bluetooth Module

bei bestimmungsgemäßer Verwendung den grundlegenden Anforderungen des §3 und den übrigen einschlägigen Bestimmungen des FTEG (Artikel 3 der R&TTE) entspricht.
complies with the essential requirements of §3 and the other relevant provisions of the FTEG (Article 3 of the R&TTE Directive), when used for its intended purpose

Gesundheit und Sicherheit gemäß §3(1)1.(Artikel 3 (1) a))
Health and safety requirements pursuant to §3(1)1.(Article 3(1)a))
Ausgangsleistung ist kleiner 20mW
Output Power is lower than 20mW
angewendete harmonisierte Normen
harmonised standards applied
EN 60 950-1: 2006/A1:2010
EN 50 371: 2002

Wenn der bestimmungsgemäße Betrieb des Endgerätes in das dieses Bluetooth Modul eingebaut wird vorsieht, dass dieses Endgerät am menschlichen Körper getragen wird, ist eine SAR Betrachtung dieses Endgerätes durchzuführen.
If the intended use of the OEM product integrating this Bluetooth Module allows to wear the OEM product on the human body, a SAR evaluation of the OEM product has to be conducted.

Schutzanforderungen in Bezug auf die elektromagnetische Verträglichkeit §3(1)2, Artikel 3(1)b))
Protection requirements concerning electromagnetic compatibility §3(1)(2), (Article 3(1)b))

angewendete harmonisierte Normen
harmonised standards applied
EN 301 489-1 V1.8.1 (tested mounted on EVA Board 52495V03)
EN 301 489-17 V2.1.1 (tested mounted on EVA Board 52495V03)
EN 300 328 V1.7.1
Elektromagnetische Verträglichkeit in Bezug auf das Radio Frequenz Spektrum
Electromagnetic compatibility and Radio Spectrum Matters (ERM)

Ort, Datum Place & date of issue	Firmenstempel Firm stamp	Name, Unterschrift Name and signature
Hamburg, den 06.07.2011 i.A.	Stollmann Entwicklungs- und Vertriebs-GmbH Mendelssohnstr. 15d 22761 Hamburg	

7.2 Conformity for Japan

CETECOM ICT Services GmbH
Untertürkheimer Strasse 6-10, D-66117 Saarbrücken, Germany

Conformity Assessment Body
Recognized Certification Body for Japan (CAB ID: 202)

認証書 TYPE- BASED CERTIFICATE

特定無線設備の種類 Classification of specified radio equipment:	特定無線設備の技術基準適合証明等に関する規則 (Ordinance concerning Technical Regulations Conformity Certification etc. of Specified Radio Equipment) Article 2, Clause 1,Item 19 Low power data communications system in the 2.4GHz band - Bluetooth Radio component
電波の形式、周波数及び空中線電力 Type of emissions, frequency and antenna power	F1D/G1D 2400 -2483.5 MHz (2402MHz – 2480MHz 0.000 007 W/MHz
型式又は名称 Model Name:	BlueMod+P2x_G2;PAN1x55
製造者名 Manufacturer Name:	Stollmann E+V GmbH Mendelssohnstr. 15d D-22761 Hamburg / GERMANY
認証番号 Certified Number:	202WW11132491
認証をした年　月　日 Certified Date:	2011-06-08

上記のとおり、電波法第 38 条の 24 第 1 項の規定に基づく認証を行ったものであることを証する。
This is to certify that the above-mentioned Type certification has been granted in accordance with the provisions of Article 38-24, Paragraph 1 of the Radio Law.

Recognized by
The Ministry of Internal Affairs and Communications(MIC)
via

Bundesnetzagentur

BNetzA-CAB-03/25-51/3

Cetecom ICT Services GmbH
Lothar Spitzer

Signature:

CETECOM ICT Services GmbH
Untertürkheimer Strasse 6-10, D-66117 Saarbrücken, Germany

Conformity Assessment Body
Recognized Certification Body for Japan

認証書 TYPE- BASED CERTIFICATE

Applicant 認証を受けた者	**Stollmann E+V GmbH** **Mendelssohnstr. 15d** **D-22761 Hamburg / GERMANY**
Model Name 端末機器の名称	**BlueMod+P2x_G2;PAN1x55**
Development Equipment Name 展開機器名	
Type of Equipment 端末機器の種類	**Terminal equipment connected to digital data transmission facilities**
Certified Number 認証番号	**D 11-0009 202**
Certified Date 認証年月日	**2011-06-08**

This certificate is issued and valid based on consent to comply with the following conditions:
- This equipment was evaluated and found to conform with the technical compliance standards for connecting with a circuit. This evaluation is based upon usage specified in the application and it does not guarantee the quality or performance of the equipment in any other type of usage.
- The model name and certification label must be displayed on an external surface of the equipment.
- The direct current resistance of the equipment required at the time of installation is specified in the manual for usage or handling description.

端末機器の取扱については、下記事項を了承願います。
・本品は、申込書類に基づき、回線へ接続するための技術基準に適合しているかどうか及び当該設計に合致するものとなることができるかどうか審査したもので、機器の品質、性能を保証するものではありません。
・機器には、認定の表示、機器名を機器の外面の見易い箇所に容易に消えない方法で表示して下さい。
・本機器設置時に必要な機器の直流抵抗値等を取扱説明書等に明記して下さい。

This is to certify that the above mentioned equipment has been approved in accordance with the provisions of Article 56 of the Telecommunication Business Law.
上記の端末機器は、電気通信事業法第56条の規定に基づく端末機器の設定についての認証を行ったものであることを証する。

Recognized by
The Ministry of Internal Affairs and Communications(MIC)
via

Bundesnetzagentur

BNetzA-CAB-03/25-51/3

CETECOM ICT Services GmbH
Lothar Spitzer
Signature:

7.3 FCC Compliance

7.3.1 FCC Grant

TCB

GRANT OF EQUIPMENT AUTHORIZATION

Certification

Issued Under the Authority of the
Federal Communications Commission
By:

TCB

CETECOM ICT Services GmbH
Untertuerkheimer Strasse 6-10
D-66117 Saarbruecken,
Germany

Date of Grant: 04/16/2009

Application Dated: 04/16/2009

Panasonic Electronic Devices Europe GmbH
Zeppelinstrasse 19
Lueneburg, 21337
Germany

Attention: Heino Kaehler , Manager

NOT TRANSFERABLE

EQUIPMENT AUTHORIZATION is hereby issued to the named GRANTEE, and is VALID ONLY for the equipment identified hereon for use under the Commission's Rules and Regulations listed below.

FCC IDENTIFIER: T7V-BC06
Name of Grantee: Panasonic Electronic Devices Europe GmbH
Equipment Class: Part 15 Spread Spectrum Transmitter
Notes: Bluetooth Module
Modular Type: Single Modular

Grant Notes	FCC Rule Parts	Frequency Range (MHZ)	Output Watts	Frequency Tolerance	Emission Designator
	15C	2402.0 - 2480.0	0.00315		

Power output listed is conducted.

7.3.2 FCC Notice

The Bluetooth modules BlueMod+P25/G2, including the ceramic antenna and also the BlueMod+P24/G2, used with an external antenna as listed in chapter “Approved External Antenna List” complies with part 15 of the FCC rules. The Bluetooth modules meet the requirements for modular transmitter approval as detailed in

FCC public Notice DA00-1407.transmitter.

Notice:
Operation is subject to the following two conditions: (1) These Bluetooth modules may not cause harmful interference, and (2) must accept any interference received, including interference that may cause undesired operation.

7.3.3 Caution

Warning: Changes or modifications made to this equipment not expressly approved by Stollmann Entwicklungs und Vertriebs GmbH may void the FCC authorization to operate this equipment.

7.3.4 FCC Warning

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.

Consult the dealer or an experienced radio/TV technician for help.

7.3.5 Labeling Requirements

The Original Equipment Manufacturer (OEM) must ensure that FCC labeling requirements are met. This includes a clearly visible label on the outside of the OEM enclosure specifying the appropriate FCC identifier for this product as well as the FCC notice above. The FCC identifier is **FCC ID: T7V-BC06.** The FCC identifier is valid for both versions BlueMod+P24/G2 and BlueMod+P25/G2.

In any case the OEM end product must be labeled exterior with

Contains transmitter
FCC ID: T7V-BC06
IC: 216Q-BC06

7.3.6 Antenna Warning

The BlueMod+P24/G2 with the RF signal routed to a SMD pin has to be used with an external antenna as given in chapter “Approved External Antenna List”. This Bluetooth module has been tested with a U.FL connector and with the antennas listed in chapter “Approved External Antenna List”. When integrated in the OEMs product, these antennas require installation preventing end-users from replacing them with non-approved antennas. Any antenna not listed in chapter “Approved External Antenna List” must be tested to comply with FCC section 15.203 for unique antenna connectors and section 15.247 for emissions. This could be done together with the routine FCC EMC test of the OEMs product.

7.3.7 Approved External Antenna List

Item	Part Number	Manufacturer	Frequency Band	Type	Gain (dBi)
1	WIMO17010.10	Wimo	2.4GHz	ROD	+2
2					

7.3.8 RF Exposure Statement

To comply with FCC RF exposure requirements, the OEM must ensure that the internal antenna or an approved external antenna as listed in chapter “Approved External Antenna List” is installed.
The preceding statement must be included as a CAUTION statement in manuals for products operating with the approved antennas to alert users on FCC RF exposure compliance.
Any notification to the end user of installation or removal instructions about the integrated radio module is not allowed.
The radiated output power of the BlueMod+P25/G2 with internal ceramic antenna (FCC ID: T7V-BC06) is far below the FCC radio frequency exposure limits. Nevertheless, the BlueMod+P2x/G2 shall be used in such a manner that the potential for human contact during normal operation is minimized.
End users may not be provided with the module installation instructions. OEM integrators and end users must be provided with transmitter operating conditions for satisfying RF exposure compliance.

7.4 IC Industry Canada Certification

The Bluetooth modules BlueMod+P25/G2 and BlueMod+P24/G2 comply with the regulatory requirements of Industry Canada (IC), license **IC: 216Q-BC06**

Manufacturers of mobile, fixed or portable devices incorporating these modules are advised to clarify any regulatory questions and ensure compliance for SAR and/or RF exposure limits. OEMs can obtain Canadian information on RF exposure and compliance from http://www.ic.gc.ca/.

The Bluetooth modules have been designed to operate with the internal ceramic antenna or with external antennas listed in chapter “Approved External Antenna List”. Having a maximum gain of +2.0dBi. Antennas not included in this list or having a gain greater than +2.0dBi are strictly prohibited for use with these modules. The required antenna impedance is 50 Ohm. The antenna used for this transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

7.5 Bluetooth Qualification

BlueMod+P2x/G2 is a qualified design according to the Bluetooth Qualification Program Reference Document (PRD) V2.0. The Qualified Design IDs (QDID) are:

B015522 for the SPP and HID variants

B016761 for the HDP variant

For further information about marking requirements of your product attention should be paid the Bluetooth Product Marking Guide at https://programs.bluetooth.org/Download/Marking_Guide_20060601.pdf

According to the Bluetooth SIG rules (Qualification Program Reference Document – PRD V2.1) you are required to perform the mandatory End Product Listing (EPL) for your product. For further information see www.bluetooth.org or contact Stollmann.

7.6 RoHS Declaration

Declaration of environmental compatibility for supplied products:

Hereby we declare to our best present knowledge based on declaration of our suppliers that this product does not contain by now the following substances which are banned by directive 2002/95/EC (RoHS) except for applications exempted or if contain a maximum concentration of 0,1% by weight in homogeneous materials for

Lead and lead compounds

Mercury and mercury compounds

Chromium (VI)

PBB (polybrominated biphenyl) category

PBDE (polybrominated biphenyl ether) category

And a maximum concentration of 0,01% by weight in homogeneous materials for

Cadmium and cadmium compounds

8 Related Documents

[1] Stollmann: AppNote_B0601_Antenna_Design_V1_0.pdf

[2] BlueRS+ AT Command Reference r06

9 Packing

9.1 Embossed tape

Dimension of the tape

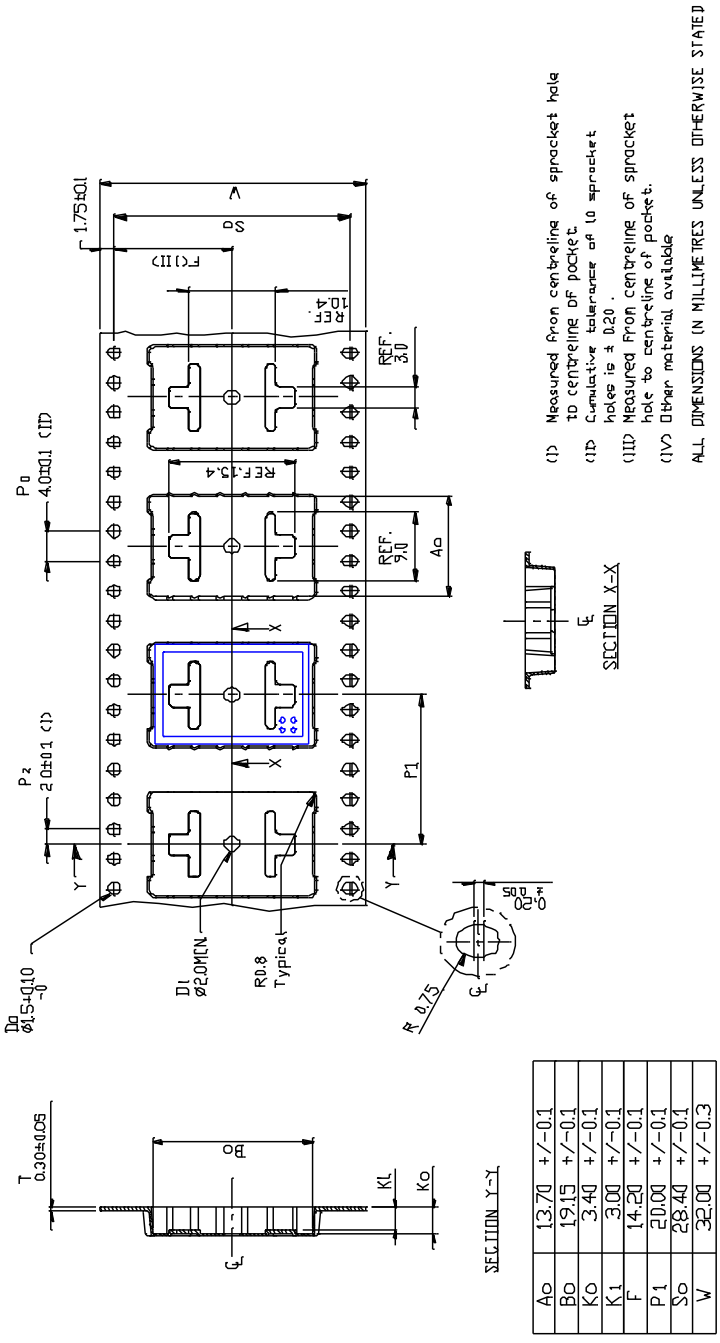


Figure 9.1 Tape Dimension

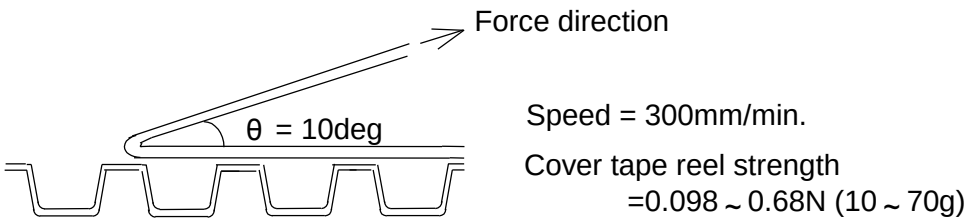


Figure 9.2 Force Direction

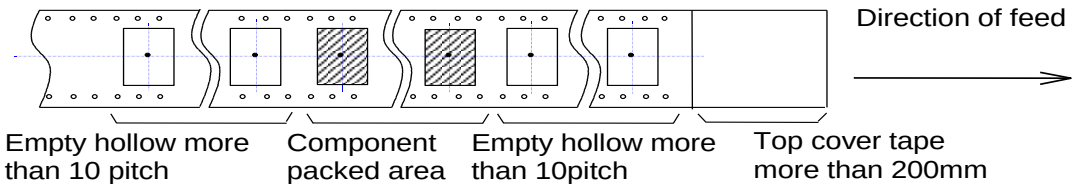


Figure 9.3 Empty hollow

Empty hollow in component packed area shall be less than two per reel and those hollows shall not be consecutive.

9.2 Component direction

Top cover tape shall not be found on reel holes and shall not stick out from reel.

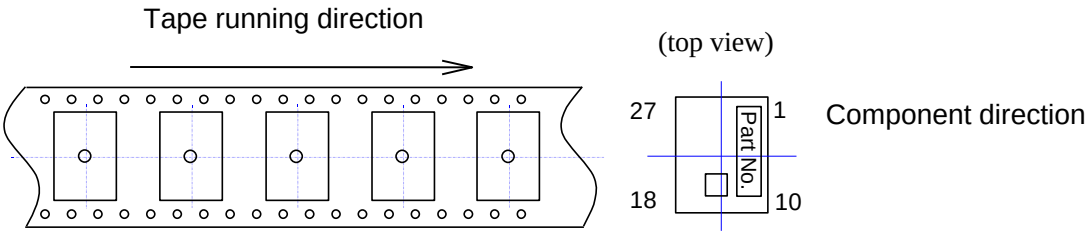


Figure 9.4 Component direction

9.3 Reel dimension

Package box: 1 or 2 reel (depends on quantity)

Marking: Customer's part No. / Quantity / Lot No. and our part# with bar-code shall be on the package box.

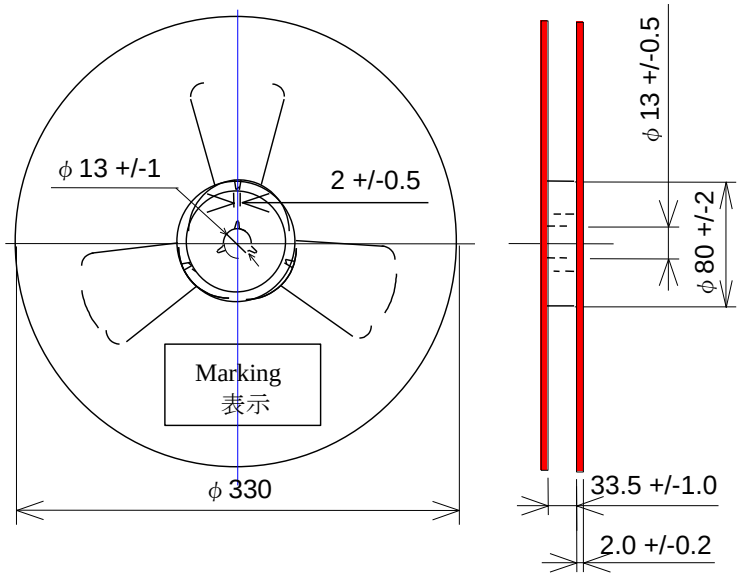


Figure 9.5 Reel Dimension

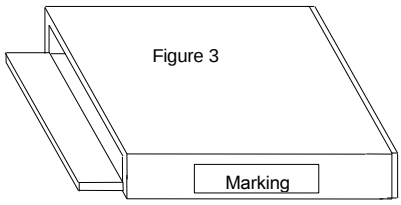


Figure 9.6 Marking Package Box

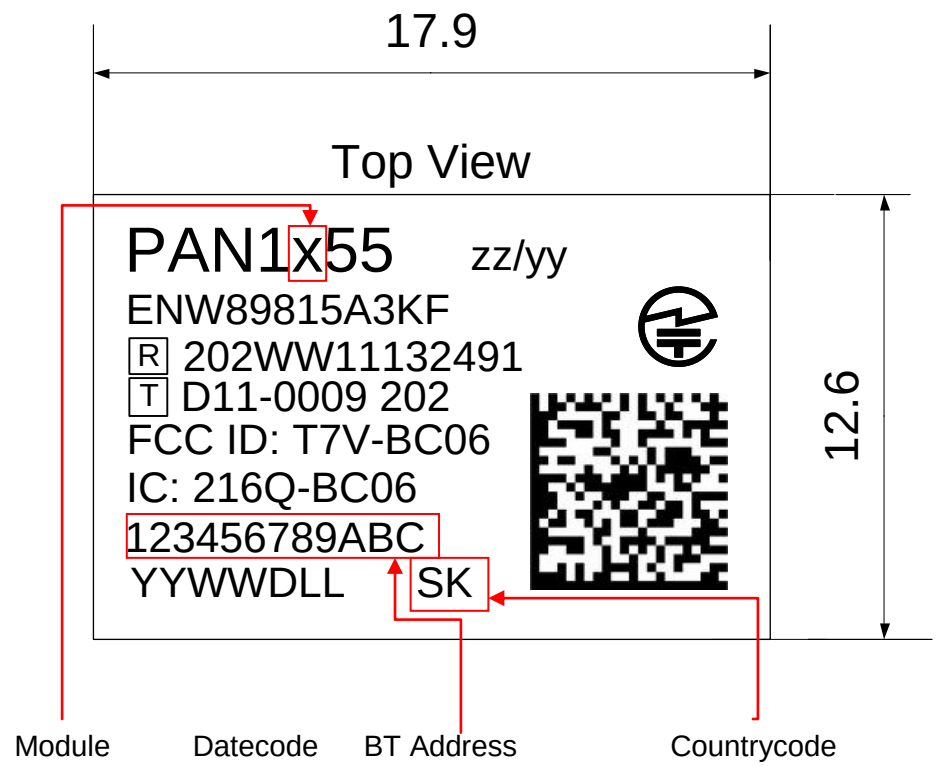
Quantity per reel: 500 pieces

Marking: Quantity / Lot No. and our part# with bar-code shall be on the reel (refer to figure 9.5).

10 Label Information

10.1 Module Label

The module is marked with the following label:



Field	Description
Module	4 = BlueMod+P24/G2, 5 =BlueMod+P25/G2
Datecode	Date of production, format YYMMDD
BT Address	Last 6 digits of Bluetooth device address
Countrycode	Country of production
yy	Software version
zz	Hardware version
YY	Year of production
WW	Week of production
D	Day of production
L	Lot number

The label is direct laser printed. To read the barcode of the label a special camera system (e.g. Cognex DataMan) is needed instead of standard 2D barcode scanners.

10.2 Package Label

The package box is marked with the following label:

Field	Description
name	Name of product
p/n	Product number
firmware	Firmware version
fw p/n	Product number of firmware
date	Date (YearCalendarWeek) YYWW
quantity	Number of contained modules
bundle	Bundle ID

If the label on the package box is different to the label described please contact Stollmann for detailed information.

11 Ordering Information

BlueMod+P2x/G2 is available in the following variants:

Name	Antenna	Part No.	Minimum Order Quantity:
BlueMod+P25/G2/SPP	Internal	52988-xx	50 units
BlueMod+P24/G2/SPP	External	52989-xx	50 units
BlueMod+P25/G2/HID-KB	Internal	53033-xx	50 units
BlueMod+P25/G2/HID-M	Internal	53106-xx	50 units
BlueMod+P25/G2/HDP	Internal	53075-xx	50 units
BlueMod+P24/G2/HDP	External	53118-xx	50 units

12 History

Version	Release Date	By	Change description
r01 draft	15.12.2008	aa	Start
	13.01.2008	JW	review and corrections
r02	19.05.2009	AA HB JJ JW	Complete rework, first release
r03	13.08.2010	AA	Chapter 6.1 Dimensions, max. dimensions update Chapter 7.3.1 FCC Grant Chapter 8 Related Documents Chapter 9 Packing
		JW	Added chapter 4.2.2.2 HDP PIN Configuration Added HDP variant Clarifications in chapter 5.6 Power consumption and power down modes Declaration of conformance updated MOQ increased
r04d01	25.08.2010	BG	Replaced product photo Added legend figure 2.1
	24.09.2010	HB	Added chapter 10 Label Information
r04d02	28.09.2010	BG	Added links to table of contents
r05	15.07.2011	MW	Added sample diagrams Fig. 3.1 to 3.13 Added chapter 3.1.1 Added chapter 3.5.2 Added chapter 3.14 Tables in 4.2 corrected Table in 5.4 corrected Minor corrections in many chapters Added application hints in chapter 3.6 GPIO interface Clarifications in chapter 3.11USB interface
		HB	Added label information in chapter 10.1 Declaration of conformance updated Added chapter 7.2 Conformity for Japan
r06	11.08.2011	HB	Chapter 3.1: Low voltage note removed Chapter 3.5.2: Pin names changed according 4.2.1 General Pin Descripton Chapter 4.2.1: UART_CTS# has external pullup Chapter 4.2.2.1 and 4.2.2.2 corrected application specific pin description Added chapter 5.6.1.5 Power consumption HDP

Stollmann Entwicklungs- und Vertriebs-GmbH
Mendelssohnstraße 15 D
22761 Hamburg
Germany

Phone: +49 (0)40 890 88-0
Fax: +49 (0)40 890 88-444
E-mail: info@stollmann.de
www.stollmann.de