

Circuit Description

- a. IC U1(AU647GL) is master control chip, master function include data exchange processing, indicate status, and provide data stream to U2.
- b. IC U2(FE1.1) is master control chip for uplink and downlink data transmission of the USB port, and to exchange data via U1 and other memory.
- c. Oscillator Y1, Y2 are 12MHz clock signal as base clock for U1, U2.
- d. Work voltage is DC 5V, provided by PC USB port.