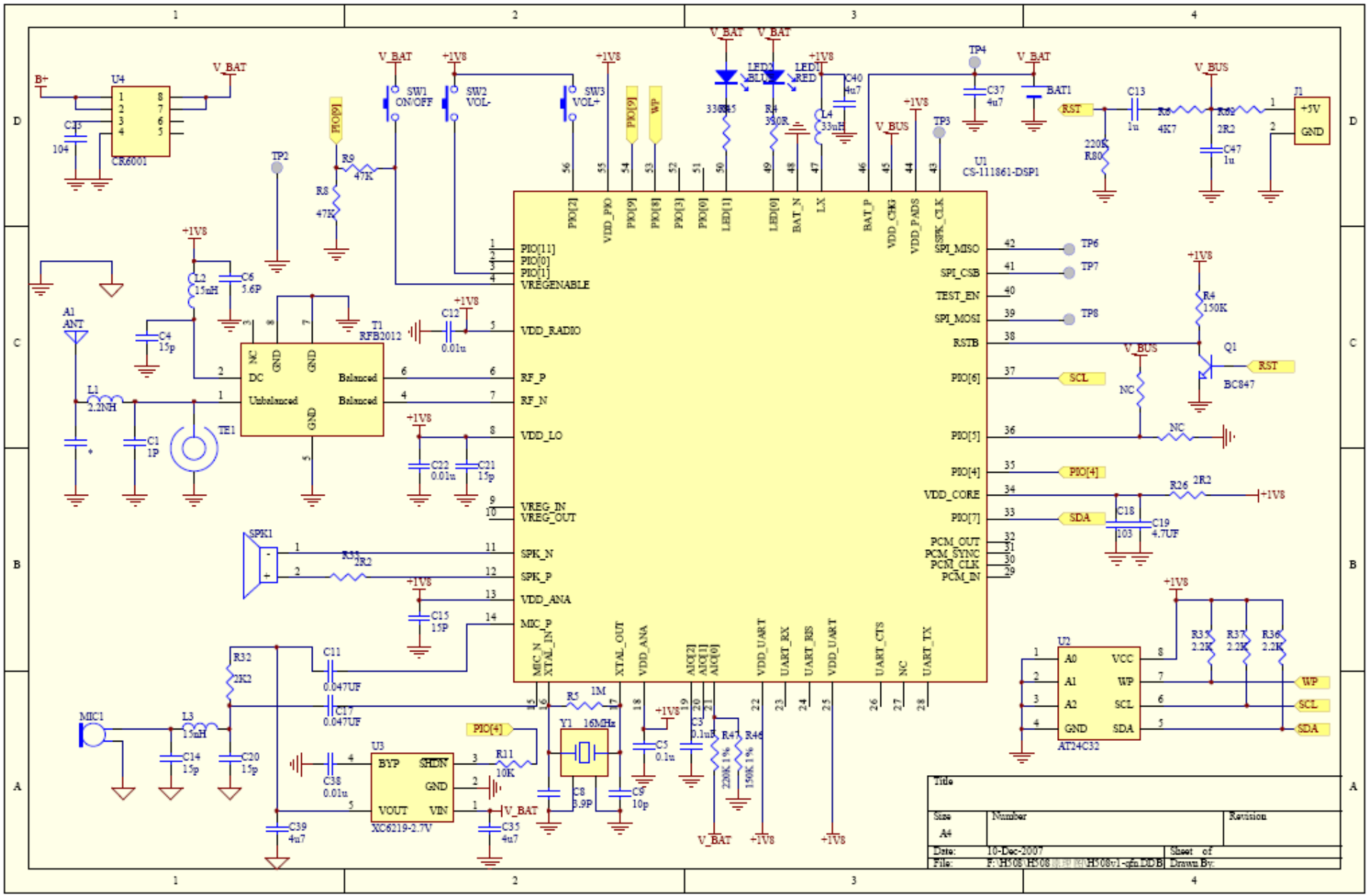

U4
CR6001
B+
V_BAT
C23
104
SW1
ON/OFF
SW2
VOL-
SW3
VOL+
+1V8
PIO[9]
WP
TP2
R9
47K
R8
47K
LED2
BLU
LED1
RED
R4
330R
L4
33uH
C40
4u7
TP4
C37
4u7
BAT1
TP3
V_BUS
RST
C13
1u
R6
4K7
R62
2R2
C47
1u
J1
+5V
GND
220K
R80
U1
CS-111861-DSP1
PIO[2]
VDD_PIO
PIO[9]
PIO[8]
PIO[3]
PIO[0]
LED[1]
LED[0]
BAT_N
LX
BAT_P
VDD_CHG
VDD_PADS
SPK_CLK
PIO[11]
PIO[0]
PIO[1]
VREGENABLE
VDD_RADIO
RF_P
RF_N
VDD_LO
VREG_IN
VREG_OUT
SPK_N
SPK_P
VDD_ANA
MIC_P
MIC_N
XTAL_IN
XTAL_OUT
VDD_ANA
AIO[2]
AIO[1]
AIO[0]
VDD_UART
UART_RX
UART_RIS
VDD_UART
UART_CTS
NC
UART_TX
SPI_MISO
SPI_CSB
TEST_EN
SPI_MOSI
RSTB
PIO[6]
PIO[5]
PIO[4]
VDD_CORE
PIO[7]
PCM_OUT
PCM_SYNC
PCM_CLK
PCM_IN
TP6
TP7
TP8
R4
150K
Q1
BC847
SCL
NC
PIO[4]
R26
2R2
SDA
C18
103
C19
4.7UF
A1
ANT
L2
15nH
C6
5.6P
C4
15p
L1
2.2NH
C1
1P
TE1
T1
RFB2012
DC
Unbalanced
Balanced
C12
0.01u
C22
0.01u
C21
15p
SPK1
R33
2R2
C15
15P
R32
2K2
C11
0.047UF
C17
0.047UF
MIC1
L3
15nH
C14
15p
C20
15p
U3
XC6219-2.7V
BYP
SHDN
GND
VOUT
VIN
C38
0.01u
C39
4u7
R11
10K
C35
4u7
R5
1M
Y1
16MHz
C8
3.9P
C9
10p
C5
0.1u
C3
0.1uF
R47
220K 1%
R46
150K 1%
U2
AT24C32
A0
A1
A2
GND
VCC
WP
SCL
SDA
R35
2.2K
R37
2.2K
R36
2.2K
Title
Size
A4
Number
Revision
Date:
10-Dec-2007
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File:
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Drawn By: