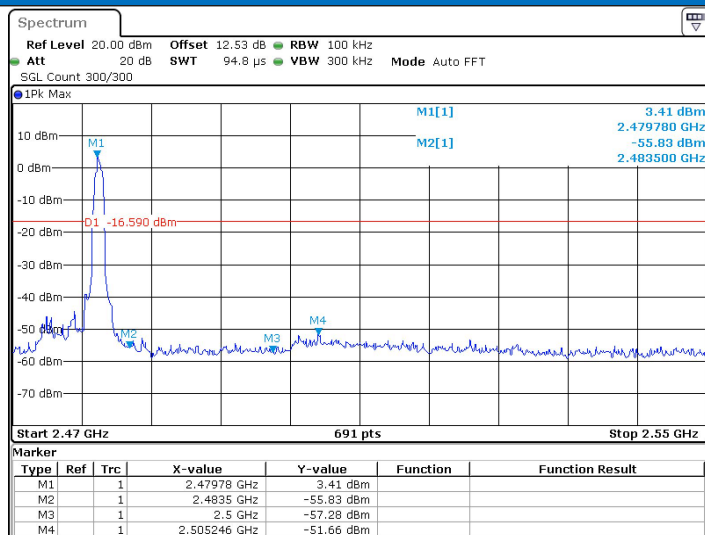
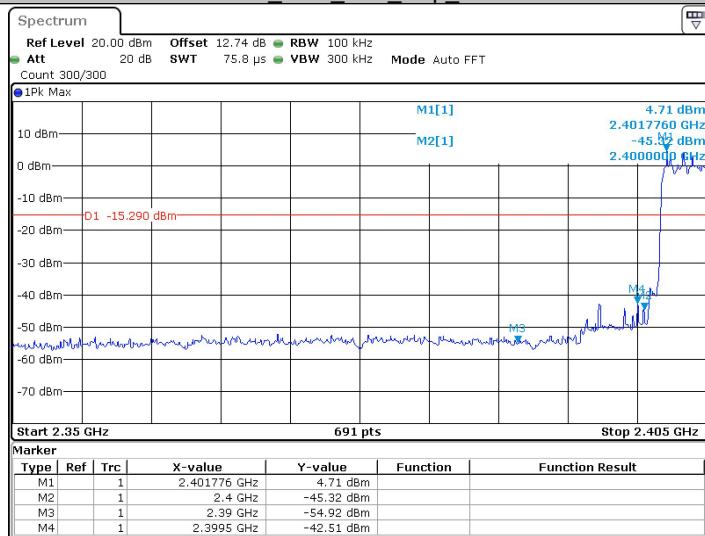




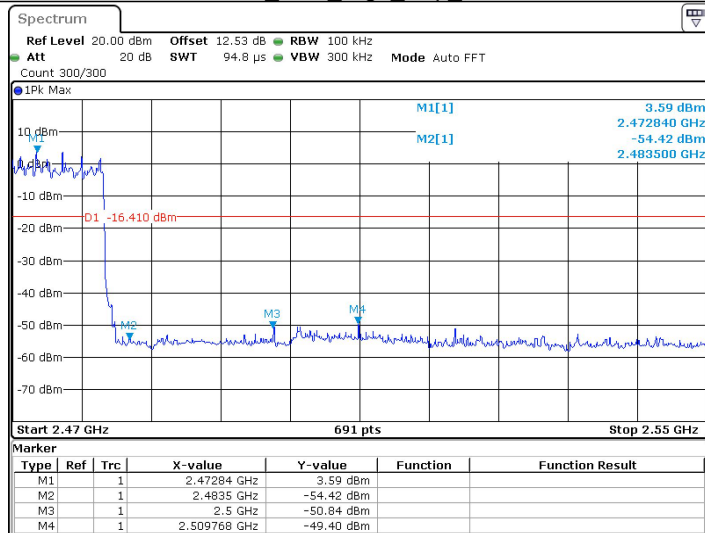
Date: 1.APR 2025 16:47:48

2DH5_Ant1_Low_Hop_2402



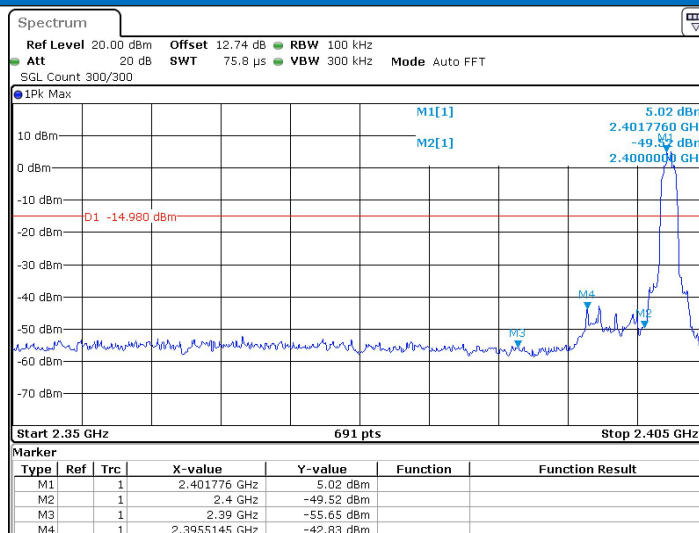
Date: 1.APR 2025 18:08:31

2DH5_Ant1_High_Hop_2480



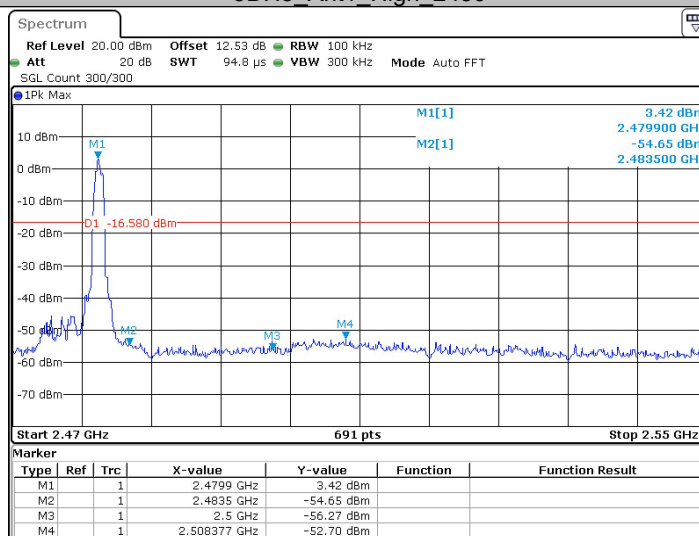
Date: 1.APR 2025 18:09:03

3DH5_Ant1_Low_2402



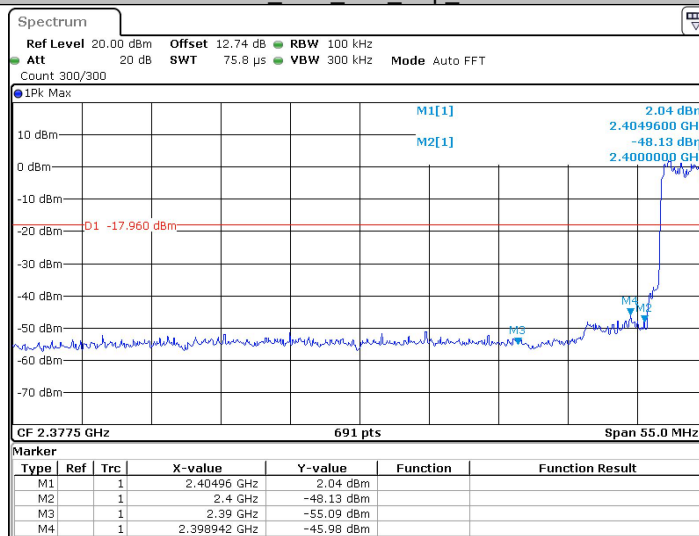
Date: 1.APR 2025 16:51:53

3DH5_Ant1_High_2480



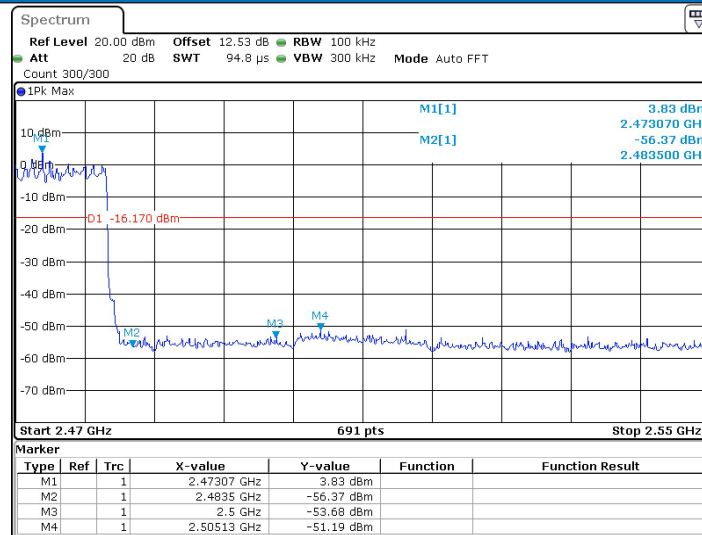
Date: 1.APR 2025 17:24:41

3DH5_Ant1_Low_Hop_2402



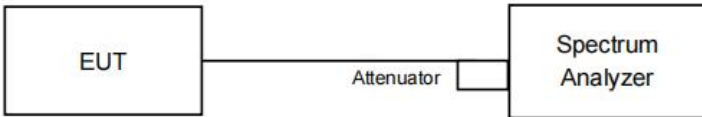
Date: 1.APR 2025 18:09:38

3DH5_Ant1_High_Hop_2480

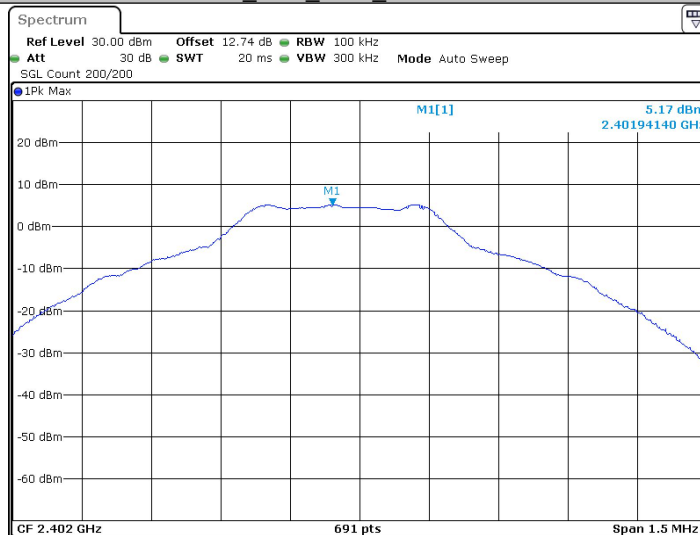


Date: 1.APR 2025 18:10:03

5.9 Spurious RF Conducted Emissions

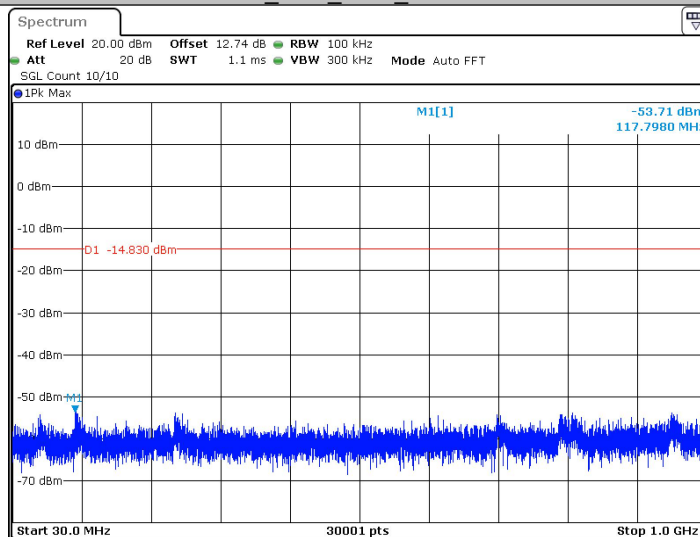
Test Requirement:	47 CFR Part 15C Section 15.247 (d)
Test Method:	ANSI C63.10:2013
Test Setup:	 Remark: Offset=cable loss+ attenuation factor.
Limit:	In any 100 kHz bandwidth outside the frequency band in which the spread spectrum intentional radiator is operating, the radio frequency power that is produced by the intentional radiator shall be at least 20 dB below that in the 100 kHz bandwidth within the band that contains the highest level of the desired power, based on either an RF conducted or a radiated measurement.
Exploratory Test Mode:	Non-hopping transmitting with all kind of modulation and all kind of data type
Final Test Mode:	Through Pre-scan, find the DH5 of data type is the worst case of GFSK modulation type, 2-DH5 of data type is the worst case of $\pi/4$ DQPSK modulation type, 3-DH5 of data type is the worst case of 8DPSK modulation type.
Test Results:	Pass

DH5_Ant1_2402_0~Reference



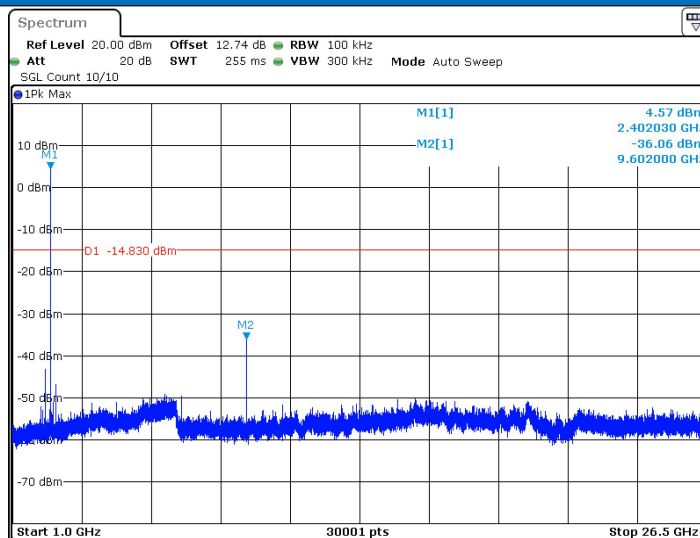
Date: 1 APR 2025 16:40:55

DH5_Ant1_2402_30~1000



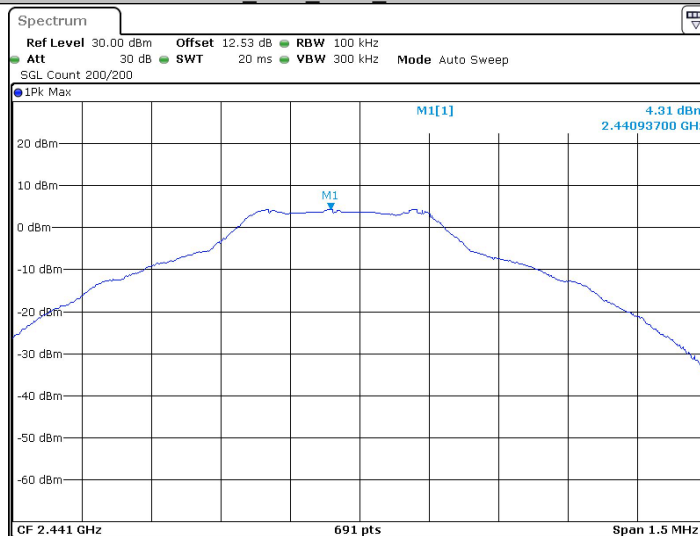
Date: 1 APR 2025 16:40:59

DH5_Ant1_2402_1000~26500



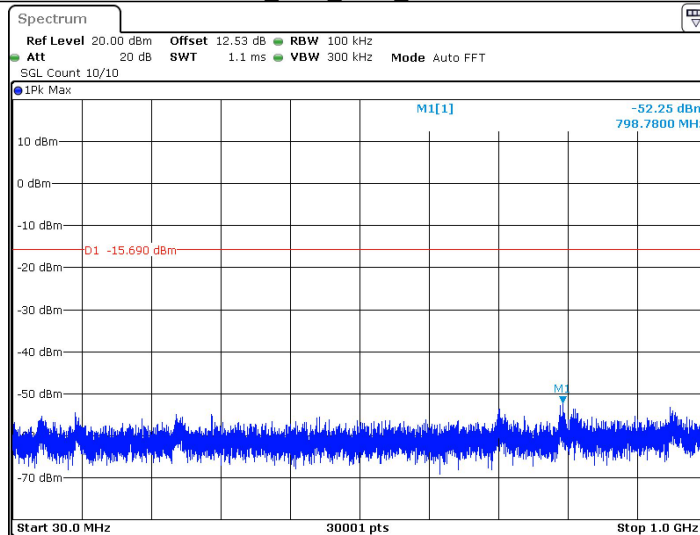
Date: 1 APR 2025 16:41:10

DH5_Ant1_2441_0~Reference



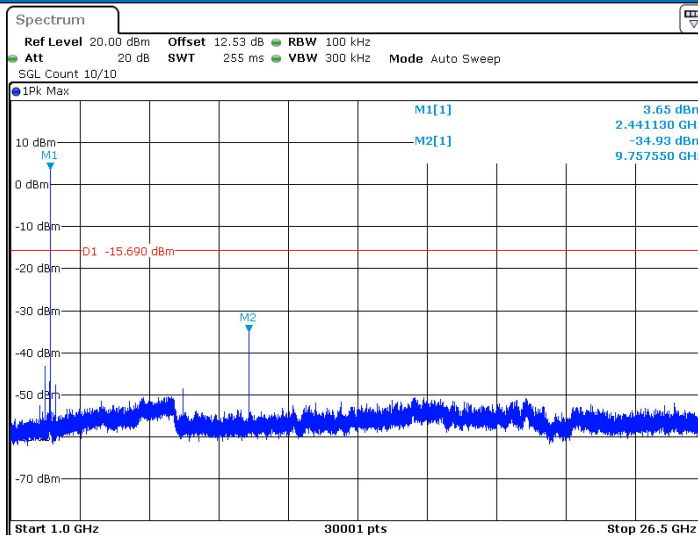
Date: 1 APR 2025 16:41:51

DH5_Ant1_2441_30~1000



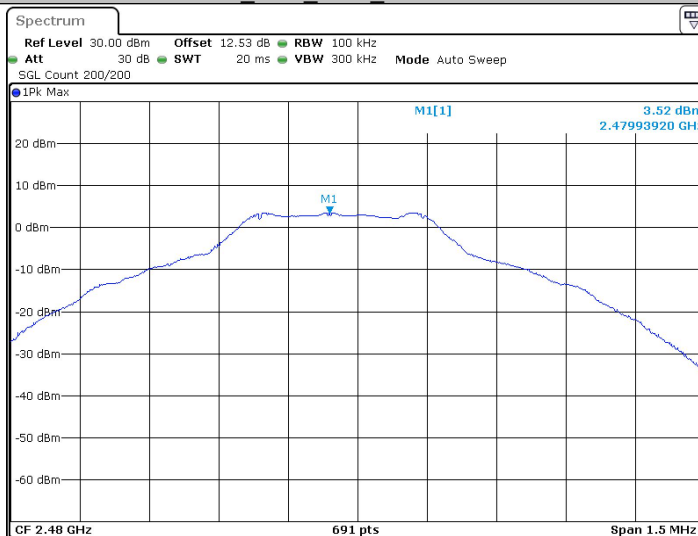
Date: 1 APR 2025 16:41:55

DH5_Ant1_2441_1000~26500



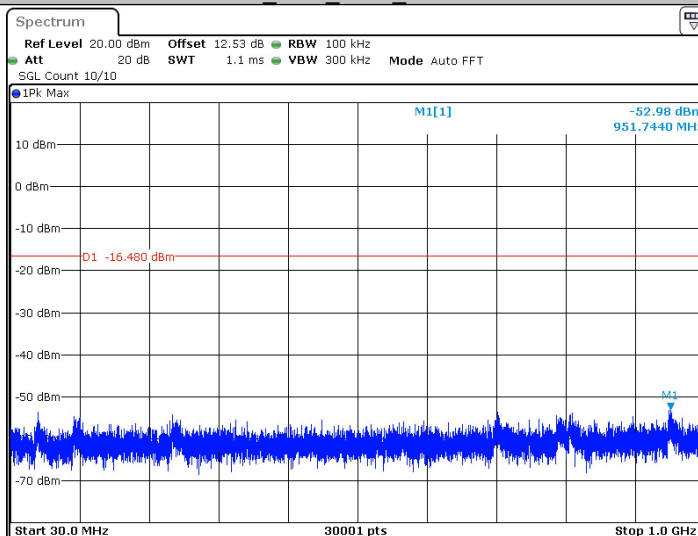
Date: 1 APR 2025 16:42:06

DH5_Ant1_2480_0~Reference



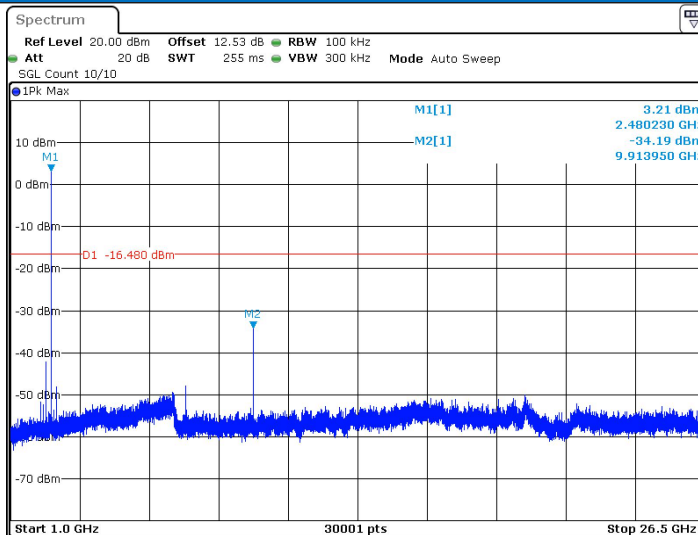
Date: 1 APR 2025 16:43:05

DH5_Ant1_2480_30~1000



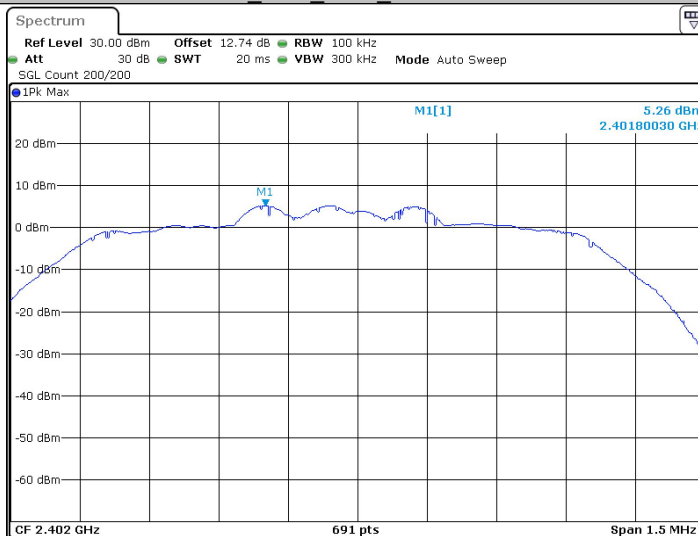
Date: 1 APR 2025 16:43:09

DH5_Ant1_2480_1000~26500



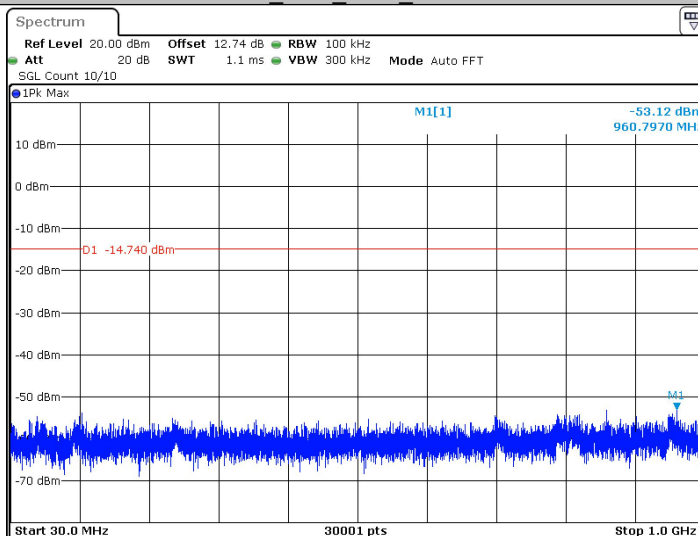
Date: 1 APR 2025 16:43:20

2DH5_Ant1_2402_0~Reference



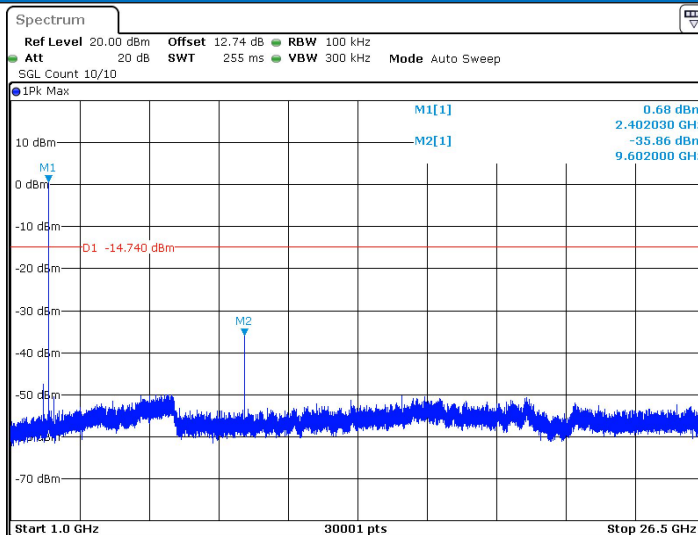
Date: 1 APR 2025 16:45:41

2DH5_Ant1_2402_30~1000



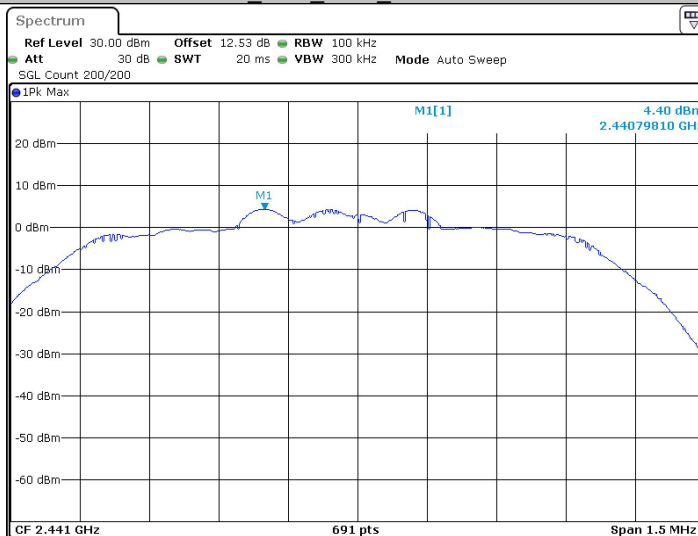
Date: 1 APR 2025 16:45:45

2DH5_Ant1_2402_1000~26500



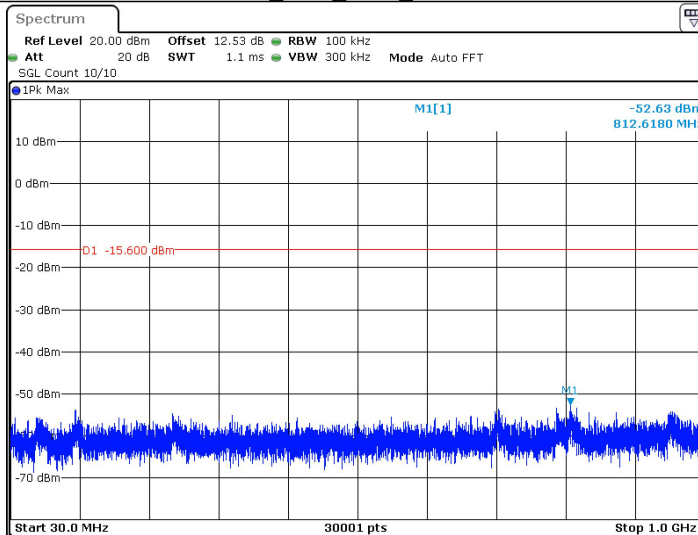
Date: 1 APR 2025 16:45:56

2DH5_Ant1_2441_0~Reference



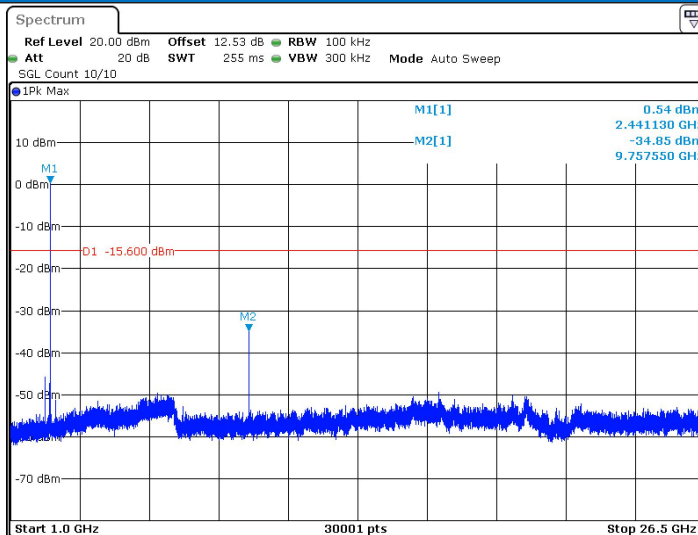
Date: 1 APR 2025 16:46:22

2DH5_Ant1_2441_30~1000



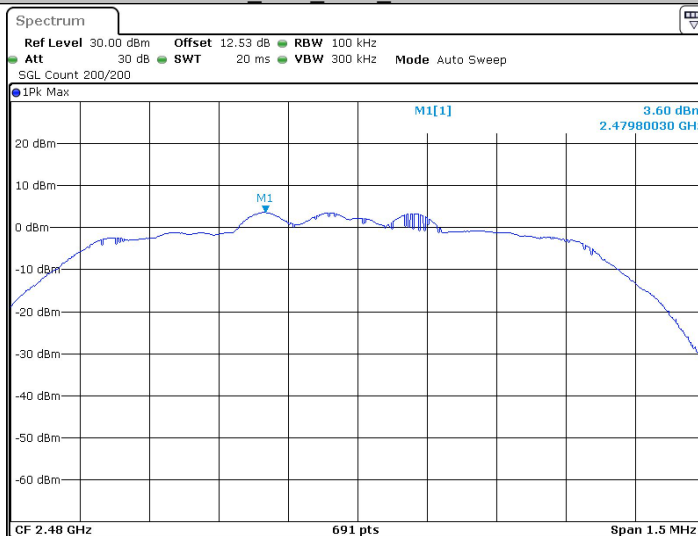
Date: 1 APR 2025 16:46:26

2DH5_Ant1_2441_1000~26500



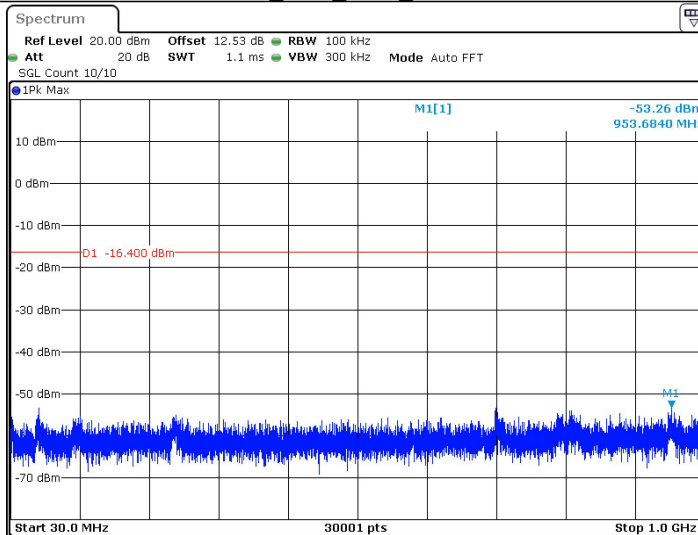
Date: 1 APR 2025 16:46:37

2DH5_Ant1_2480_0~Reference



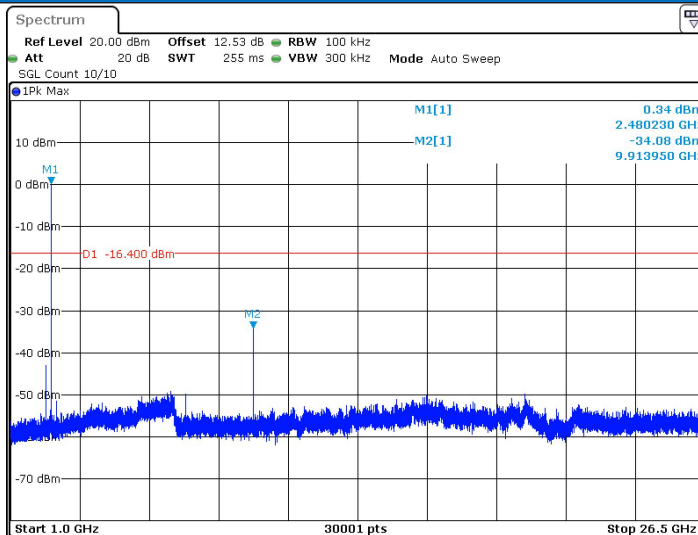
Date: 1 APR 2025 16:48:10

2DH5_Ant1_2480_30~1000



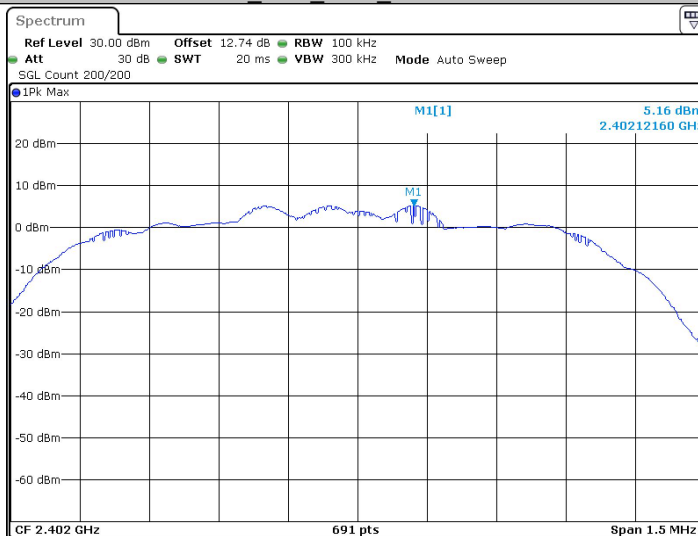
Date: 1 APR 2025 16:48:15

2DH5_Ant1_2480_1000~26500



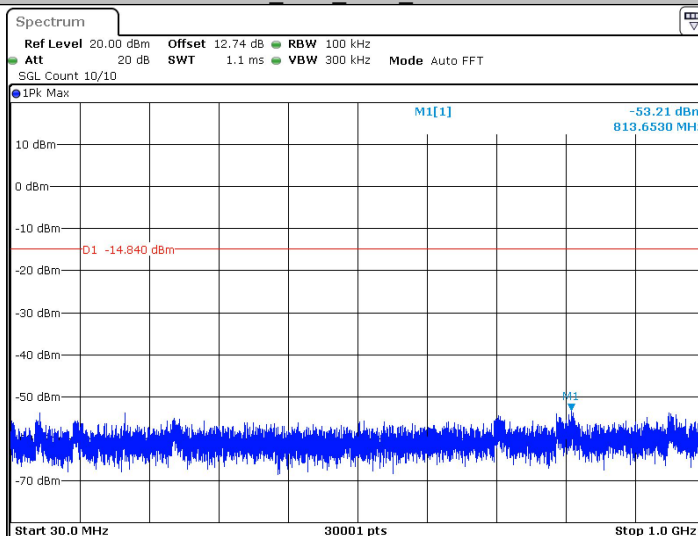
Date: 1 APR 2025 16:48:25

3DH5_Ant1_2402_0~Reference



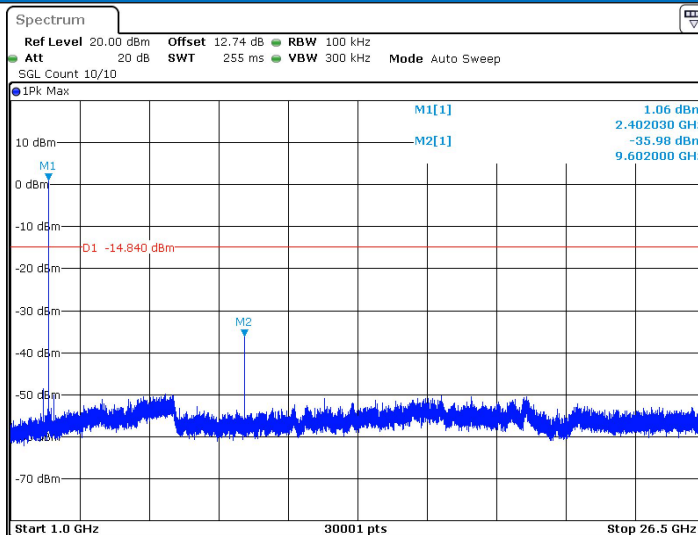
Date: 1 APR 2025 16:52:18

3DH5_Ant1_2402_30~1000



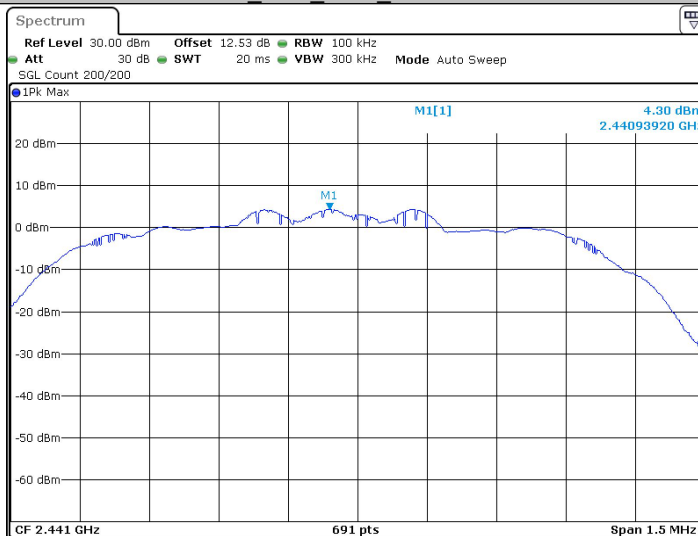
Date: 1 APR 2025 16:52:22

3DH5_Ant1_2402_1000~26500



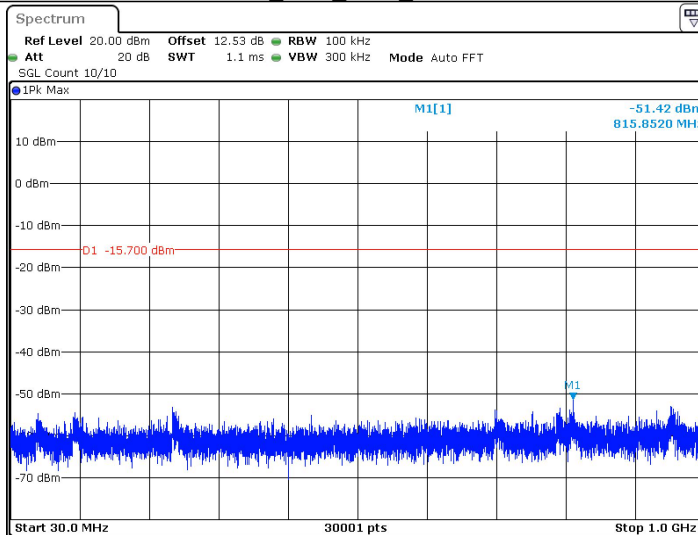
Date: 1 APR 2025 16:52:33

3DH5_Ant1_2441_0~Reference



Date: 1 APR 2025 16:53:36

3DH5_Ant1_2441_30~1000



Date: 1 APR 2025 16:53:40

3DH5_Ant1_2441_1000~26500