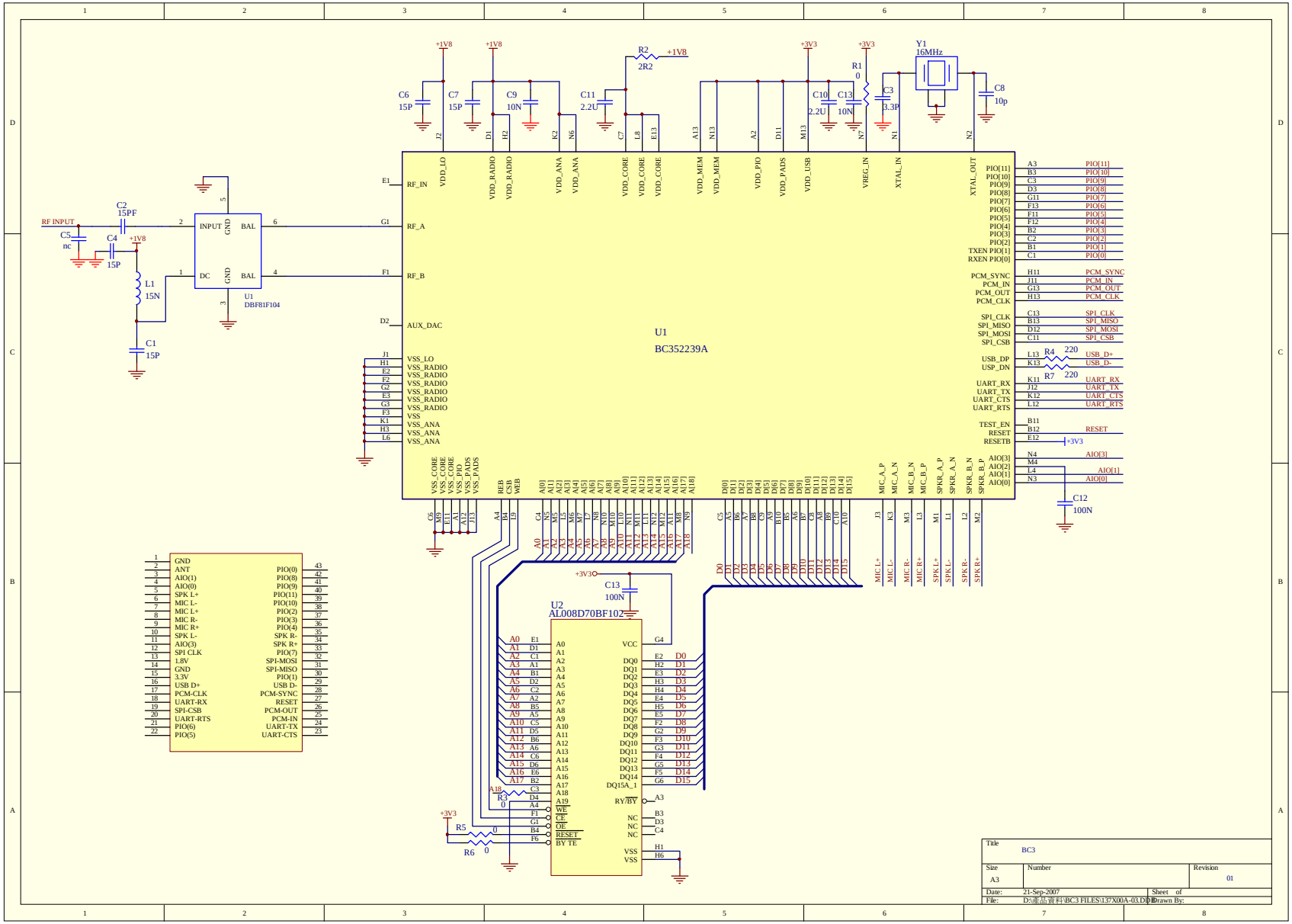
U1
BC352239A
U1
DBF81F104
U2
AL008D70BF102
Y1
16MHz
RF INPUT
INPUT
BAL
DC
GND
VDD_LO
VDD_RADIO
VDD_ANA
VDD_CORE
VDD_MEM
VDD_PIO
VDD_PADS
VDD_USB
VREG_IN
XTAL_IN
XTAL_OUT
RF_IN
RF_A
RF_B
AUX_DAC
VSS_LO
VSS_RADIO
VSS_ANA
VSS
VSS_CORE
VSS_PIO
VSS_PADS
REB
CSB
WEB
MIC_A_P
MIC_A_N
MIC_B_N
MIC_B_P
SPKR_A_P
SPKR_A_N
SPKR_B_N
SPKR_B_P
PIO[11]
PIO[10]
PIO[9]
PIO[8]
PIO[7]
PIO[6]
PIO[5]
PIO[4]
PIO[3]
PIO[2]
PIO[1]
PIO[0]
TXEN
RXEN
PCM_SYNC
PCM_IN
PCM_OUT
PCM_CLK
SPI_CLK
SPI_MISO
SPI_MOSI
SPI_CSB
USB_DP
USB_DN
UART_RX
UART_TX
UART_CTS
UART_RTS
TEST_EN
RESET
RESETB
AIO[3]
AIO[2]
AIO[1]
AIO[0]
GND
ANT
AIO(1)
AIO(0)
SPK L+
MIC L-
MIC L+
MIC R-
MIC R+
SPK L-
AIO(3)
SPI CLK
1.8V
3.3V
USB D+
PCM-CLK
UART-RX
SPI-CSB
UART-RTS
PIO(6)
PIO(5)
PIO(0)
PIO(8)
PIO(9)
PIO(11)
PIO(10)
PIO(2)
PIO(3)
PIO(4)
SPK R-
SPK R+
PIO(7)
SPI-MOSI
SPI-MISO
PIO(1)
USB D-
PCM-SYNC
RESET
PCM-OUT
PCM-IN
UART-TX
UART-CTS
WE
CE
OE
RESET
BY TE
RY/BY
VCC
VSS
NC
Title
BC3
Size
A3
Number
Revision
01
Date:
21-Sep-2007
Sheet of
File:
D:\產品資料\BC3 FILES\137X00A-03.DDB
Drawn By: