Circuit Description

The circuit system is inbuilt system. The clock chip can provide clock frequency (12 MHz) for MCU and PLL, and then they can bring respective clock frequency by the frequency-divider and frequency-multiplier. MCU chip can correspond, control and communicate with very module. PLL chip can bing high frequency signal through locking the VOC frequency to comply with transmission of wireless channel, and then the ASK key switch can bring ASK signal by MCU controlling so that high signal can load digital information. Power Amplifier can amplify ASK signal so that it can radiate to space, then the amplified signal will communicate tag in distance. It can bring echo signal after tag receiving signal, which was transmitted to the receiver by the antenna. The interrogator can bring low frequency signal with digital information through envelop detection, and the small signal amplifier can amplify the low frequency signal, after that the analog signals was changed into digital signals by A/D device. Then MCU can deal with the result according to the configured parameter of EEROM.