

Transmission circuit description

This transmitter consists of a fully integrated voltage-controlled oscillator (VCO), a divide-by-32 divider (div32), a phase-frequency detector (PFD) and a charge pump (CP). An internal loop filter determines the dynamic behavior of the PLL and suppresses reference spurious signals. A Colpitts crystal oscillator (XOSC) is used as the reference oscillator of a phase-locked loop (PLL) synthesizer. The VCO's output signal feeds the power amplifier (PA). The RF signal output (OUT) can be matched to an antenna

A CMOS compatible data stream applied at the pin FSKDTA digitally modulates the XOSC. The FSK modulation signal can be achieved by pulling the crystal oscillator frequency.