

FCC ID: VDSCB492007R

Technical Description :

The brief circuit description is listed as follows :

- Q1 and associated circuit act as 49.860 MHz Super-regenerative Receiver.
- PT8A978 acts as Decoder.
- Q2, Q3, Q6 – Q9 and associated circuit act as M1 Motor Driving Circuit.
- Q4, Q5, Q10 – Q13 and associated circuit act as M2 Motor Driving Circuit.
- ZD1 and associated circuit act as Voltage Regulator.

Antenna Used :

An integral antenna has been used.