

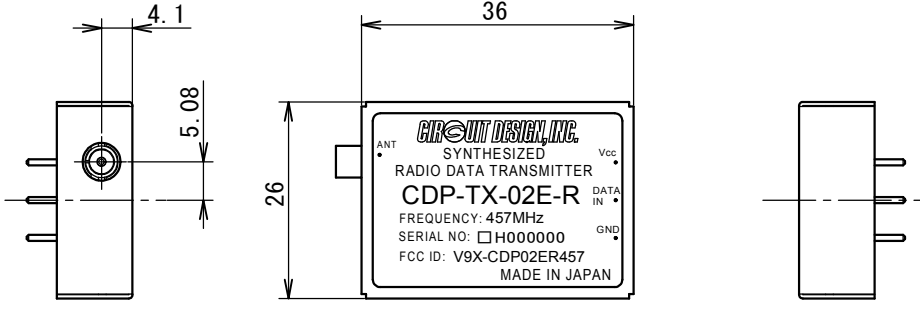
A		B		C		D	
COMMERCIAL TOLERANCE		MARK	DATE	MEMO		WR.	AP.
~	±	△ X					
~	±	△ X					
~	±	△ X					
~	±	△ X					

1

1

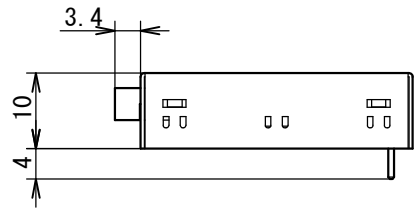
2

2



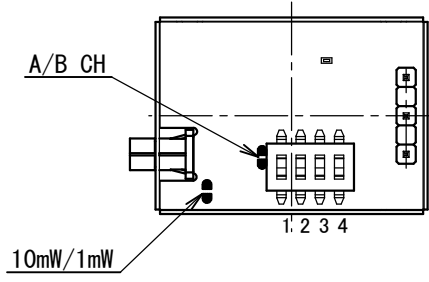
3

3



4

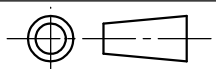
4



5

5

SCALE 1/1	DESIGN M. Tsunoda	CODE NO. _____	DATE 2008. 4.15.
MATERIAL	DRAWN R-ASADA	SPEC. FCC申請図	DRAWING NO. U044B02A03
DISPOSAL	CHECK	NAME CDP-TX-02E-R(457)外觀図	



APPROV.

CIRCUIT DESIGN, INC.

A

B

C