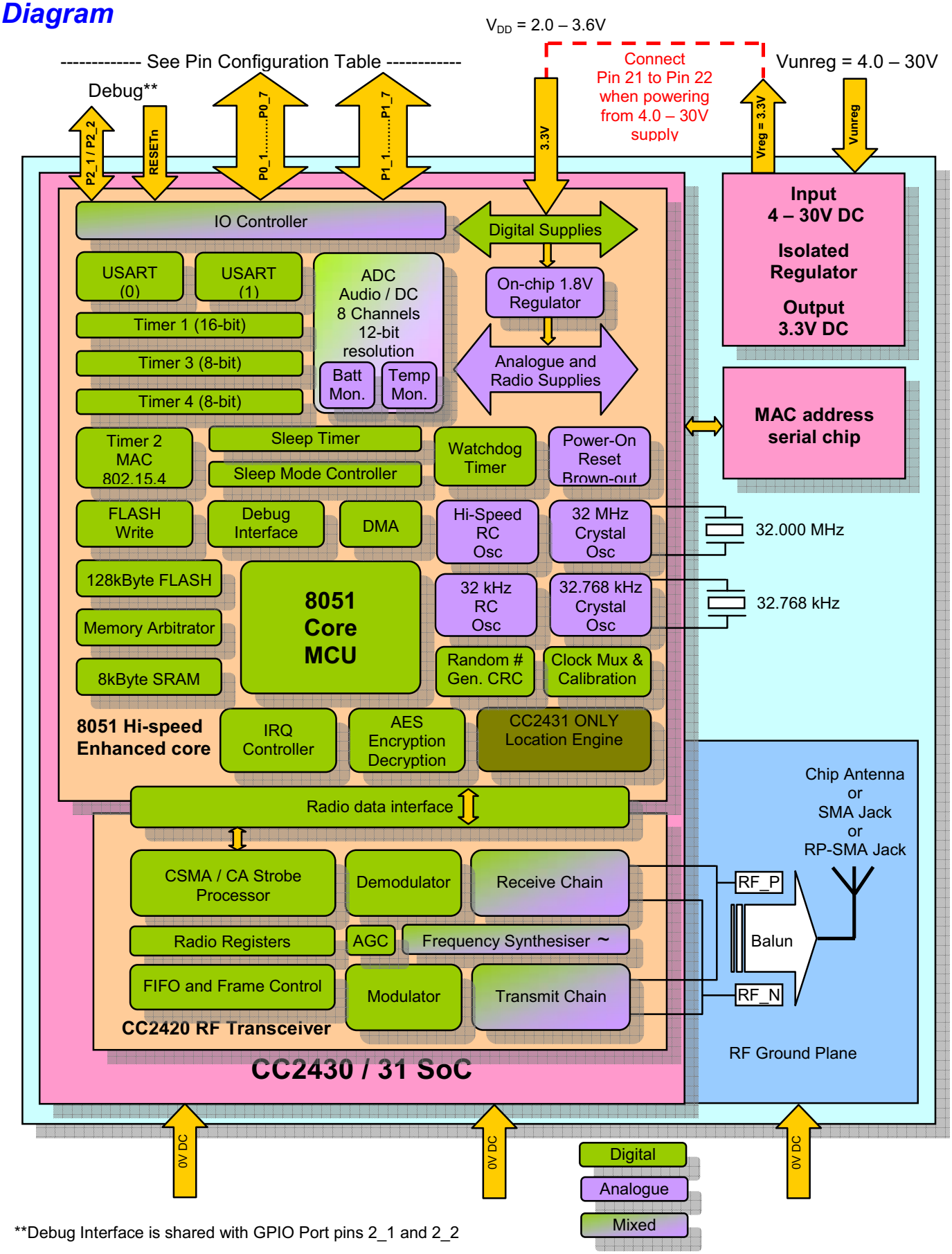
Block Diagram



**Debug Interface is shared with GPIO Port pins 2_1 and 2_2

Block Diagram