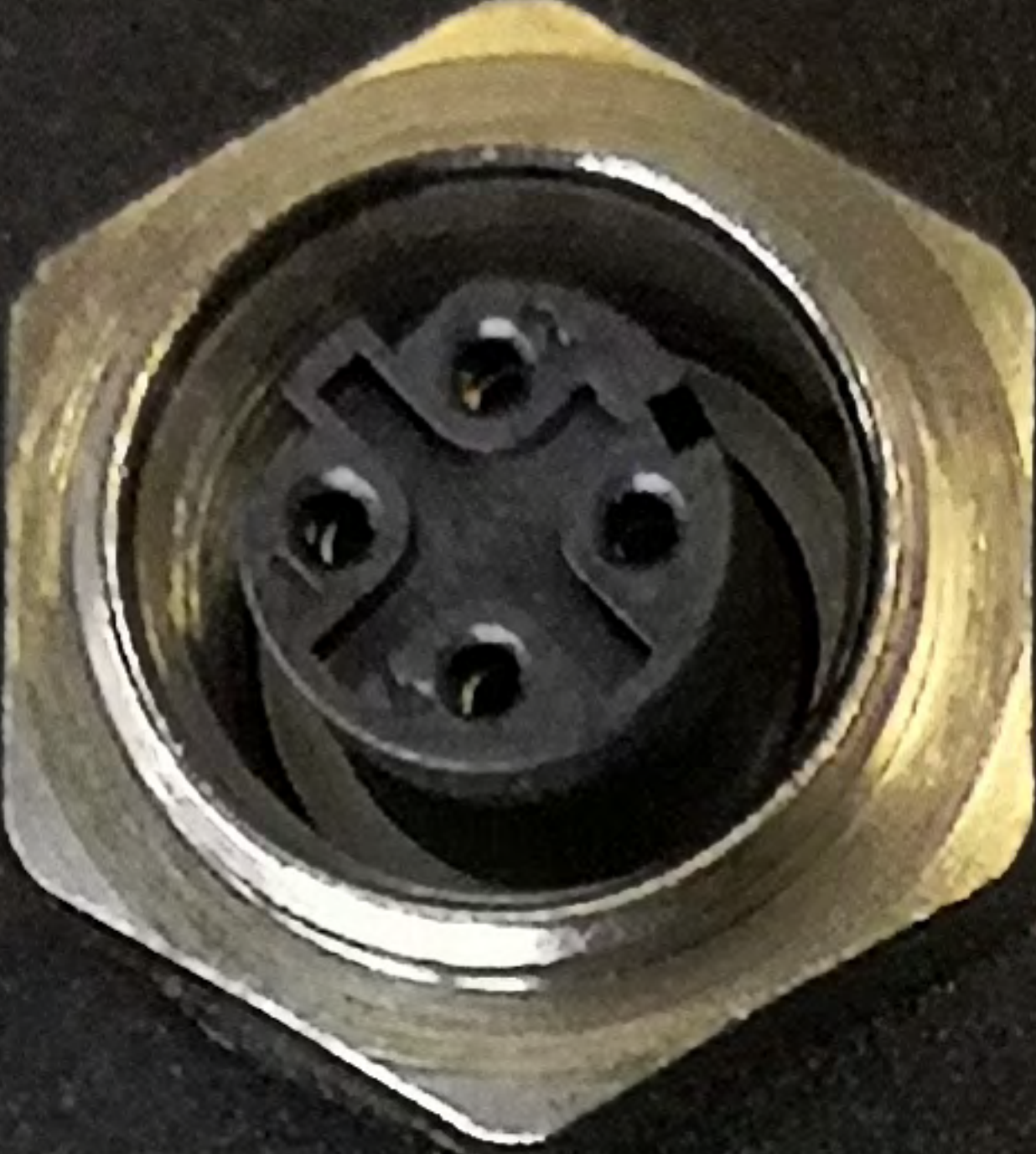
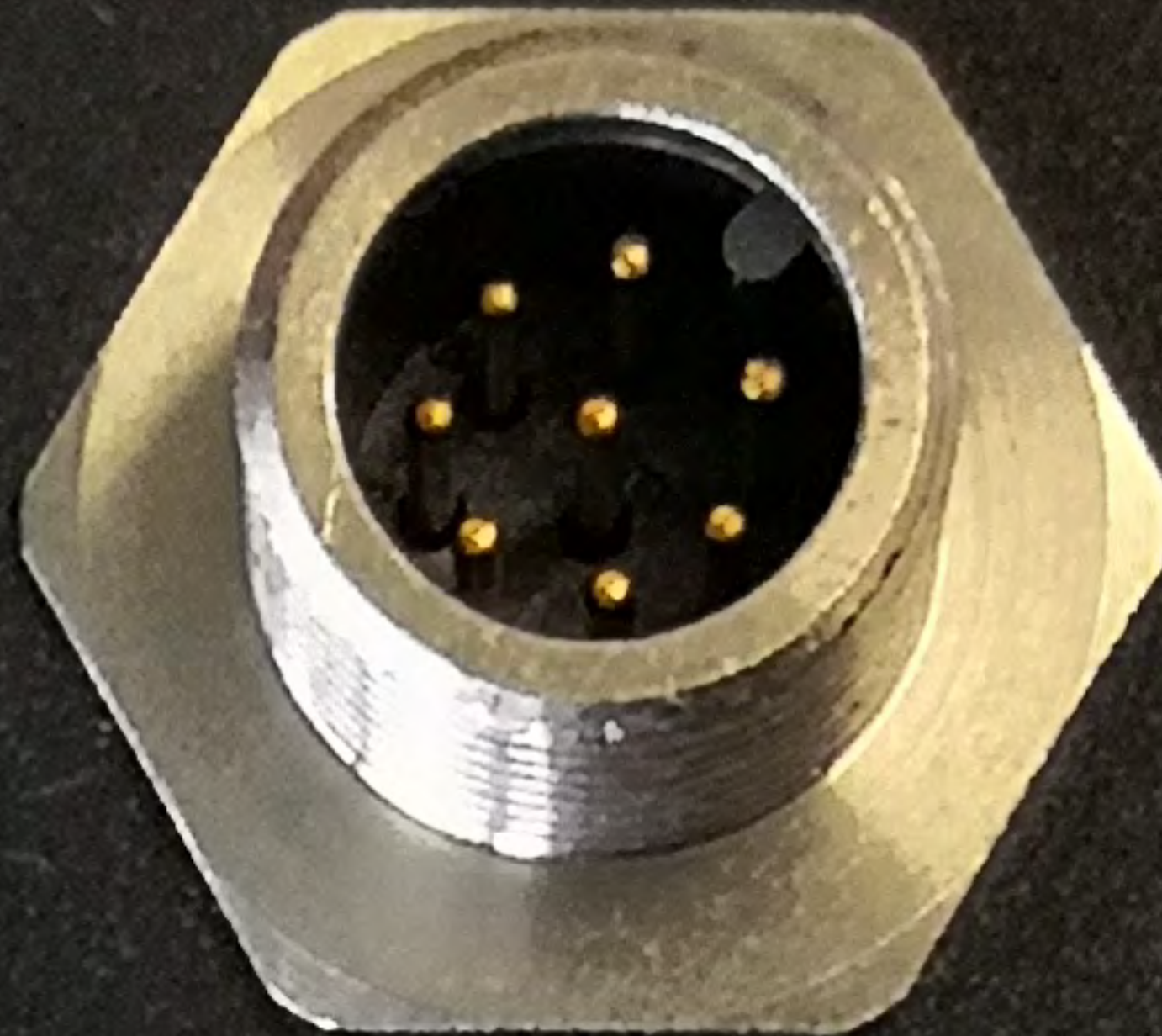


L
A



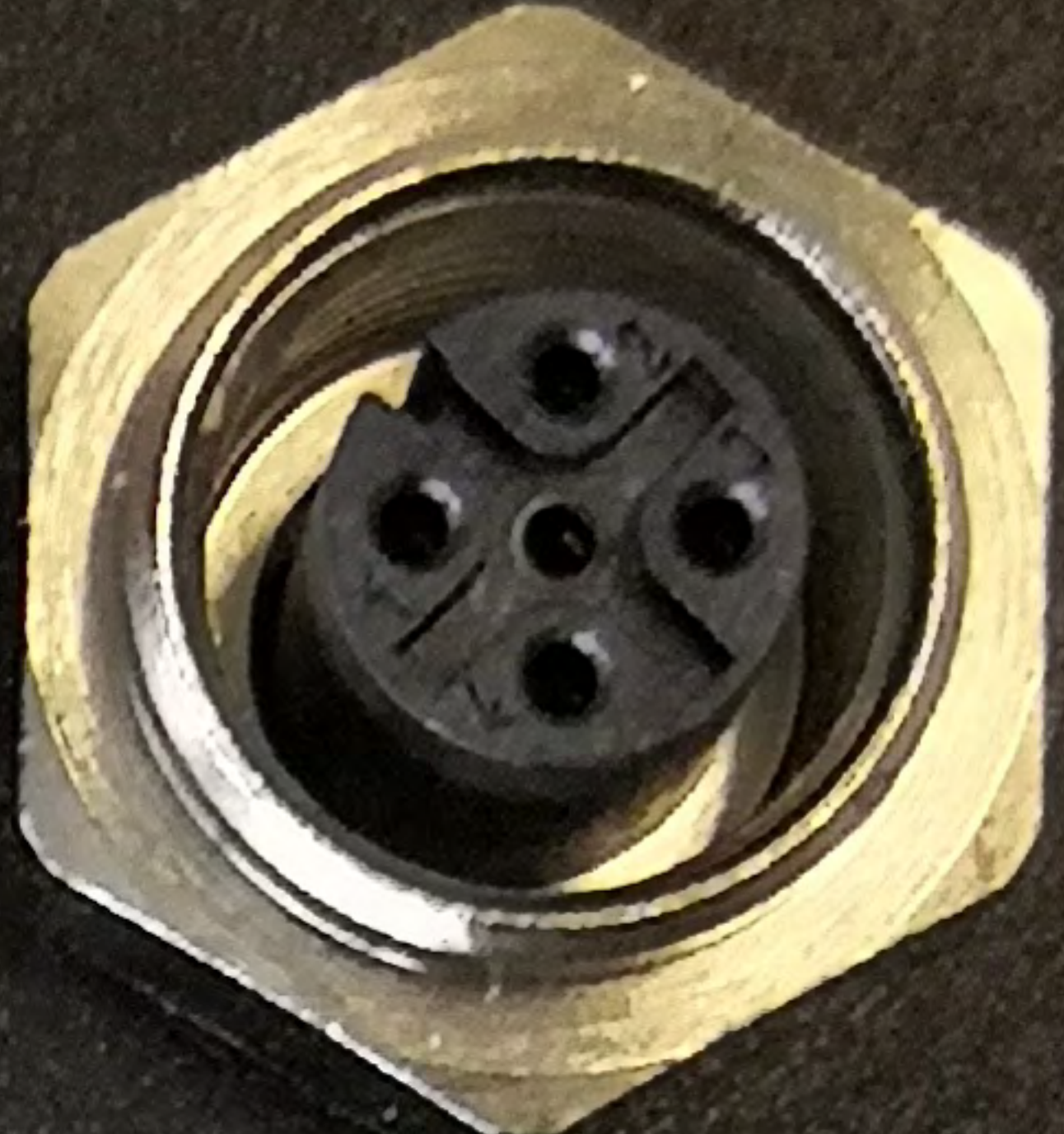
port 1

SF
BF

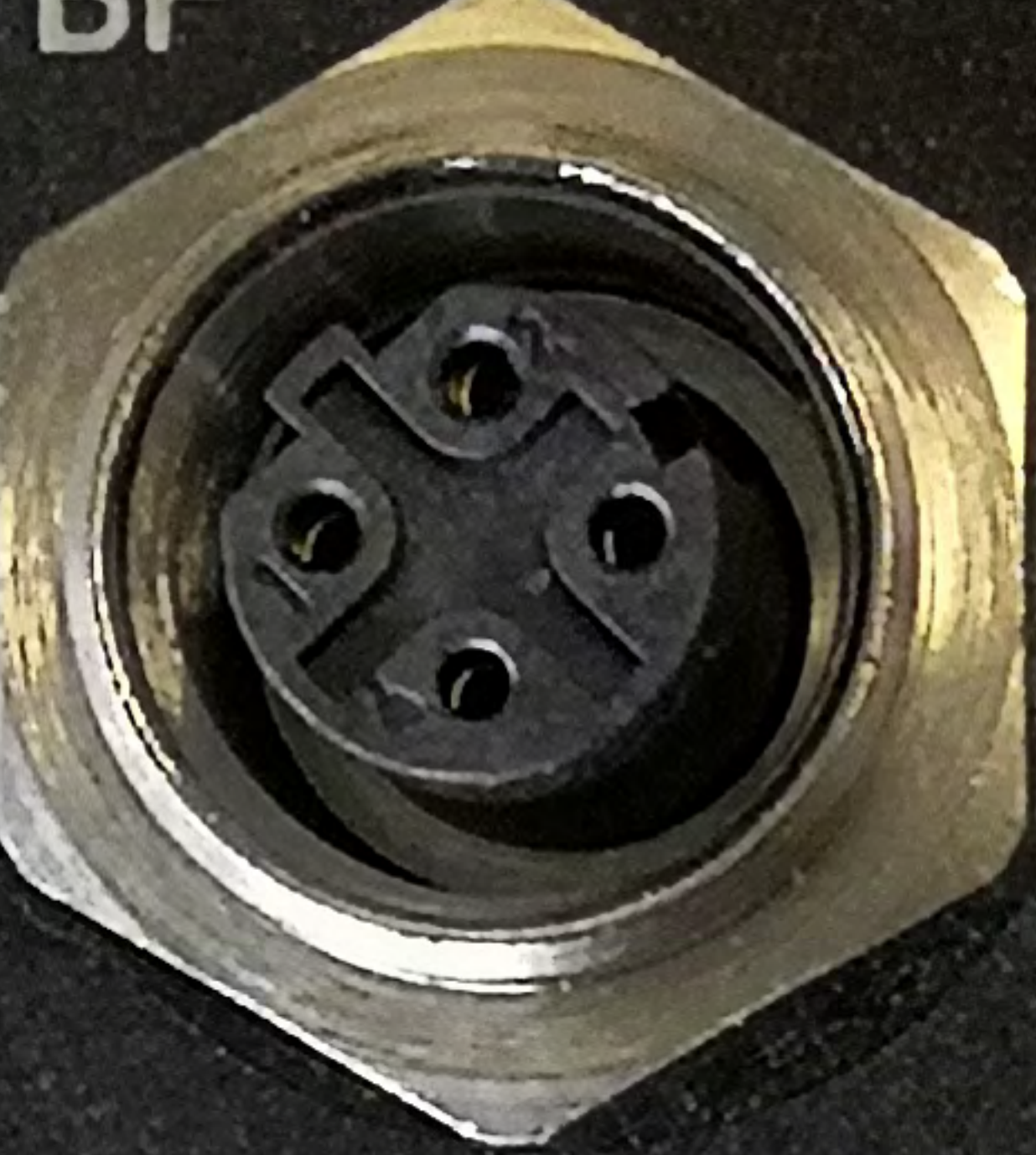


input

1

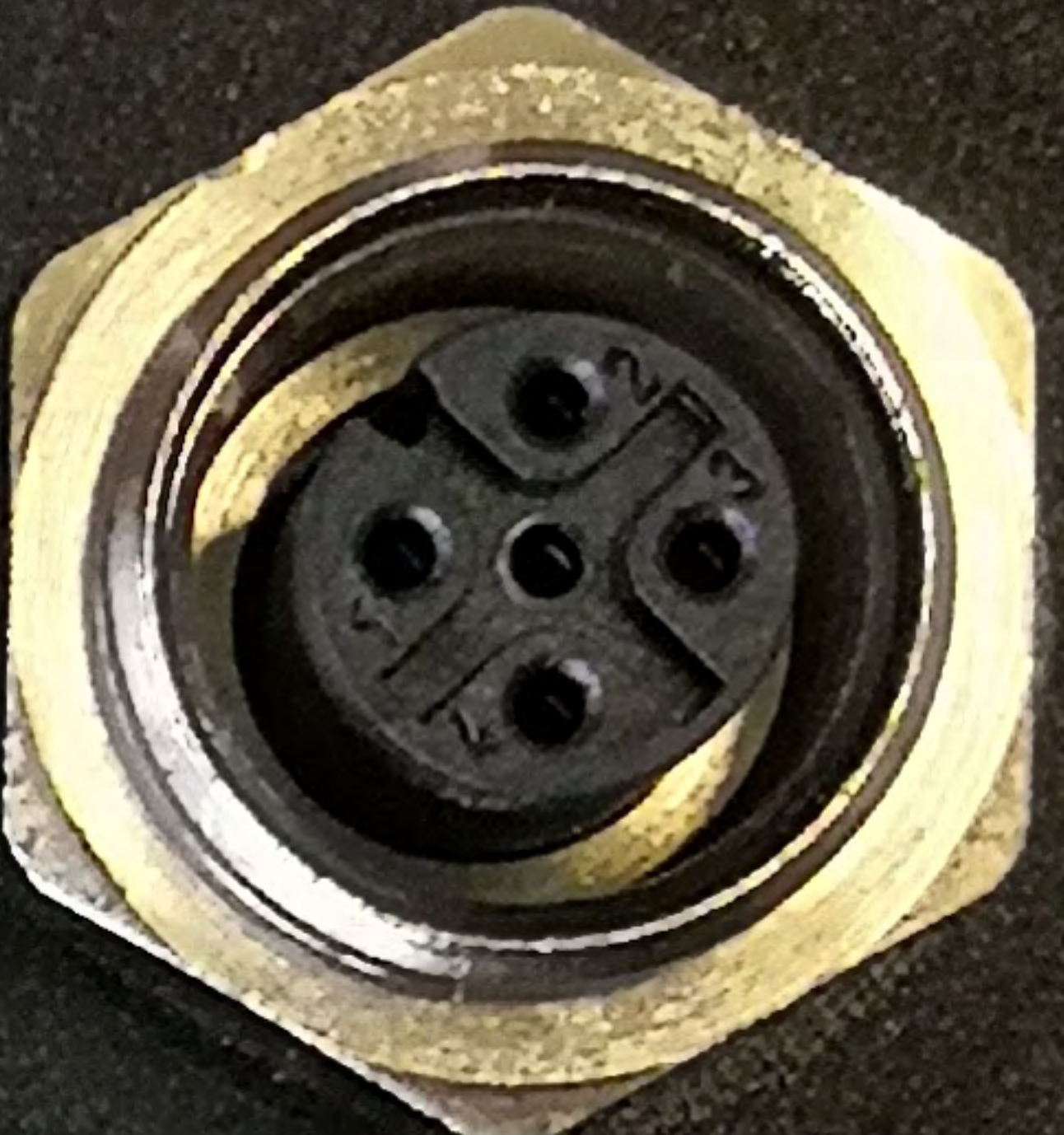


L
A



port 2

2



AEG ID
ACM 9
CE



3

S
P



AUX

output



4



