

**Appendix D. Photographs of the Test Set-Up**

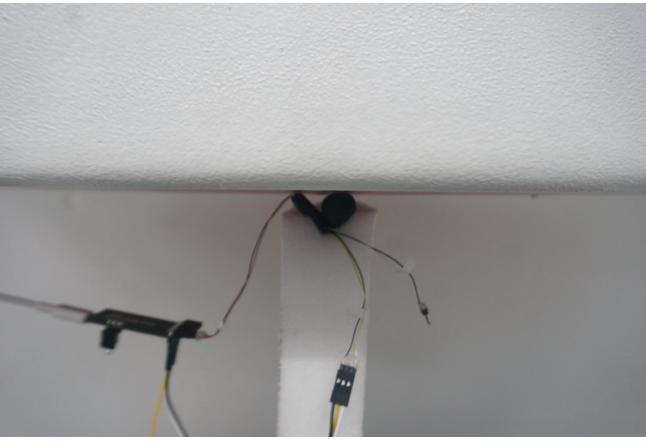
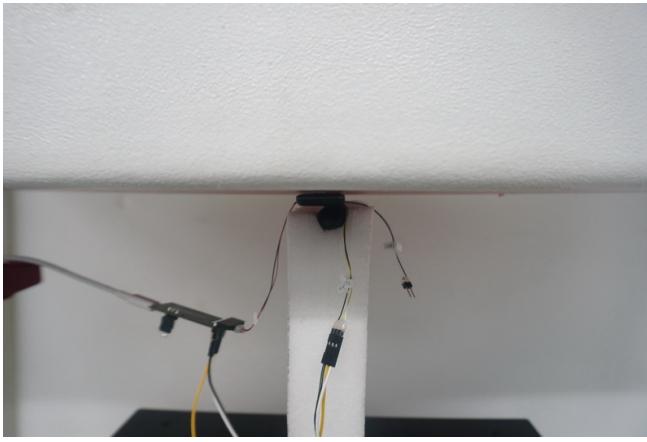
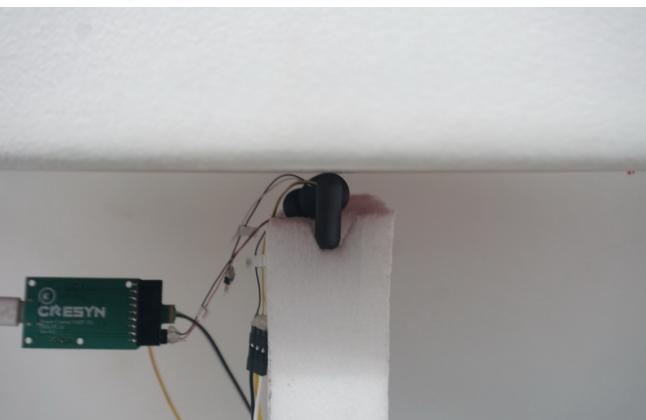
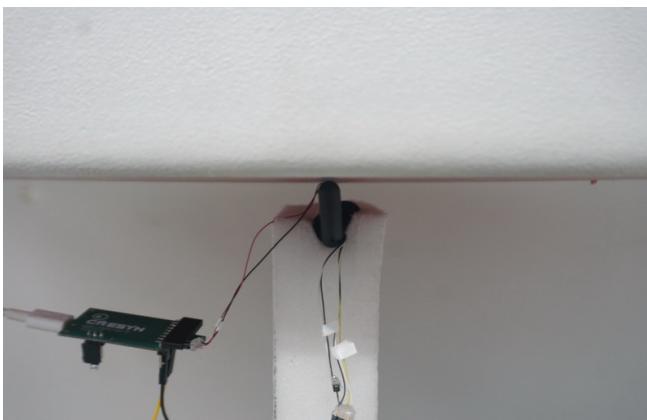
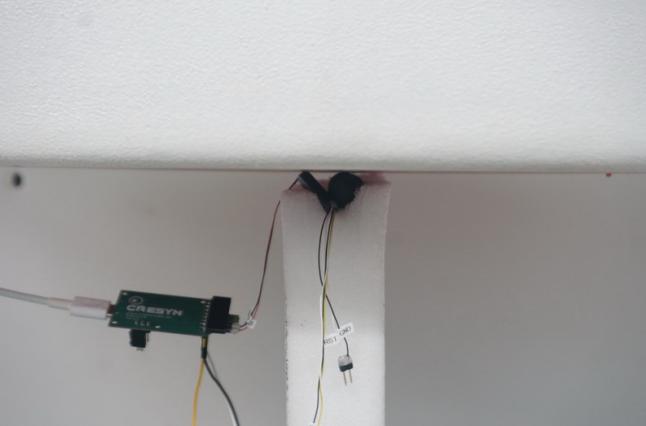
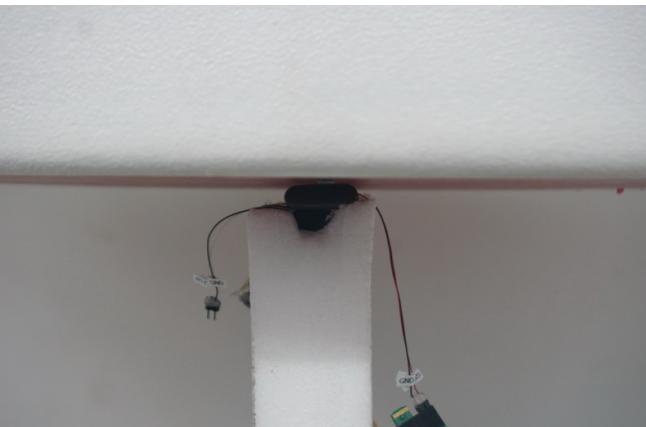
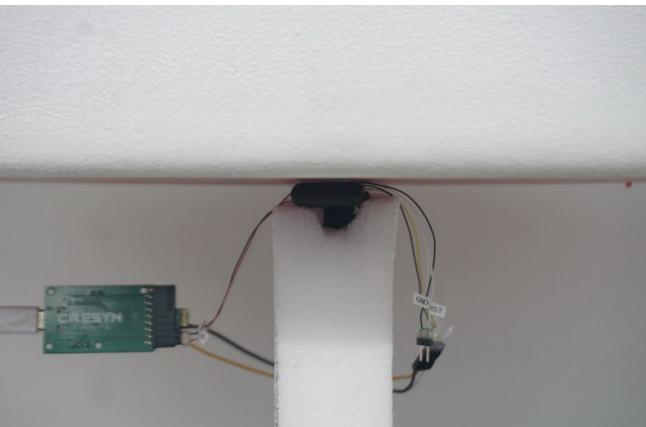
Photo 1: Front Face_0mm_Left earphone	Photo 2: Rear Face_0mm_Left earphone
	
Photo 3: Left Side_0mm_Left earphone	Photo 4: Right Side_0mm_Left earphone
	
Photo 5: Top Side_0mm_Left earphone	Photo 6: Bottom Side_0mm_Left earphone
	

Photo 7: Front Face_0mm_Right earphone	Photo 8: Rear Face_0mm_Right earphone
 A photograph showing a green printed circuit board (PCB) with a CRESYN logo and several component labels (VCC, GND, RST, TX, RX, GND, RST) connected to a white cylindrical earphone. The earphone is mounted on a white foam stand.	 A photograph showing the same green PCB and earphone setup as Photo 7, but from a different angle, highlighting the rear side of the PCB and the earphone connection.
Photo 9: Left Side_0mm_Right earphone	Photo 10: Right Side_0mm_Right earphone
 A photograph showing the left side of the green PCB and earphone setup, with the earphone mounted on a white foam stand.	 A photograph showing the right side of the green PCB and earphone setup, with the earphone mounted on a white foam stand.
Photo 11: Top Side_0mm_Right earphone	Photo 12: Bottom Side_0mm_Right earphone
 A photograph showing the top side of the green PCB and earphone setup, with the earphone mounted on a white foam stand.	 A photograph showing the bottom side of the green PCB and earphone setup, with the earphone mounted on a white foam stand.