

Date: 08/25/2009
Federal Communications Commission
Authorization and Evaluation Division
7435 Oakland Mills Road
Columbia, MD 21046

Attn: OET Dept.

Ref: FCC Class II Permissive change for FCC ID: V2L-TD1J
Original Grant Date: 05/27/2008
Applicant: Yi-Phone Inc.

Dear Examiner,

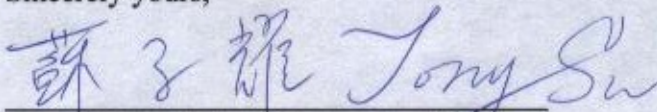
This is to request a Class II permissive change for FCC ID: V2L-TD1J
, originally granted on 05/27/2008

The major change filed under this application is:

1. Capacitor mismatch will increase the variation of clock frequency. In order to decrease the error of clock frequency, it is necessary to use the matched capacitor. After replacing C2 and C4, the error of clock frequency reduces from 46PPM to 7PPM.
2. The intrinsic character of capacitor — charge and discharge — are used to reduce the baseline wandering of Direct Current (DC) and stabilize voltage. In the circuit of V2L-TD1J, capacitor C9 is used to stabilize the voltage of CN2 VBUS and prevent baseline wandering. C12 is used to stabilize the voltage of IC12 DVDD and prevent baseline wandering. C20 is used to stabilize the voltage of VCC3V and prevent baseline wandering.

Note: A pcb layout change is permissible in some cases; please refer to the below mentioned FCC document regarding permissive change policies. The change of PCB layout of TD1J do not involve RF specification differences.

Sincerely yours,



Tony Su/ Special Assistant of President
Yi-Phone Inc.

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