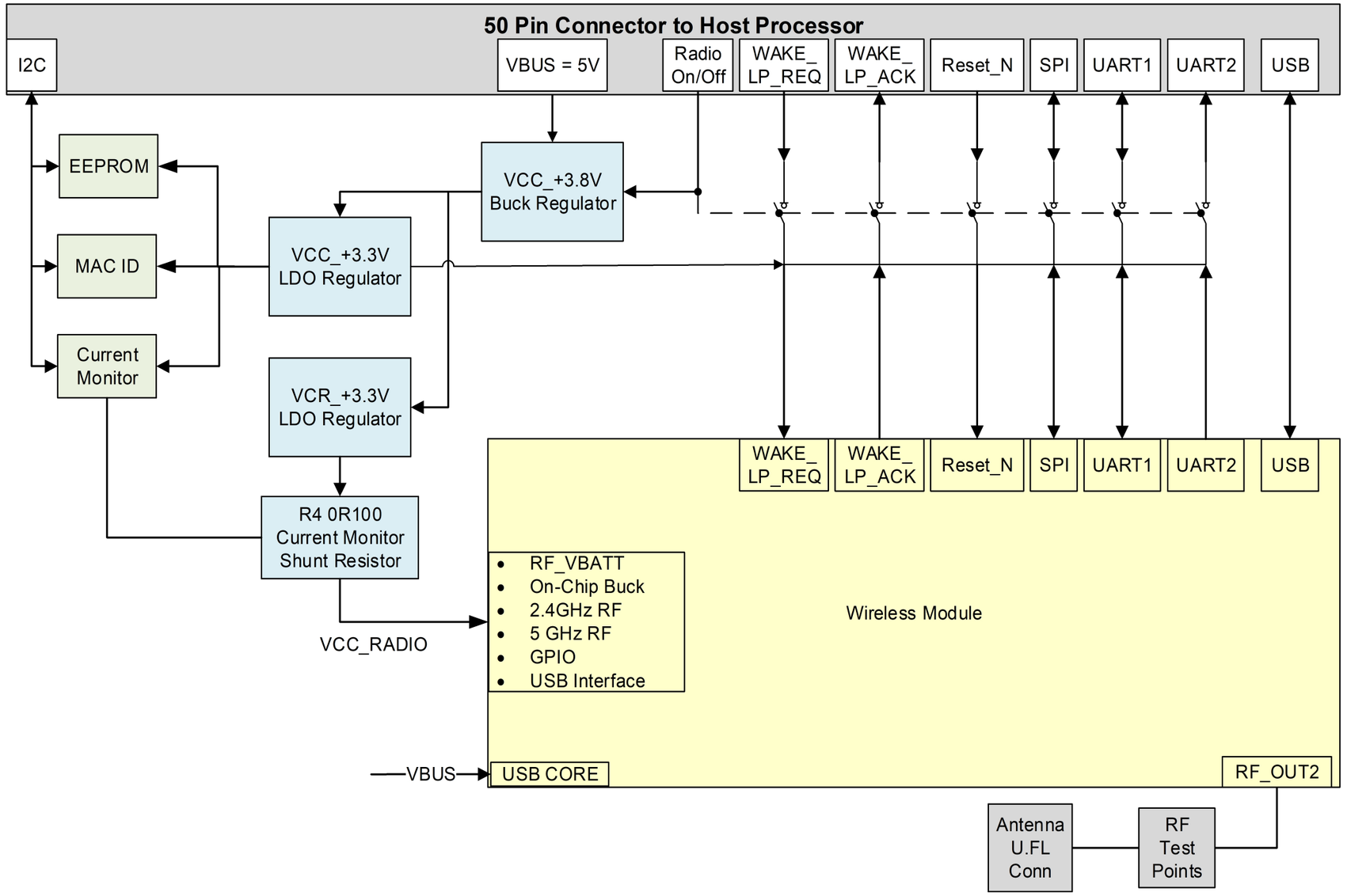

Table of content

Do Not Populate
C16
C17
C20
C29
C30
R12
R23
R24
R25
R29
R32
R34
R35
R36
R37
R38
R41
R42
R49
R6
R8

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hierarchical path:

A history of deteailed changes can be found in document 10004060

I2C Addresses

Device	Address
Current Monitor	1000100
EEPROM	1010000
MAC ID	1010001

=EX=	=QA=	=QB=	=QC=	General Tolerances
	=QS=	=QF=	Occurs	Modification Text C12 changed to 8.2pF using coplanar waveguide

Checked by	Approved by	Page	
Laurie Fraser 2023-07-06T16:14:20	Bill Dowd 2023-07-06T16:19:20	1 / 5	Dräger

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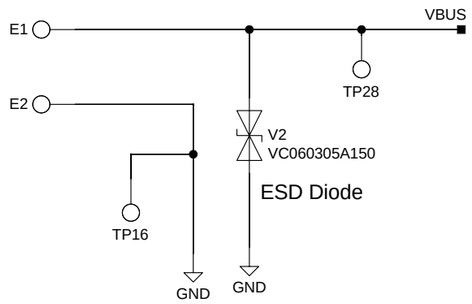
Plated Thru Holes for Ext. Power

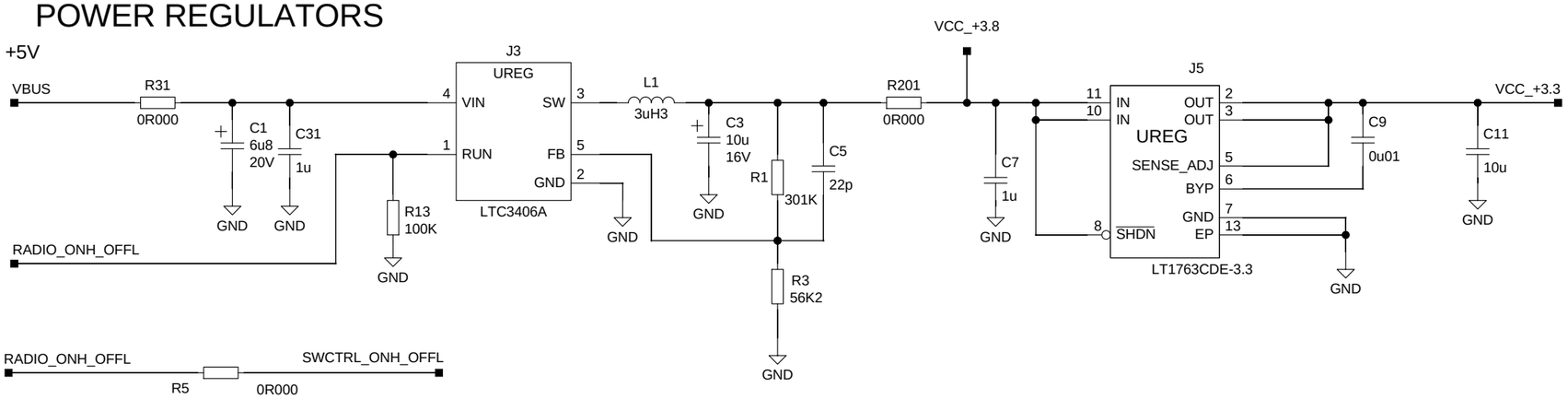
1/10" spacing for standard header

Add silk screen for E1 - VBUS E2 - GND

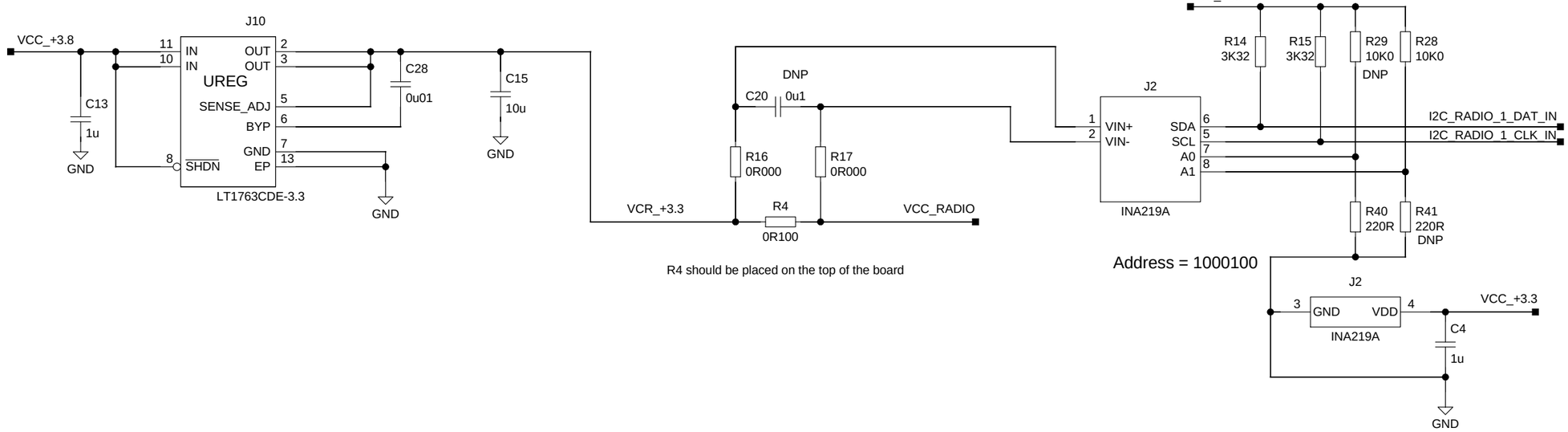


POWER REGULATORS

VBUS = +5V



CURRENT / POWER MONITOR



R4 should be placed on the top of the board

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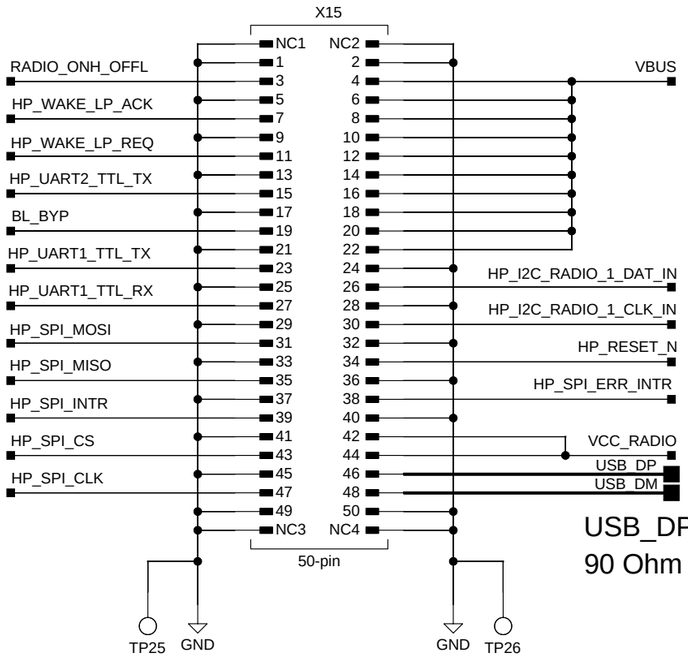
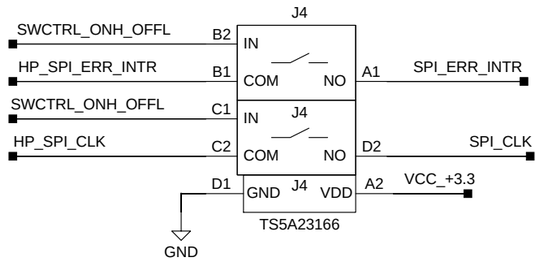
CONNECTOR

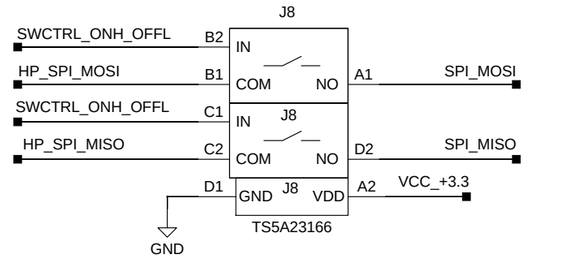


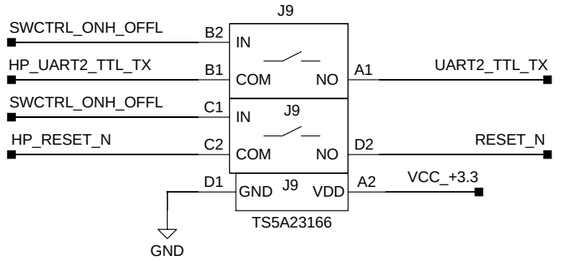
VCC_RADIO is used for troubleshooting Only
Host application should not connect

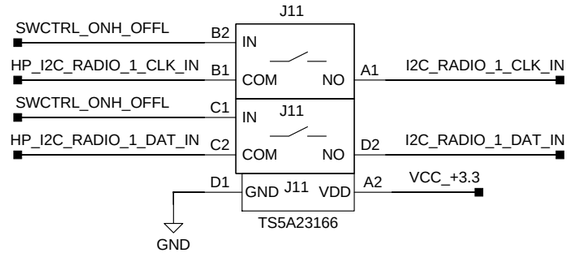
USB_DP and USB_DM
90 Ohm Differential Lines

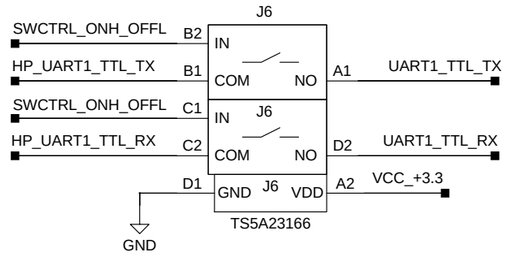
SWITCHES

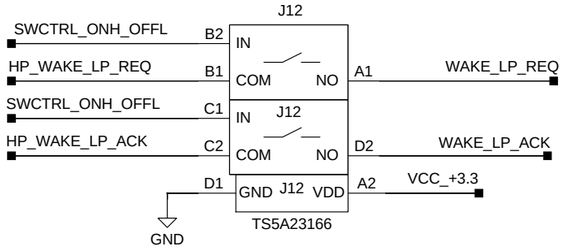


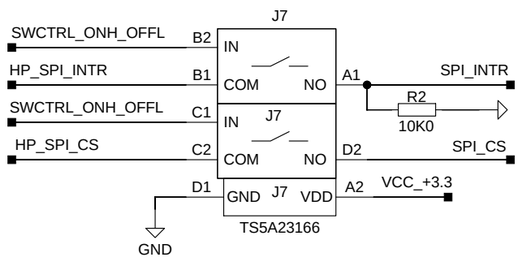




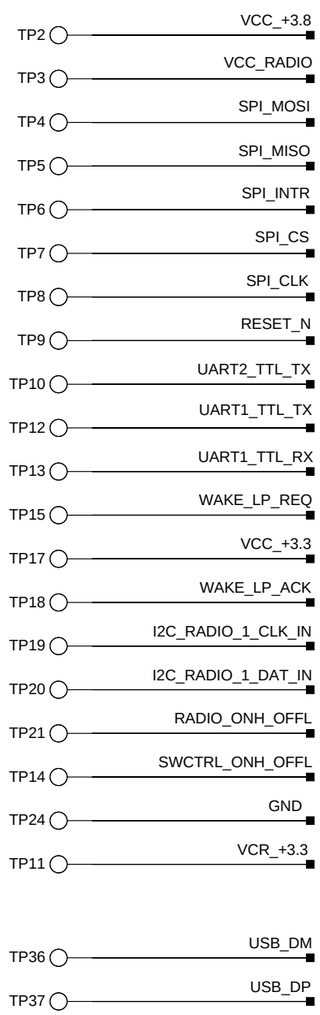






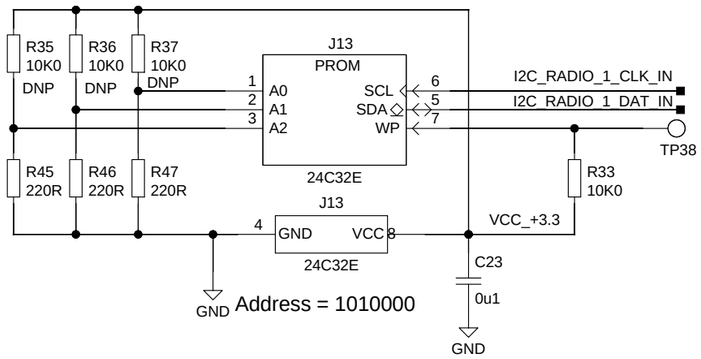


TEST POINTS



USB test point TP36 and TP37
Need to be Inline test point
<without stubs>

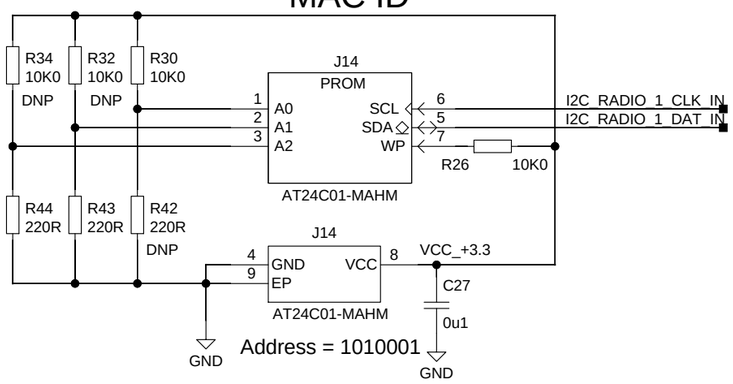
EEPROM



The eigth bit (LSB) of the device address is Read/Write operation select bit

A Read operation is initiated if this bit is high
A Write Operation is initiated if this bt is low

MAC ID



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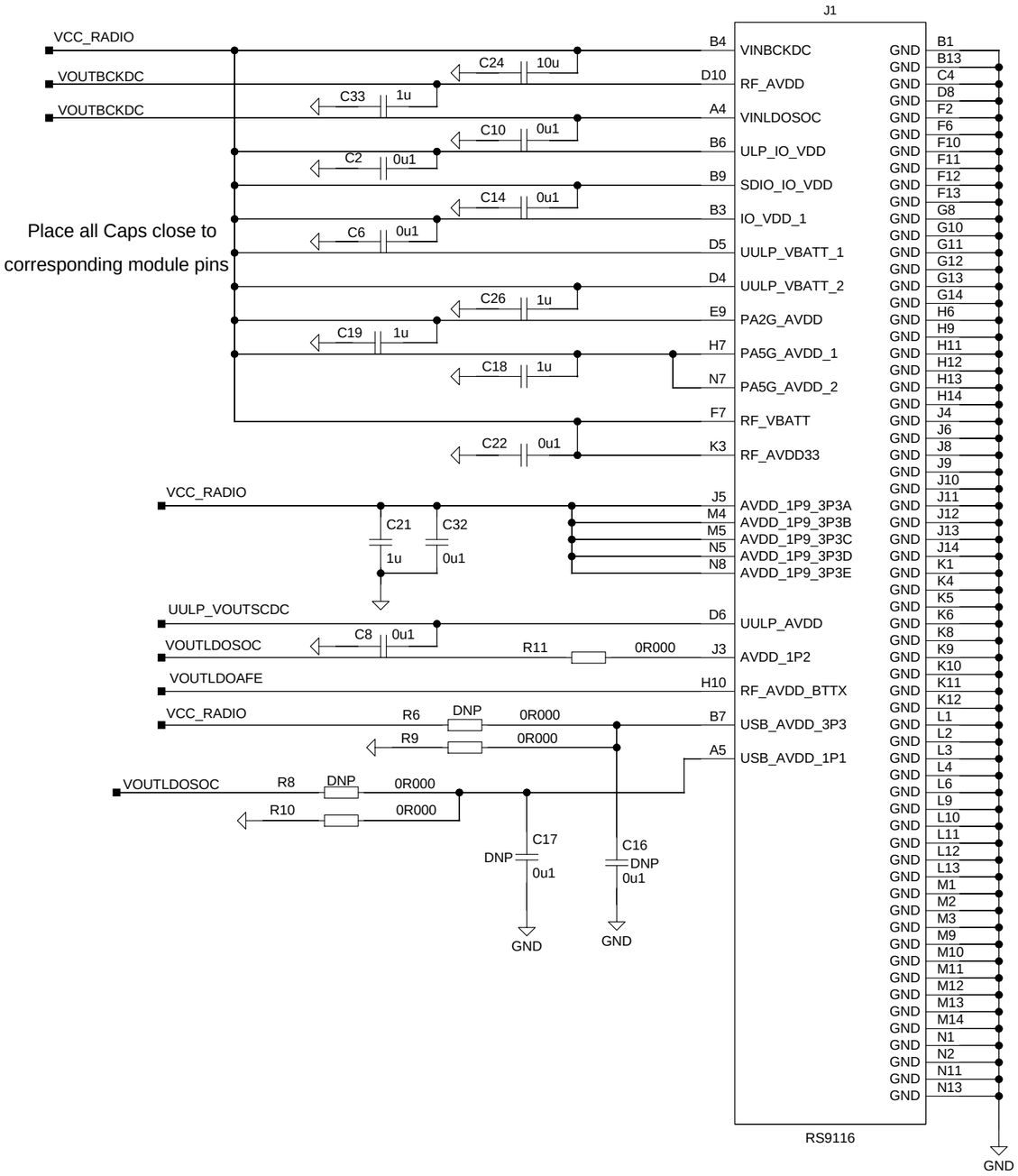
RS9116 Power

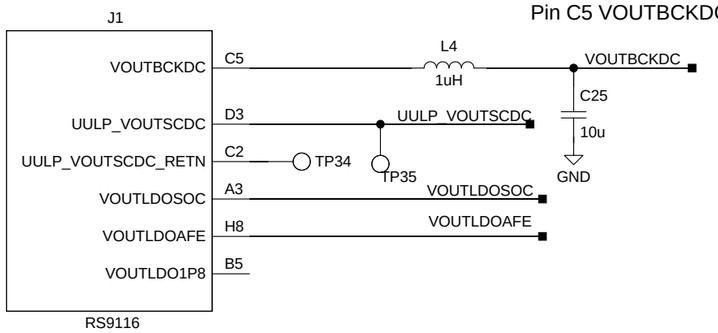
VCC_RADIO Should be Star Routed

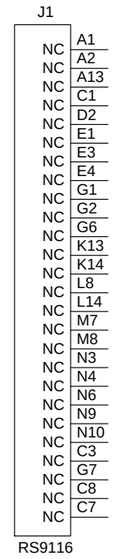


The path from VOUTBCKDC to VINLDOSOC is a high current path.

The Trace should be as short & wide as possible

Place C25 close to L4
Pin C5 VOUTBCKDC





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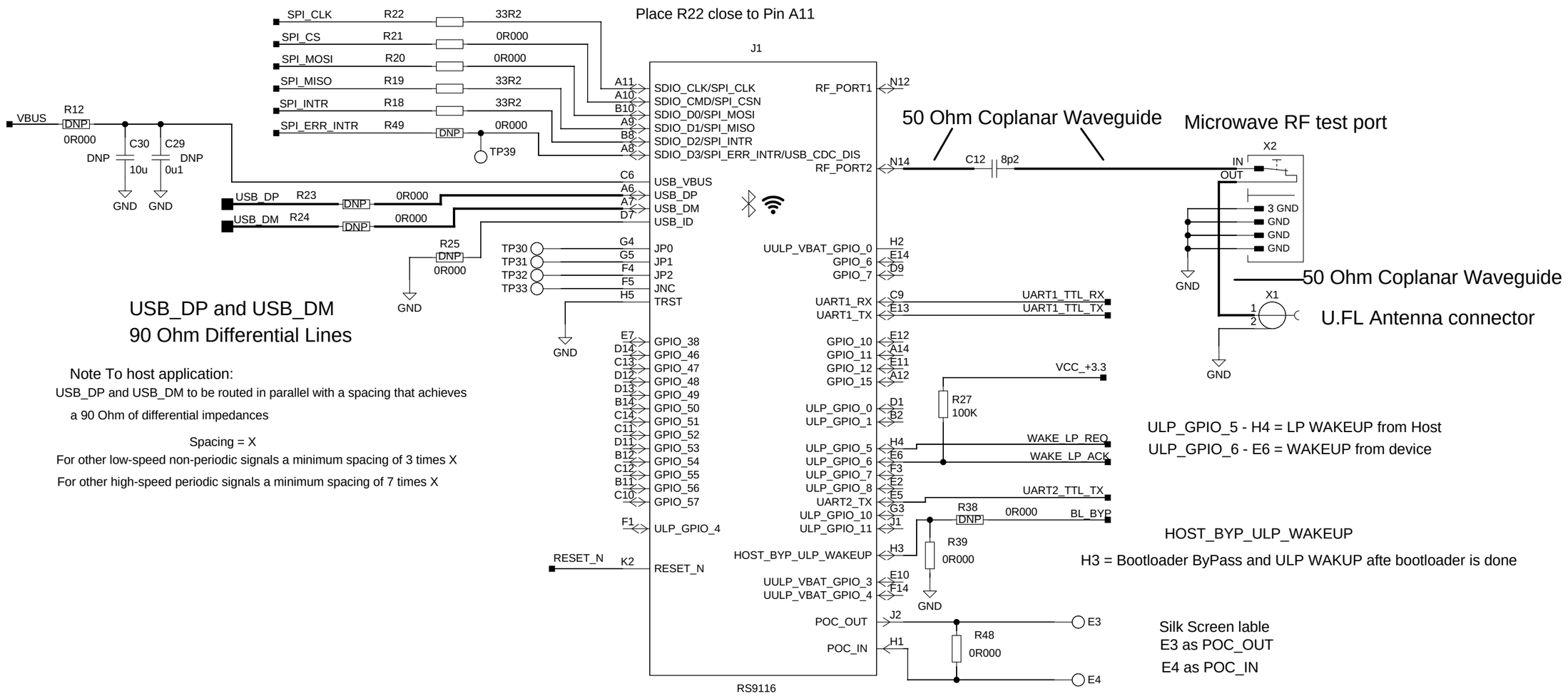
RS9116 I/O



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