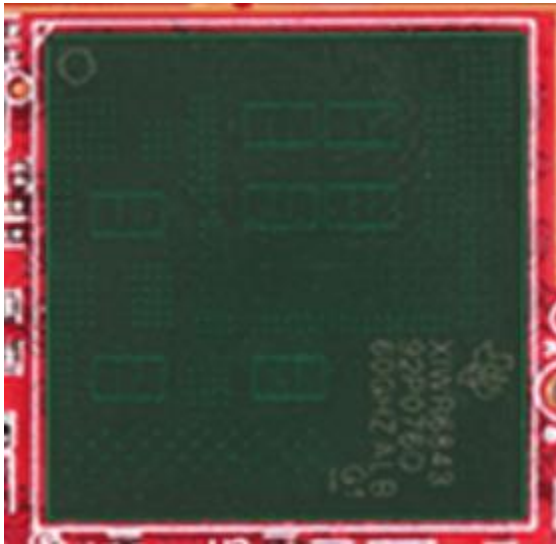


Closeup detail: manufacturer image of 60 GHz radar chip, IWR6843AOP, with embedded antennas:



Source: TI IWR6843AOPEVM Evaluation Module startup guide, www.ti.com/tool/IWR6843AOPEVM.

Schematic representation of antenna placement. (Note the rightmost figure is not relevant; the left figure corresponds to the photographic image above, just rotated 90 degrees CCL.

8.10.2 Antenna Positions

Figure 8-3 shows the placement and relative spacing of the antennas.

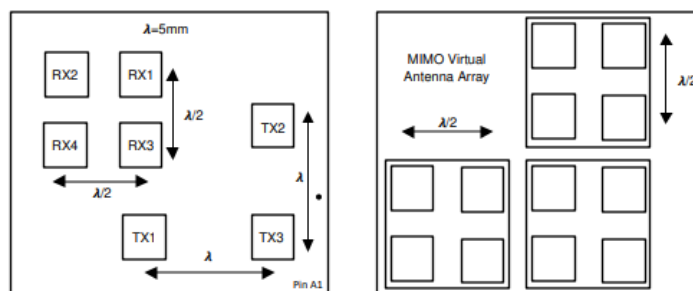


Figure 8-3. Antenna Positions (Placement and Relative Spacing)

8.10.3 Power Supply Sequencing and Reset Timing

The IWR6843AOP device expects all external voltage rails to be stable before reset is deasserted. Figure 8-4 describes the device wake-up sequence.

Source: IWR6843AOP Datasheet, TI Doc. SWRS237B.

<https://www.ti.com/lit/ds/symlink/iwr6843aop.pdf?ts=1695933825815>

Receive antenna radiation pattern:

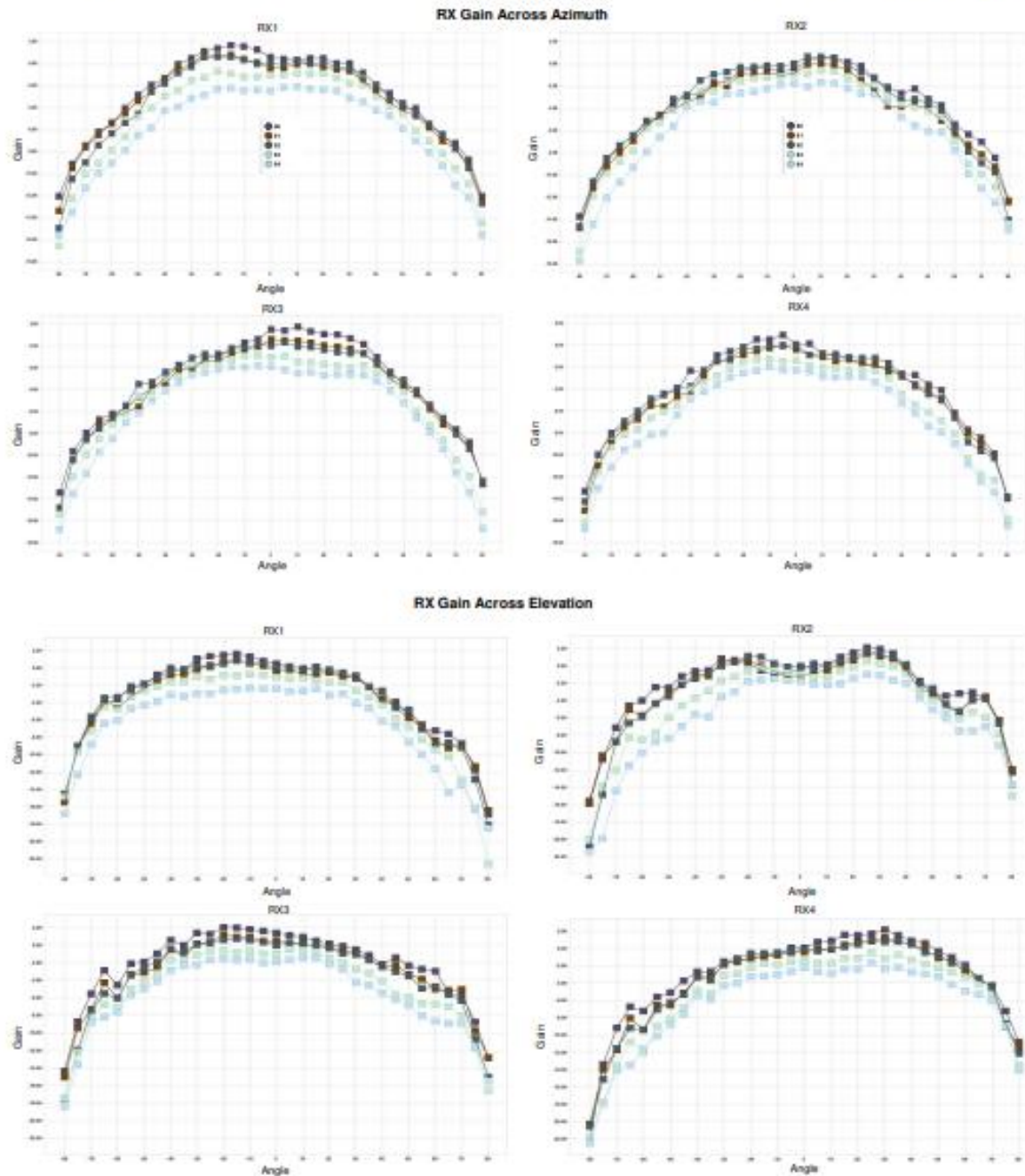


Figure 8-1. Receiver Antenna Radiation Pattern

Transmit antenna radiation pattern:

8.10.1.2 Antenna Radiation Patterns for Transmitter

Figure 8-2 shows typical antenna radiation patterns for the three transmitters in both Azimuth (H-Plane) and Elevation (E-Plane) planes.

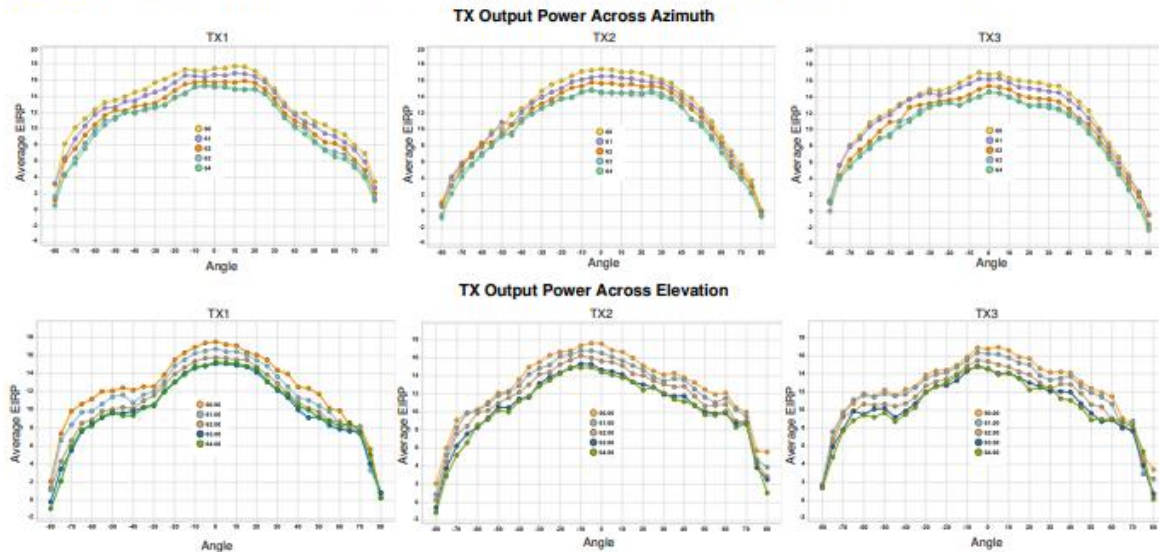


Figure 8-2. Transmitter Antenna Radiation Pattern

Antenna pattern source: IWR6843AOP Datasheet, TI Doc. SWRS237B, pp. 33-34

<https://www.ti.com/lit/ds/symlink/iwr6843aop.pdf?ts=1695933825815>