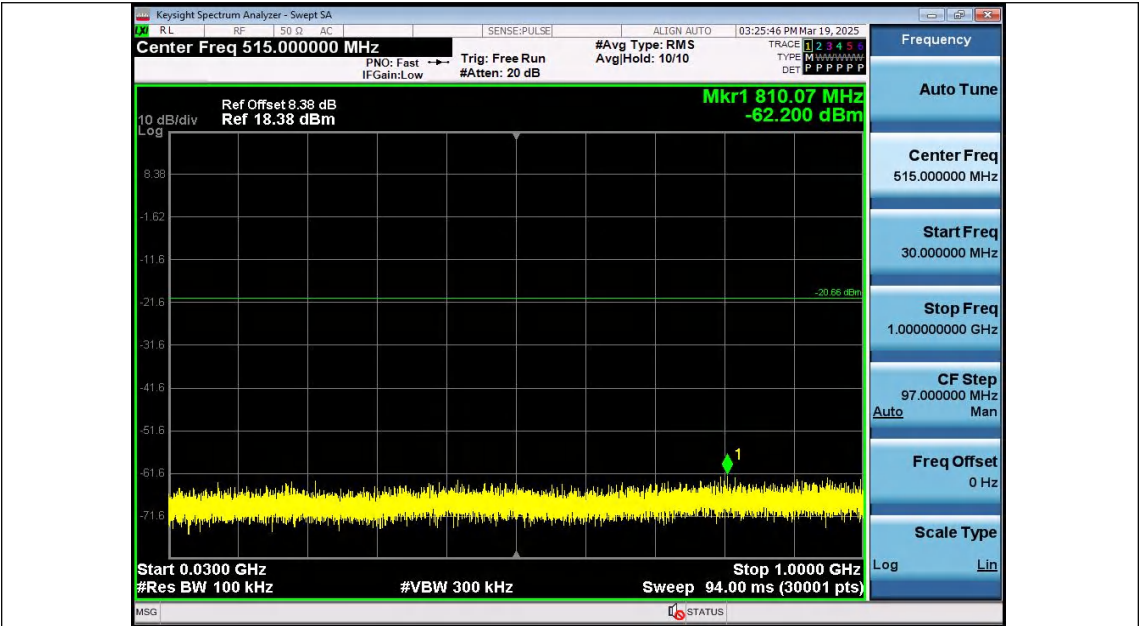
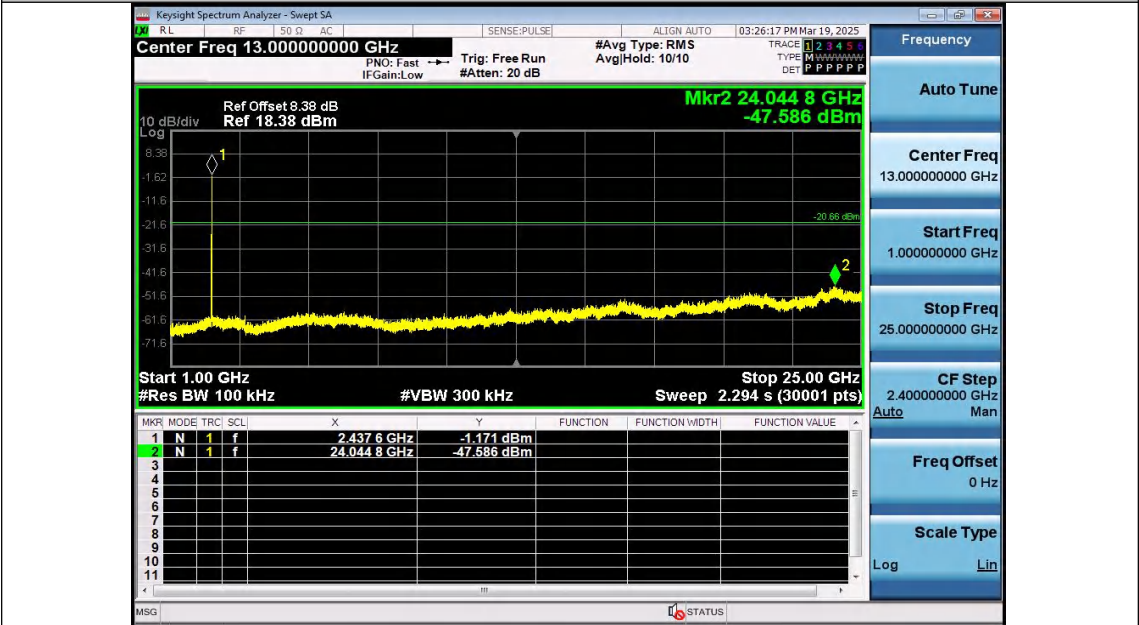
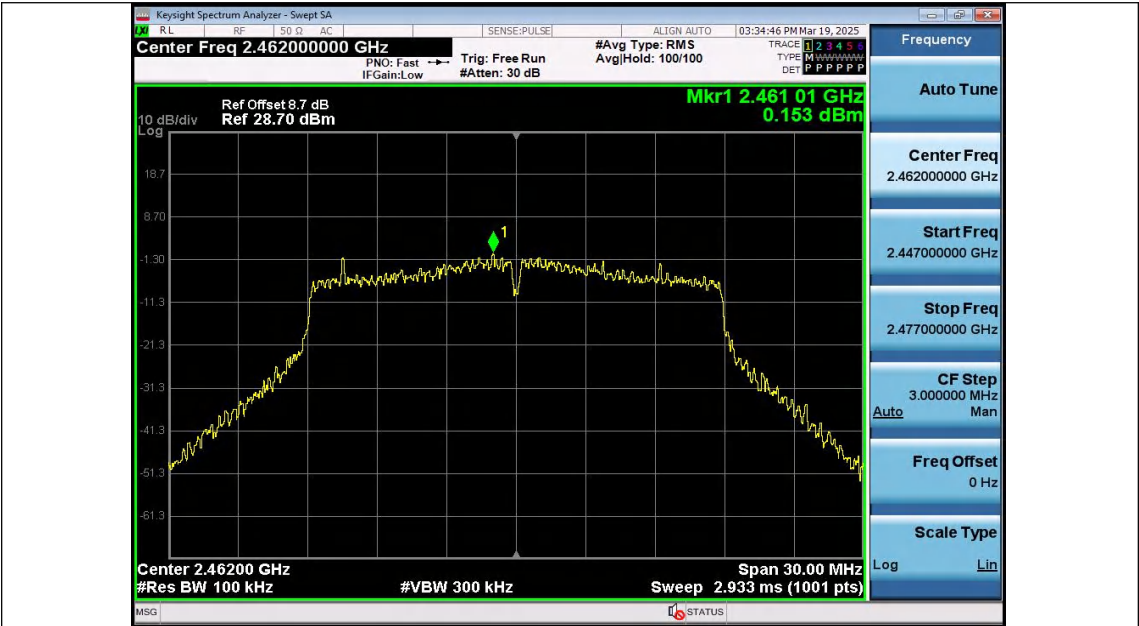




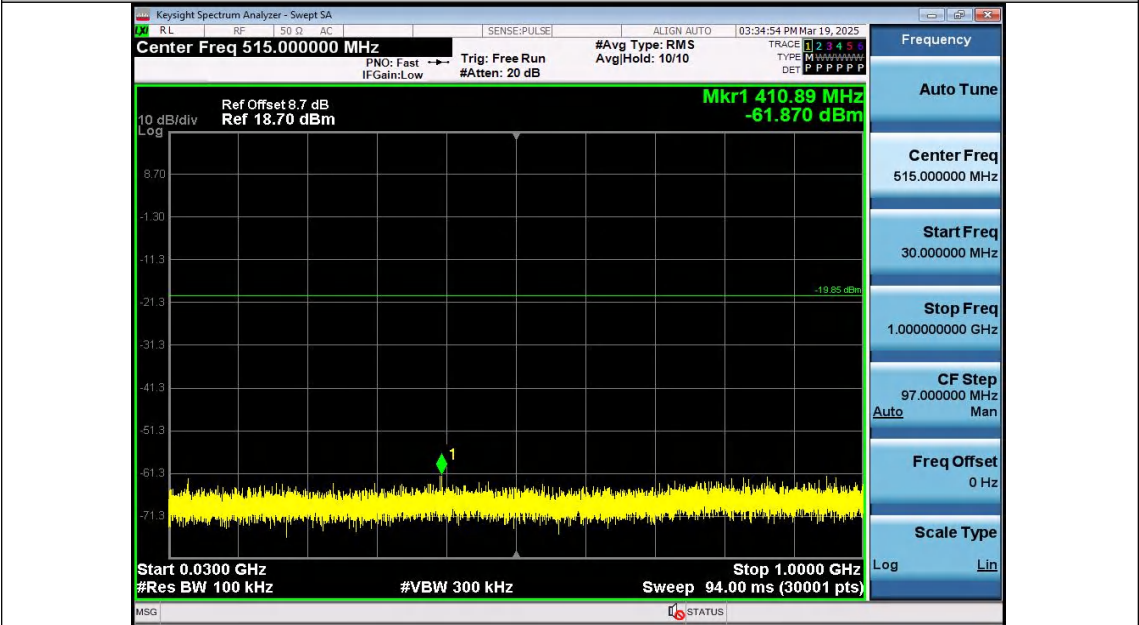
11N20SISO-Ant2-2437-30~1000-PASS



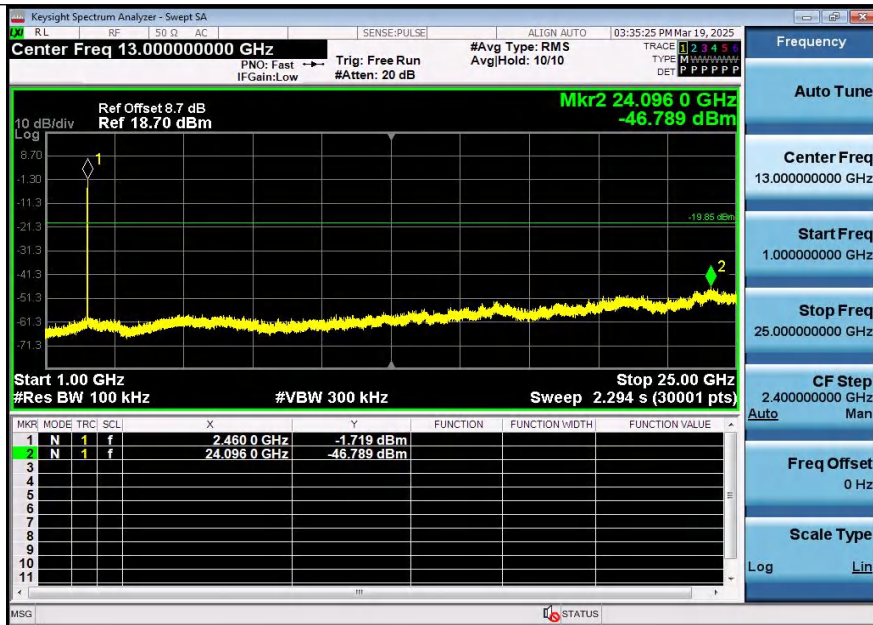
11N20SISO-Ant2-2437-1000~25000-PASS



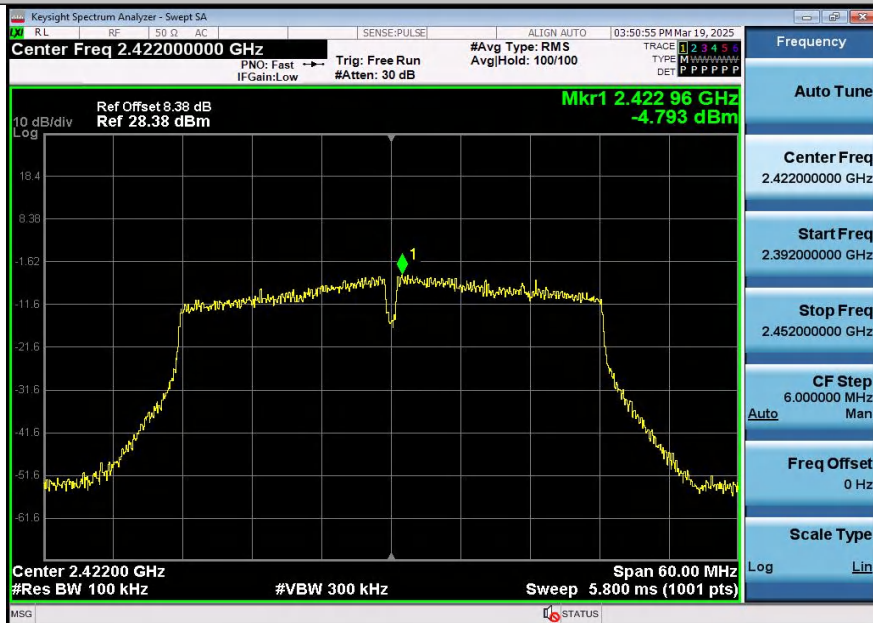
11N20SISO-Ant2-2462-0~Reference-PASS



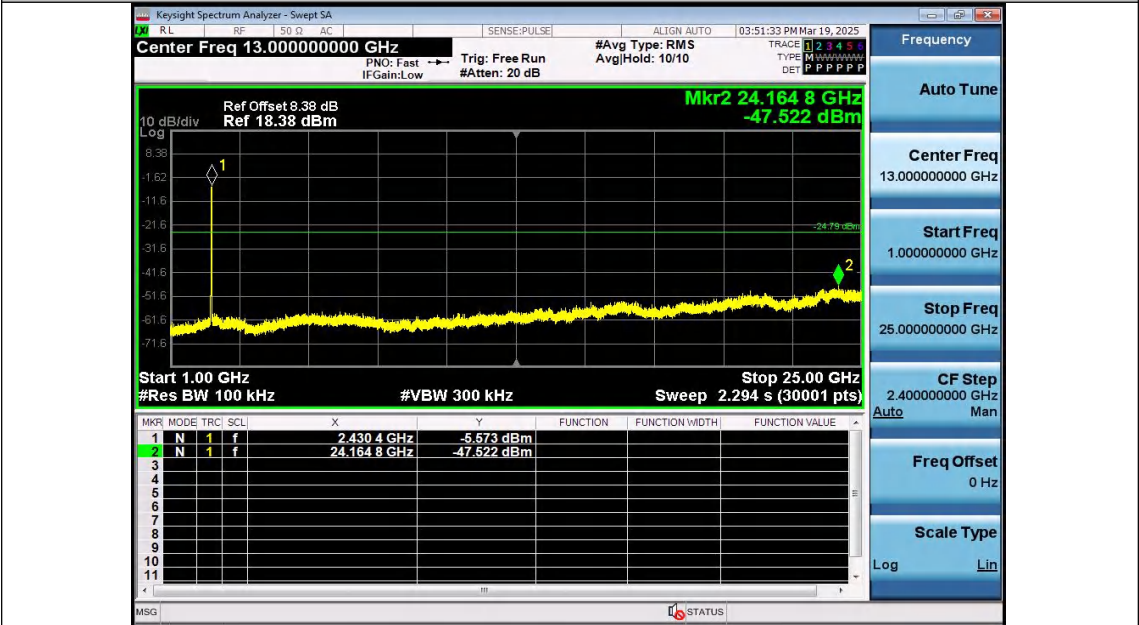
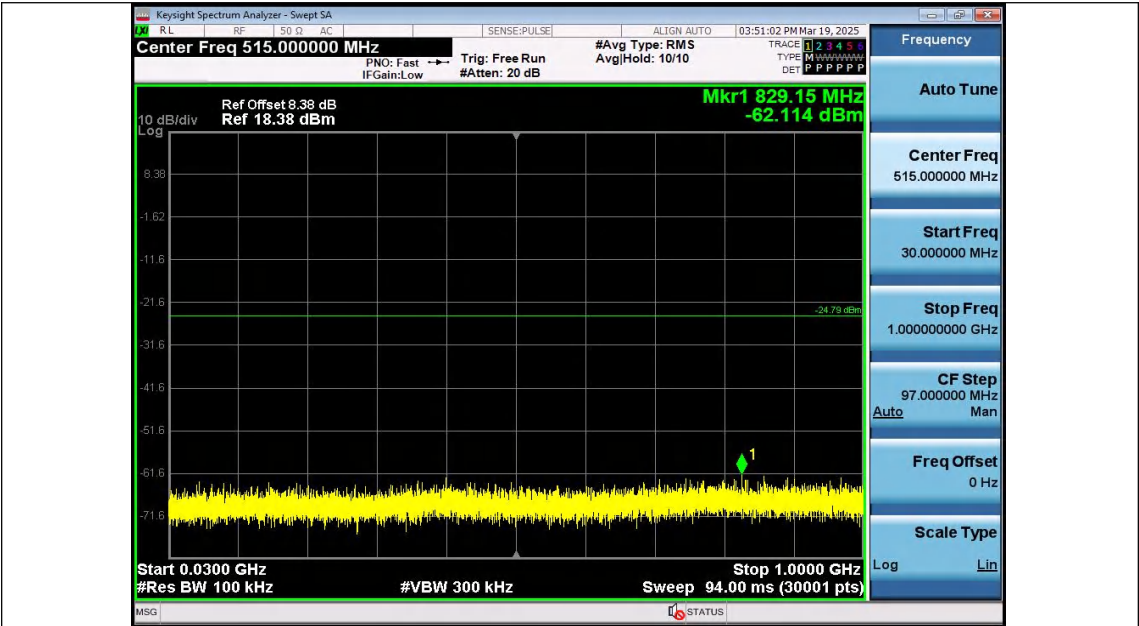
11N20SISO-Ant2-2462-30~1000-PASS

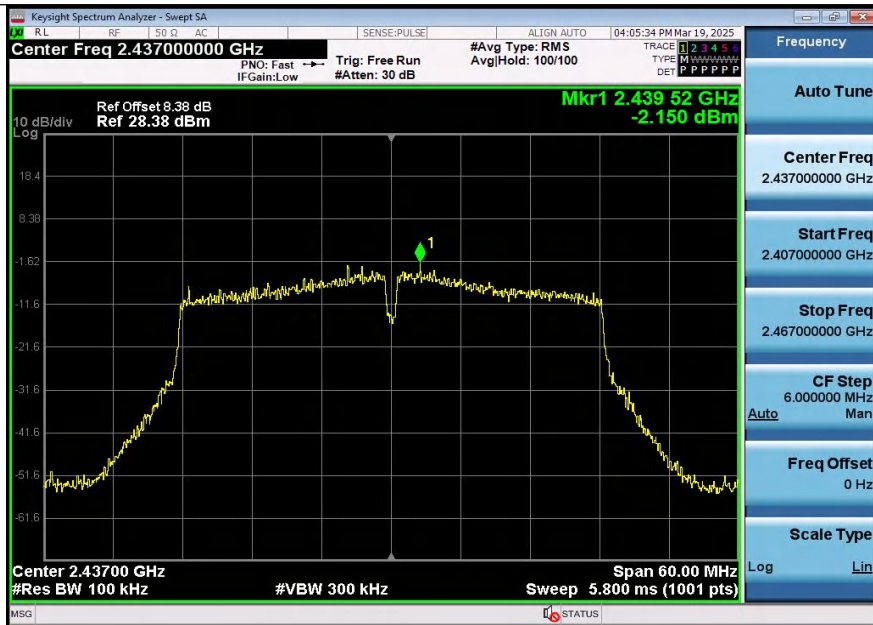


11N20SISO-Ant2-2462-1000~25000-PASS

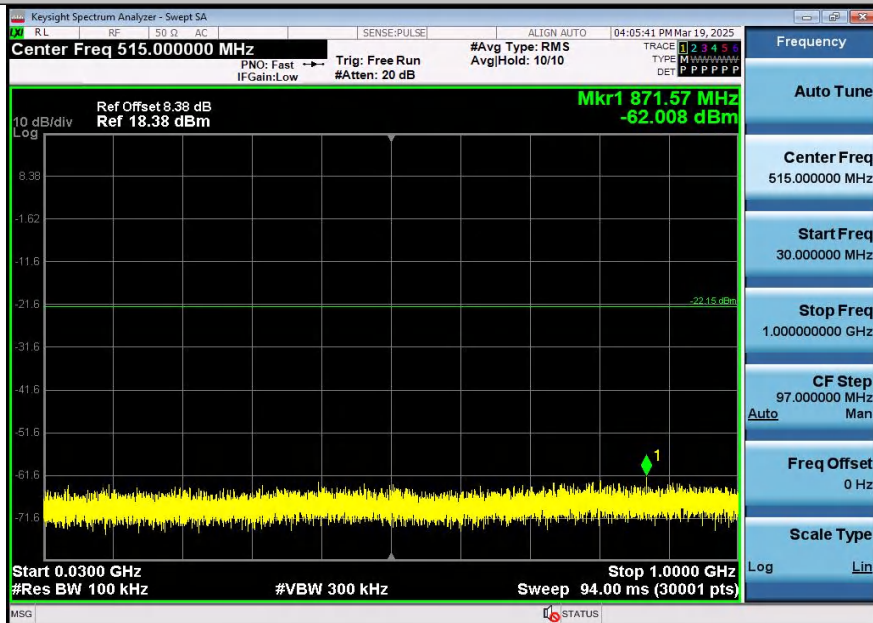


11N40SISO-Ant2-2422-0~Reference-PASS

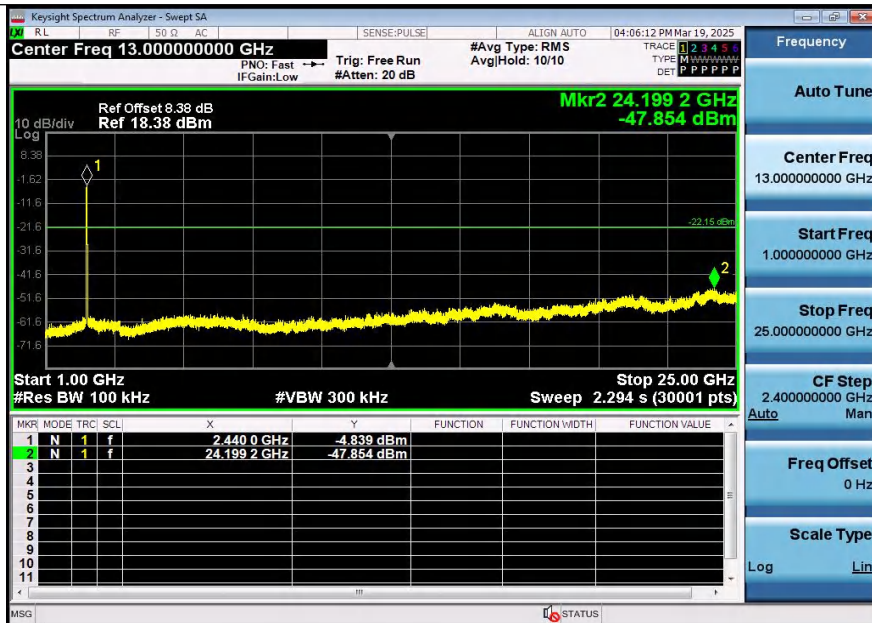




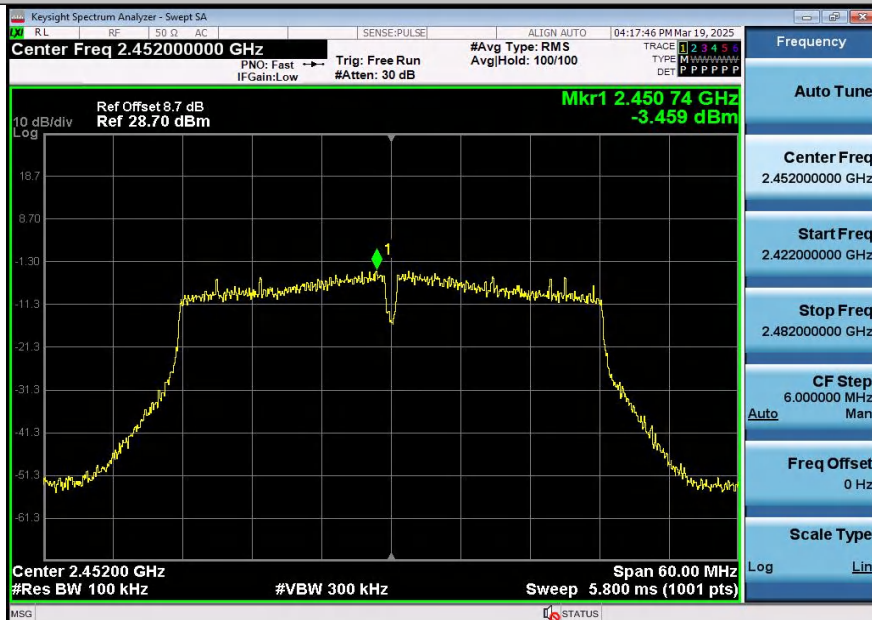
11N40SISO-Ant2-2437-0~Reference-PASS



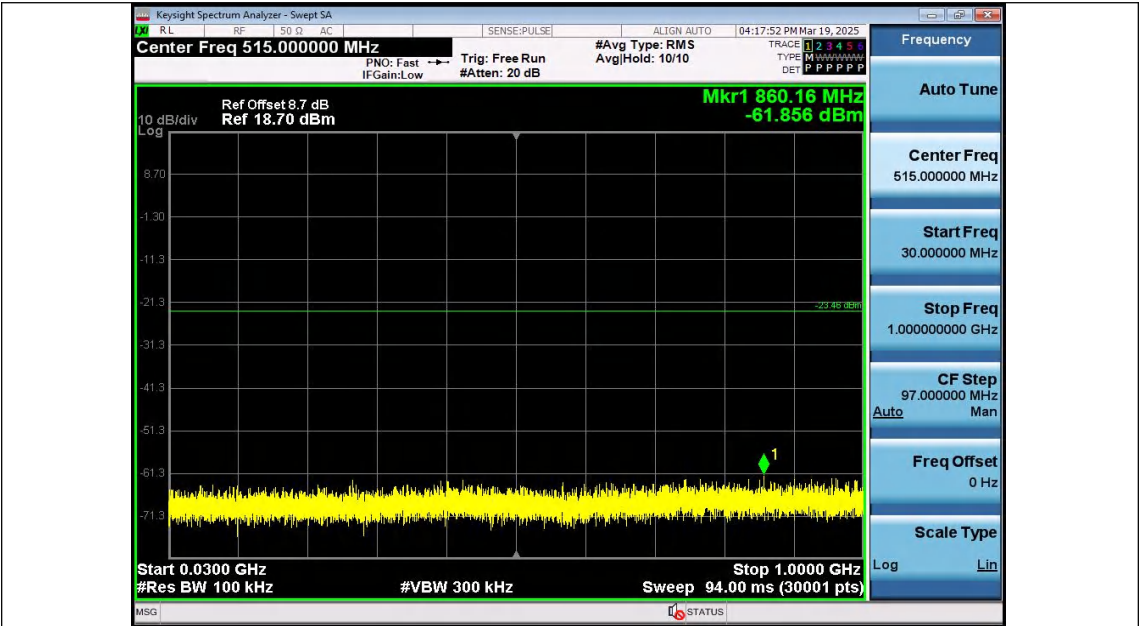
11N40SISO-Ant2-2437-30~1000-PASS



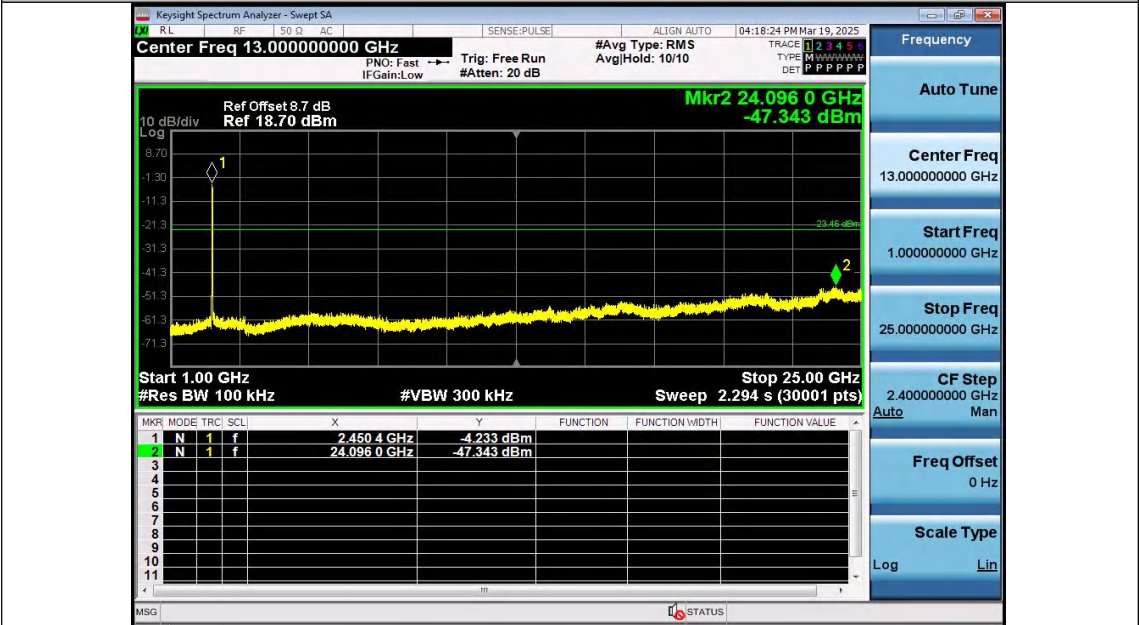
11N40SISO-Ant2-2437-1000~25000-PASS



11N40SISO-Ant2-2452-0~Reference-PASS



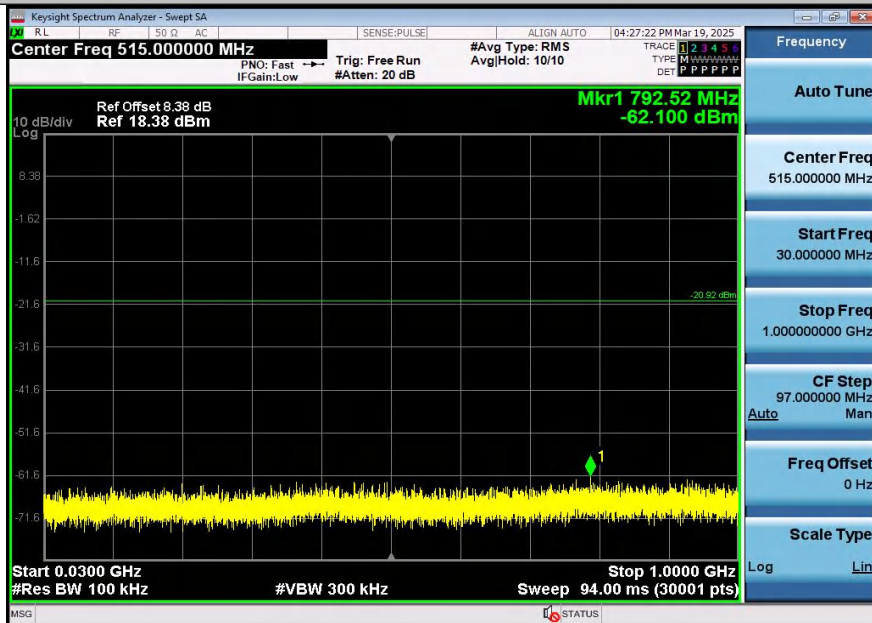
11N40SISO-Ant2-2452-30~1000-PASS



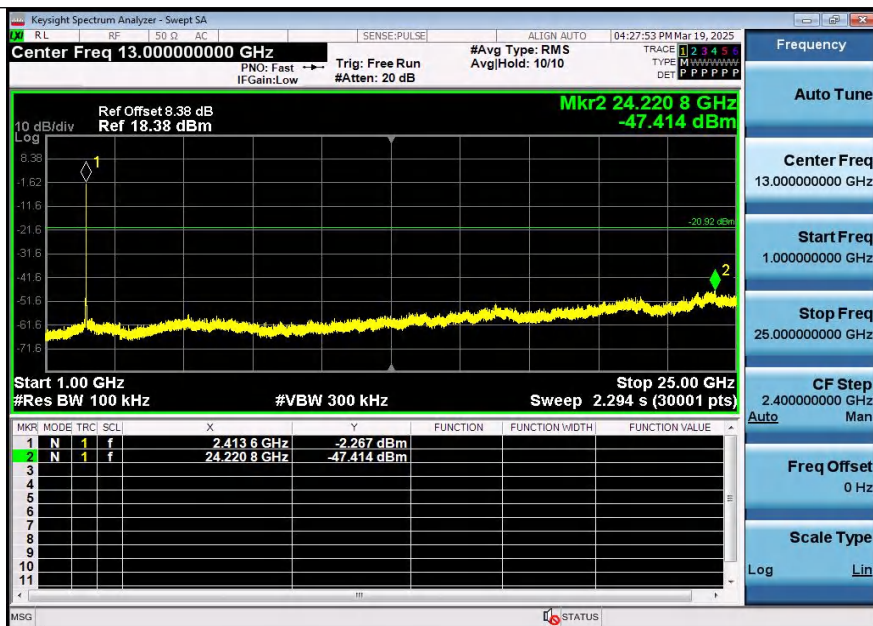
11N40SISO-Ant2-2452-1000~25000-PASS



11AX20SISO-Ant2-2412-0~Reference-PASS



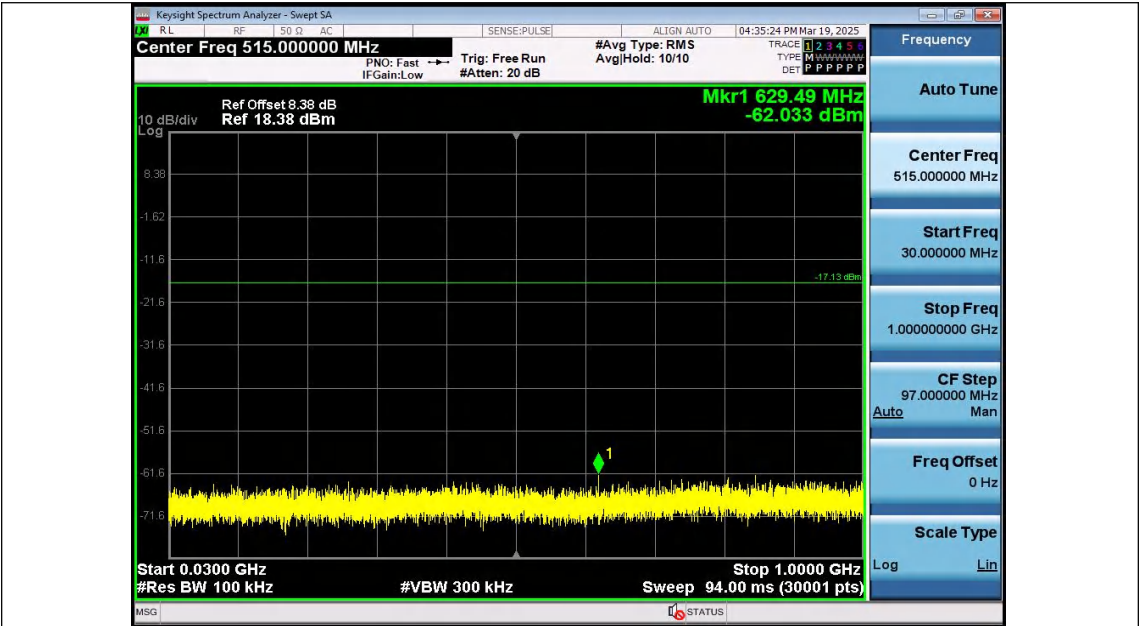
11AX20SISO-Ant2-2412-30~1000-PASS



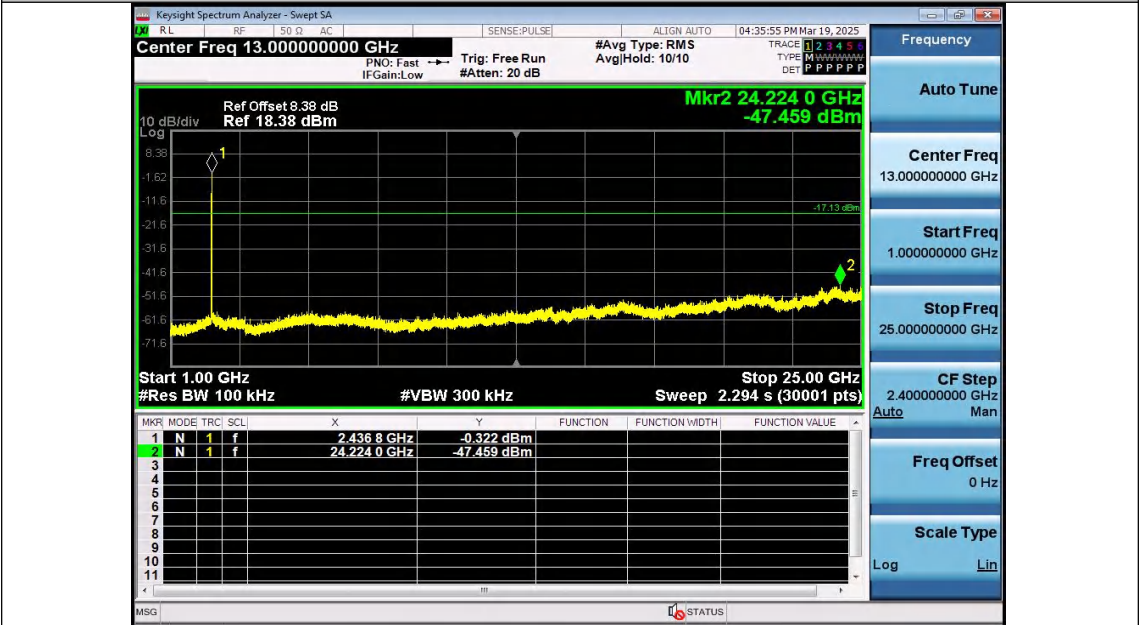
11AX20SISO-Ant2-2412-1000~25000-PASS



11AX20SISO-Ant2-2437-0~Reference-PASS



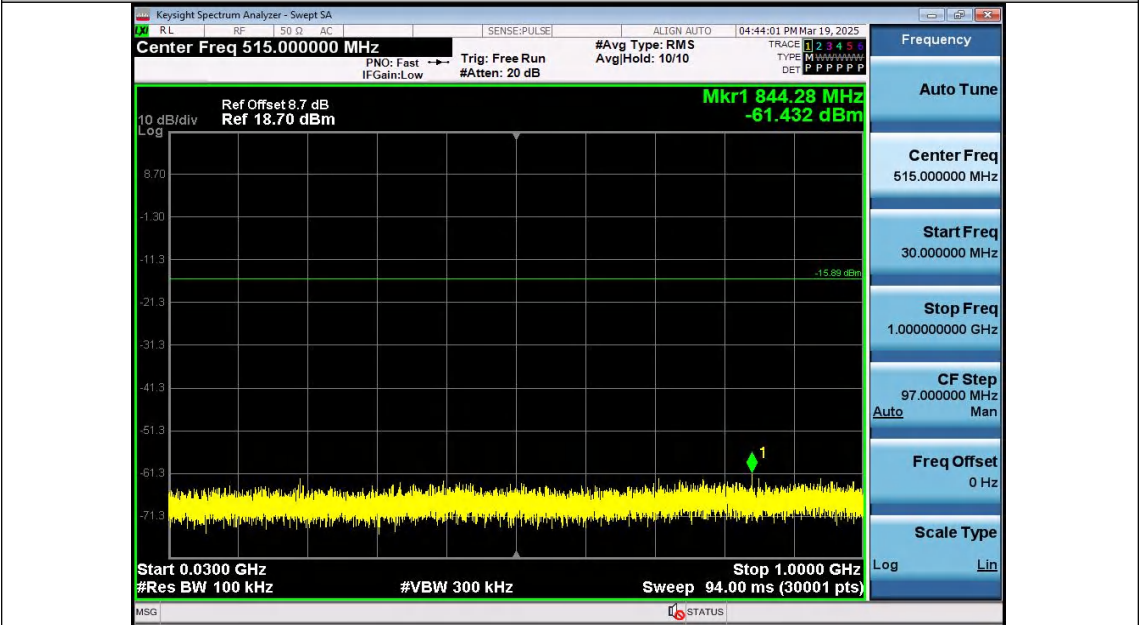
11AX20SISO-Ant2-2437-30~1000-PASS



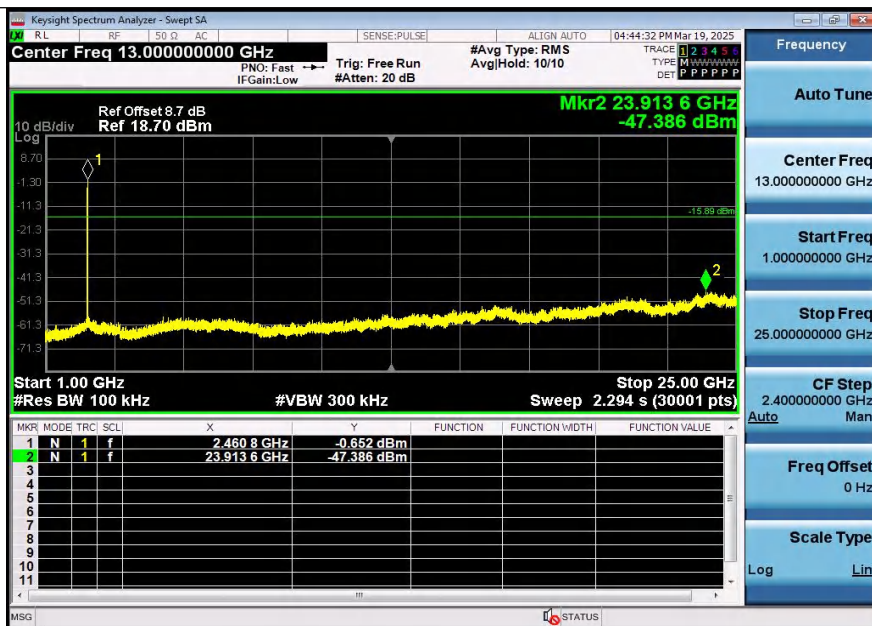
11AX20SISO-Ant2-2437-1000~25000-PASS



11AX20SISO-Ant2-2462-0~Reference-PASS



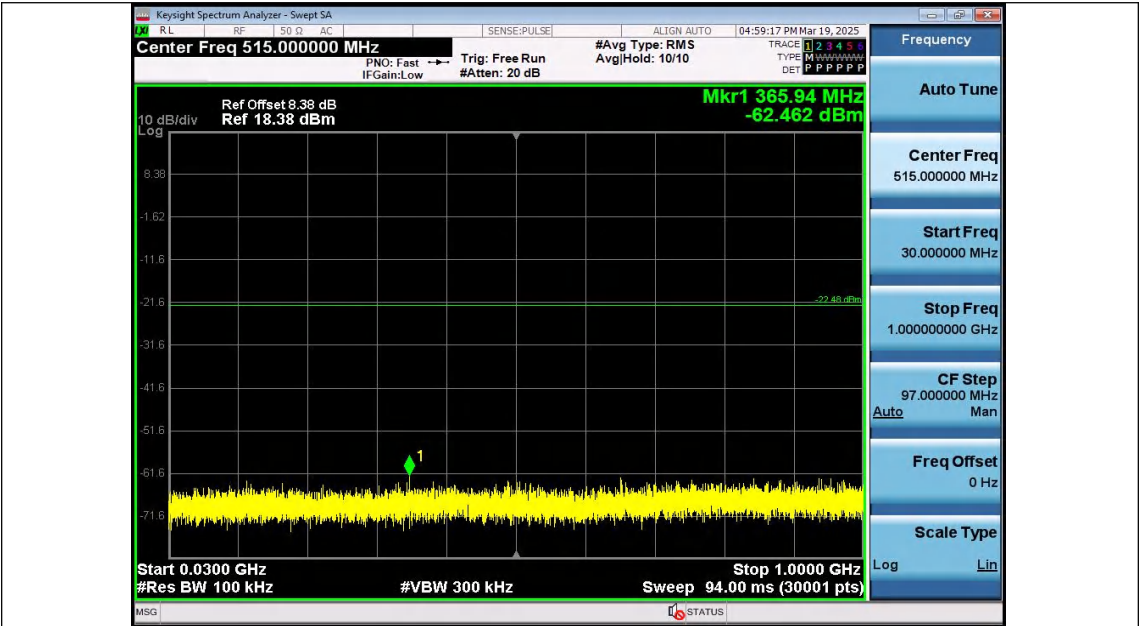
11AX20SISO-Ant2-2462-30~1000-PASS



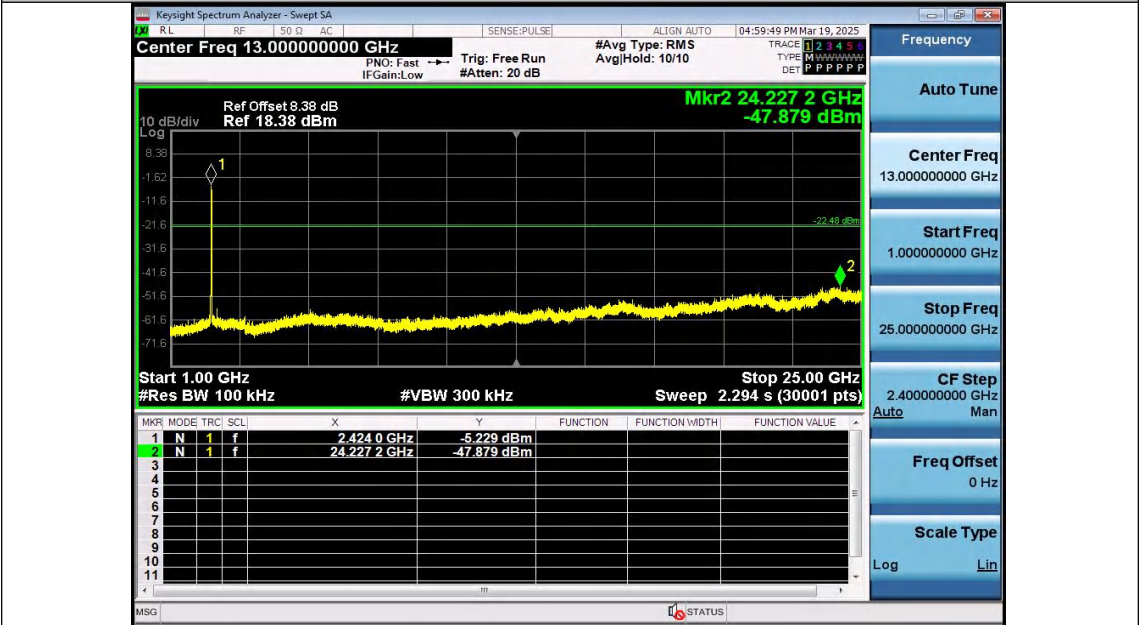
11AX20SISO-Ant2-2462-1000~25000-PASS



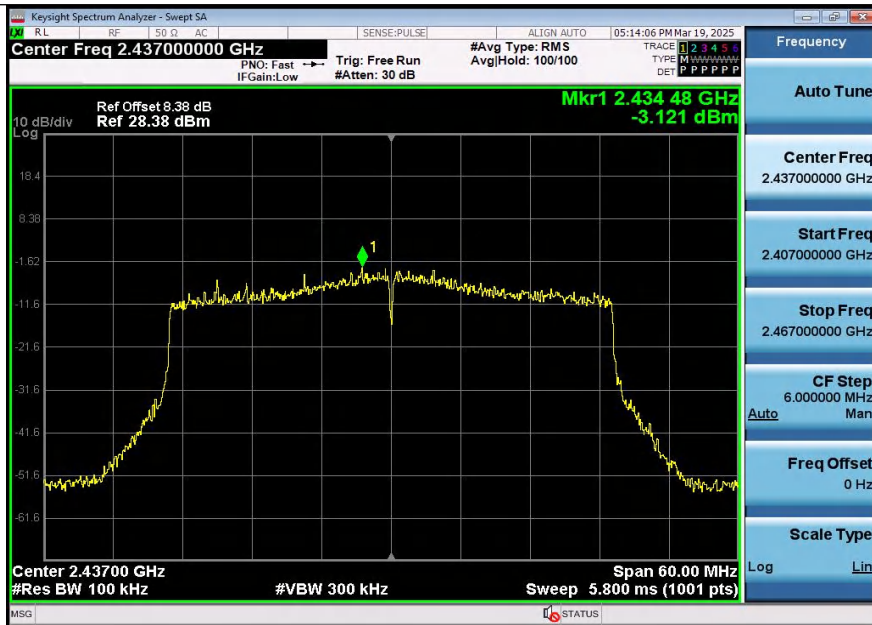
11AX40SISO-Ant2-2422-0~Reference-PASS



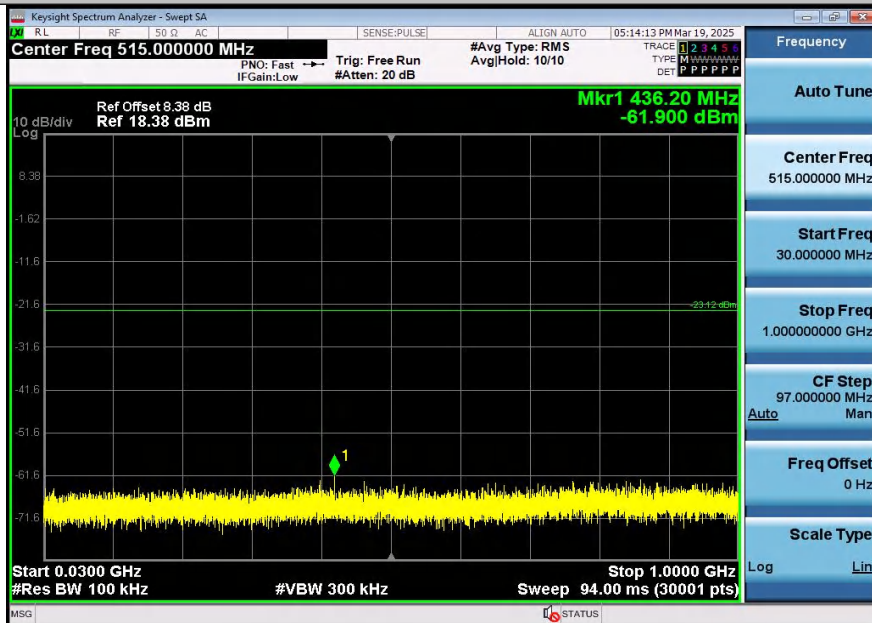
11AX40SISO-Ant2-2422-30~1000-PASS



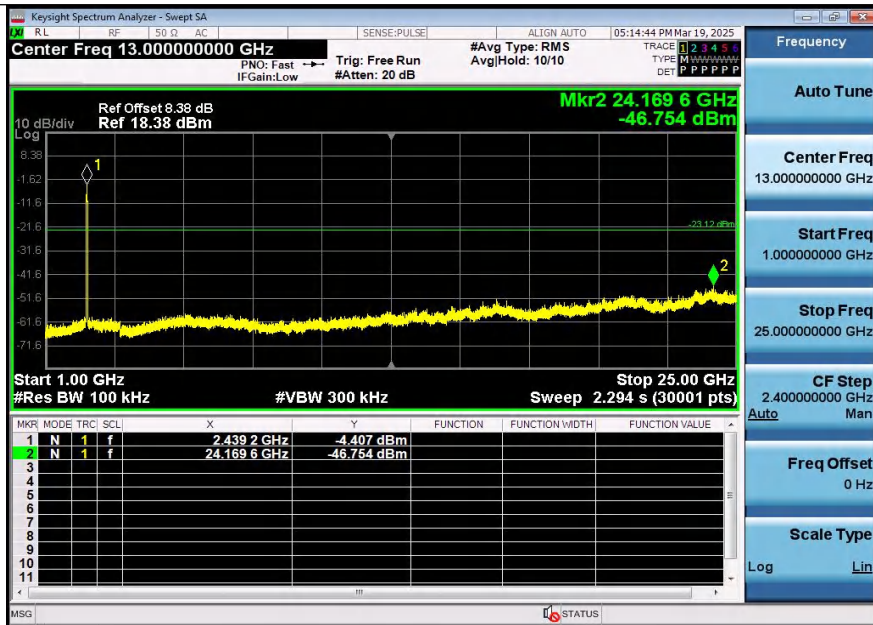
11AX40SISO-Ant2-2422-1000~25000-PASS



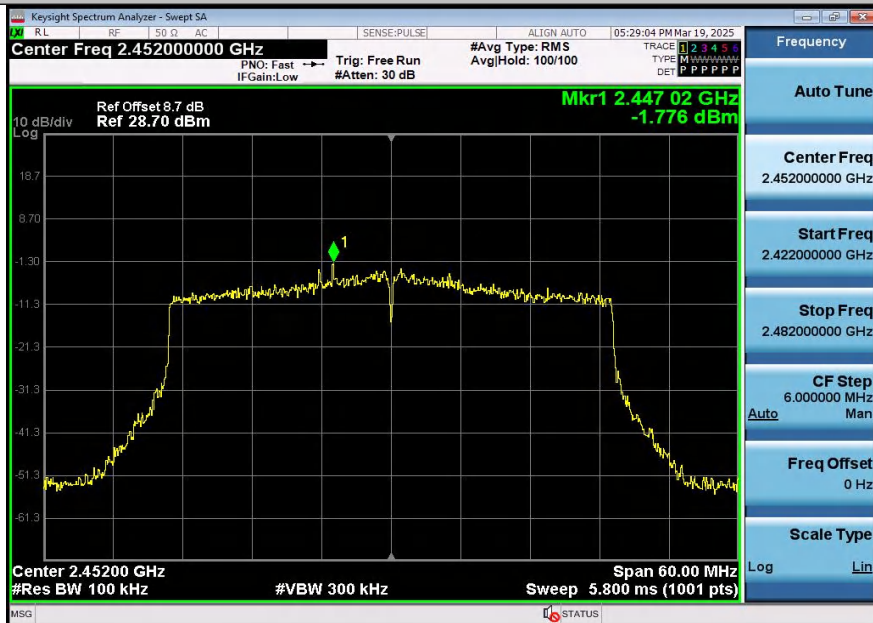
11AX40SISO-Ant2-2437-0~Reference-PASS



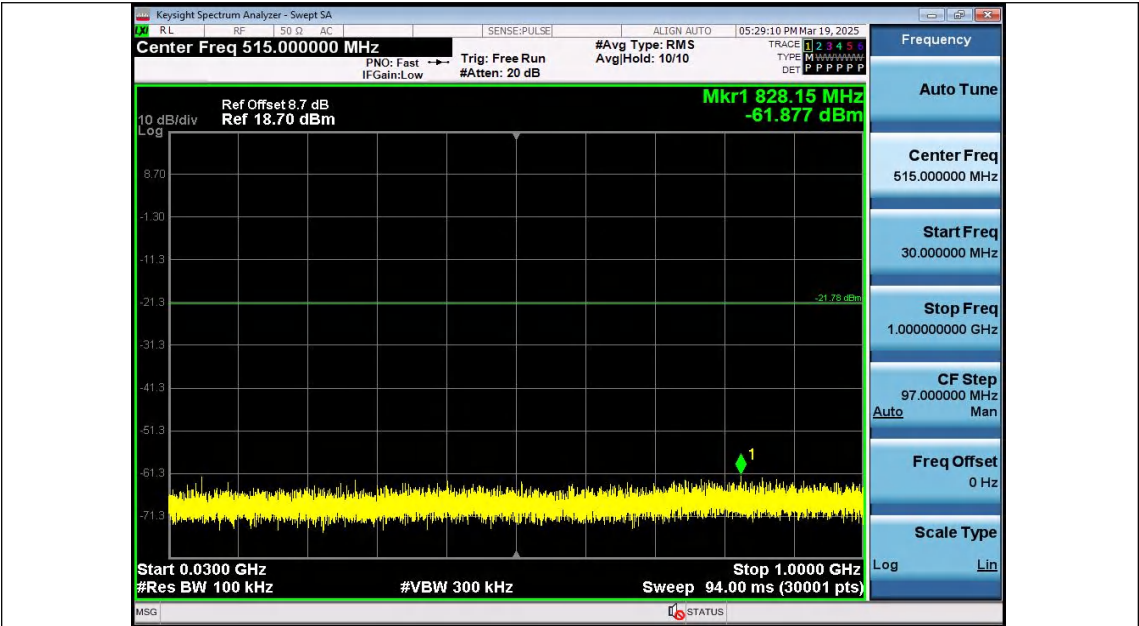
11AX40SISO-Ant2-2437-30~1000-PASS



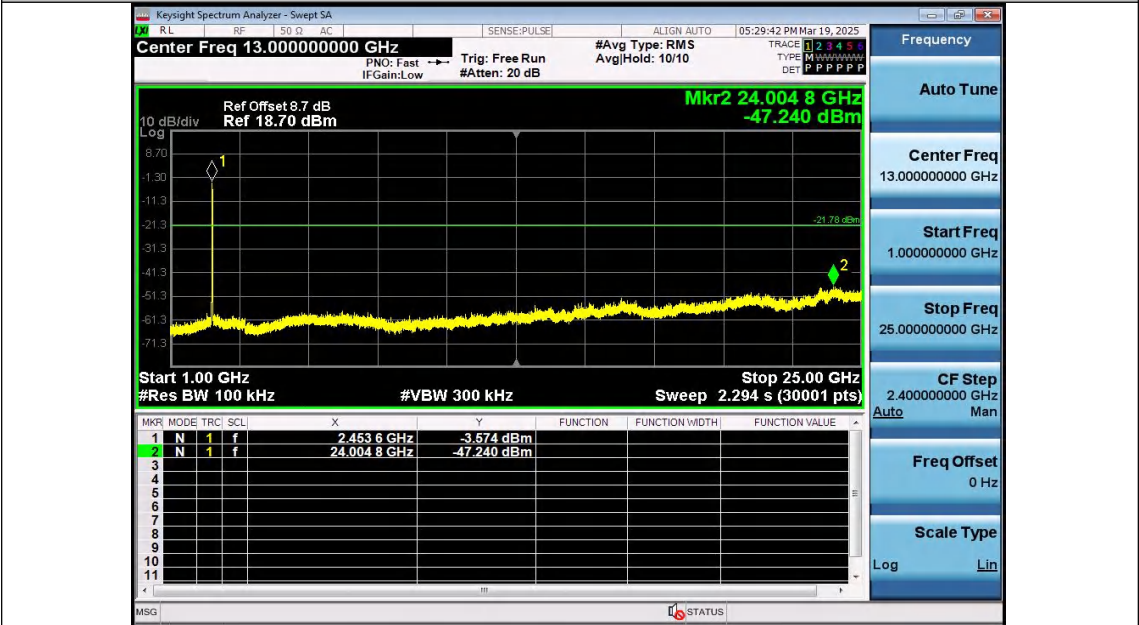
11AX40SISO-Ant2-2437-1000~25000-PASS



11AX40SISO-Ant2-2452-0~Reference-PASS



11AX40SISO-Ant2-2452-30~1000-PASS



11AX40SISO-Ant2-2452-1000~25000-PASS