KV-803 Circuit Description

1. Power Supply

   The whole headset's power provided by a Li-poly battery, which supply to CPU and MIC_BAIS IC after filtered by C11.

2. R16 and R15 are test the pressure of battery, If any change in Battery's pressure, it will handle after test by BC4131596's AIO_0.

3. R4 is reset resistance, when the RESETB turn down, Chip reset, it shows forbidden reset in picture.

4. SPKR_P and SPKR_N are the output of speaker, and it directly connect with the headset.

5. MIC+ and MIC- are the input of Microphone, they enter the input port after coupling by C8 and C9, U1 provide the Bios for MIC.

6. Y1, C13 and C18 provide the 16M surge for the CPU.

7. LED1, LED2, R13, R14 are the working directive circuit, it drive by CPU and give different directions according to the working status of headset.

8. SW1, SW2, SW3, R2, R3 are the chip control circuit, they control the main operation of headset for example turn on, turn off , answering call.

9. D1 is backward protective diode, and C25, C20, C17, C22, C16 are 1.8V regulated circuit. 1.8V provided by CPU.

10. U5 is EEPROM, it transfer the data to CPU.

11. L4, C28, F1, L4 are Antenna matching circuit, FL1 transform the the Bluetooth signal which received by antenna, then transfer the signal to the CPU for processing.