

7.4.2. Conducted Spurious Emissions

Low Band-edge Lowest Channel & Modulation : GFSK

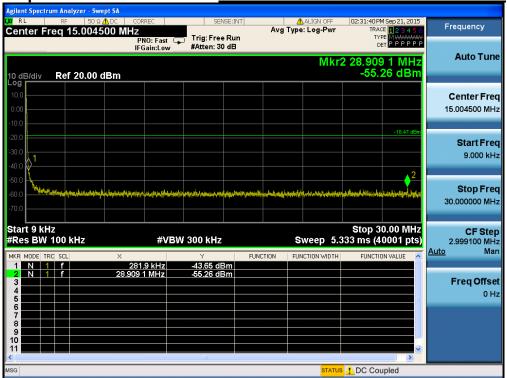


Low Band-edge <u>Hopping mode & Modulation : GFSK</u>





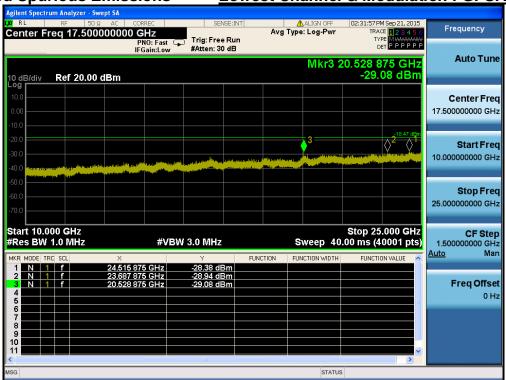
Conducted Spurious Emissions <u>Lowest Channel & Modulation : GFSK</u>











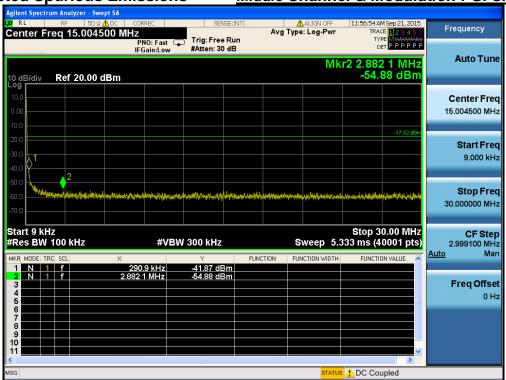


Reference for limit

Middle Channel & Modulation : GFSK

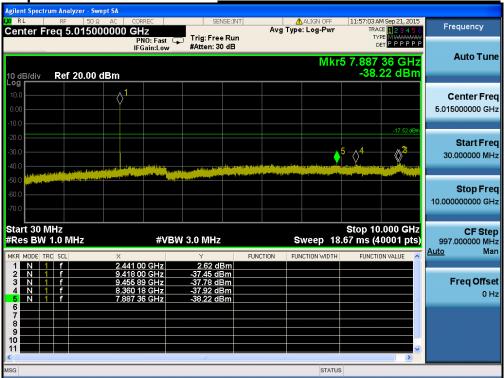


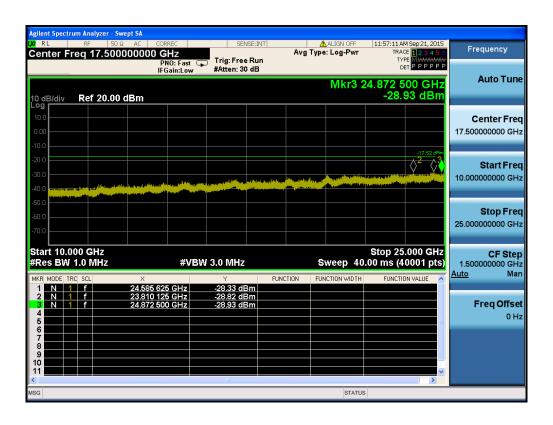
Conducted Spurious Emissions <u>Middle Channel & Modulation : GFSK</u>





Conducted Spurious Emissions <u>Middle Channel & Modulation : GFSK</u>







High Band-edge

Highest Channel & Modulation : GFSK



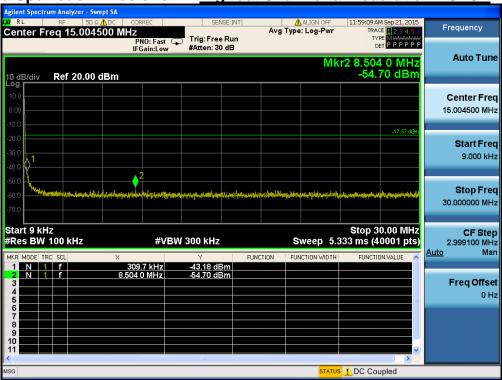
High Band-edge

Hopping mode & Modulation: GFSK





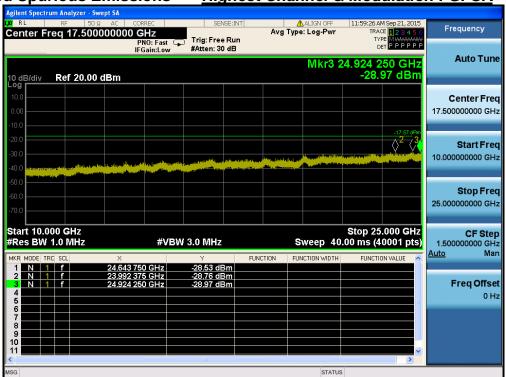














Low Band-edge

Lowest Channel & Modulation : π/4DQPSK



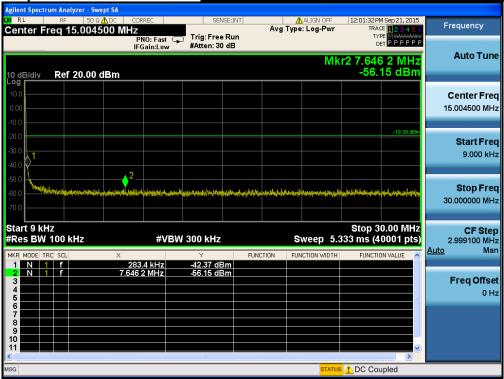
Low Band-edge

Hopping mode & Modulation : π/4DQPSK





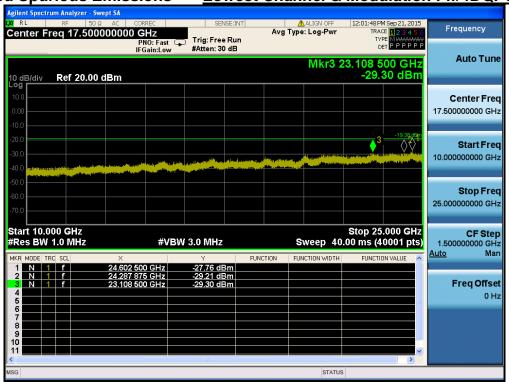
Conducted Spurious Emissions <u>Lowest Channel & Modulation : π/4DQPSK</u>







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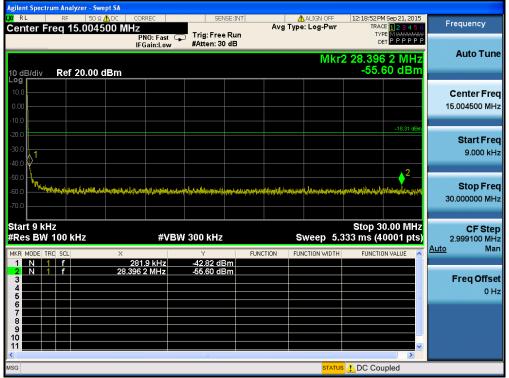


Reference for limit

Middle Channel & Modulation : π/4DQPSK



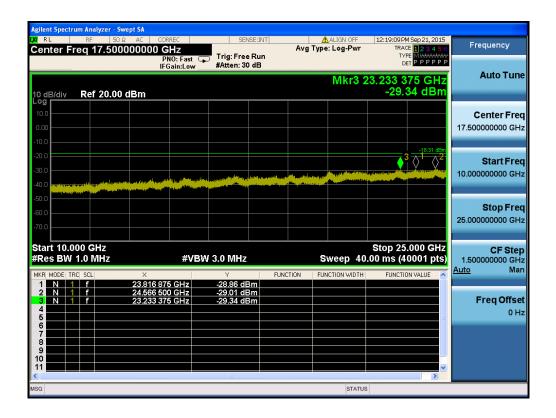
Conducted Spurious Emissions <u>Middle Channel & Modulation : π/4DQPSK</u>





Conducted Spurious Emissions <u>Middle Channel & Modulation : π/4DQPSK</u>







High Band-edge

Highest Channel & Modulation : π/4DQPSK



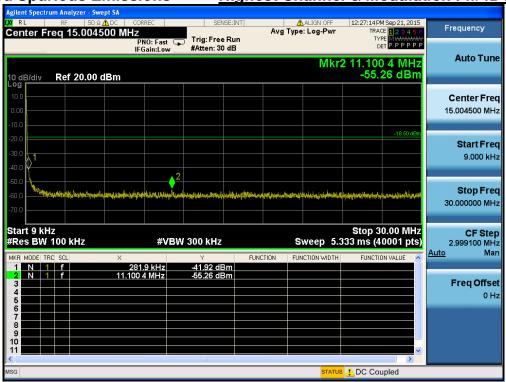
High Band-edge

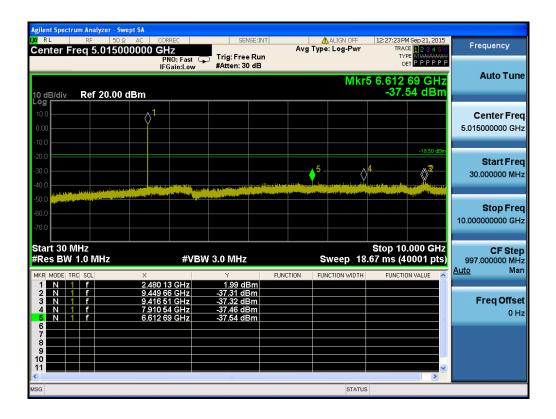
Hopping mode & Modulation : π/4DQPSK





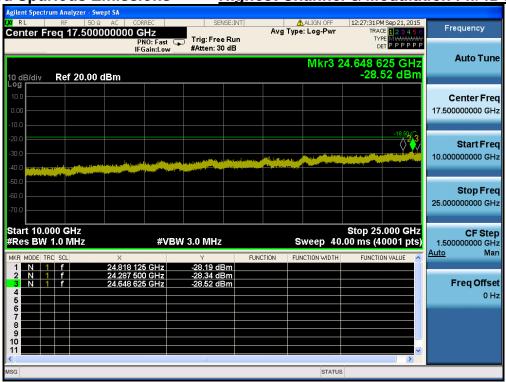
Conducted Spurious Emissions <u>Highest Channel & Modulation : π/4DQPSK</u>







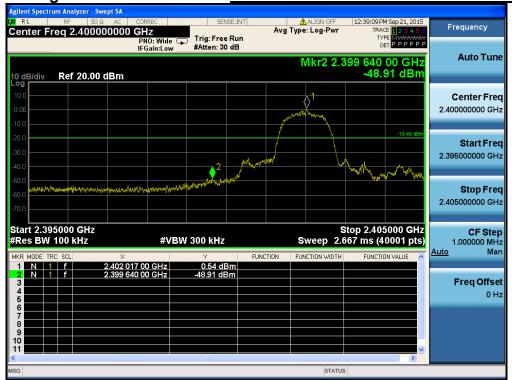
Conducted Spurious Emissions <u>Highest Channel & Modulation : π/4DQPSK</u>





Low Band-edge

Lowest Channel & Modulation: 8DPSK



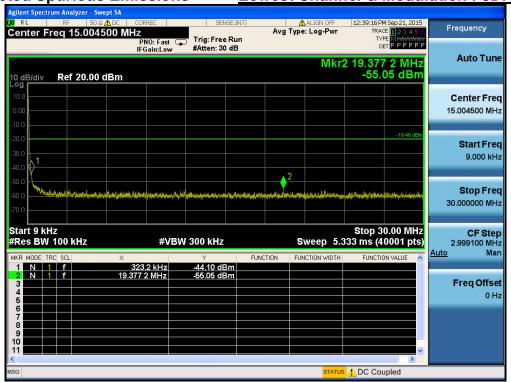
Low Band-edge

Hopping mode & Modulation: 8DPSK





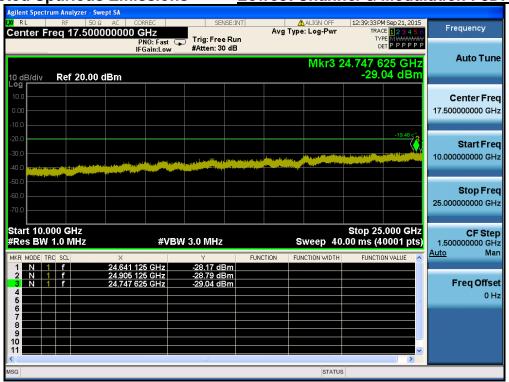
Conducted Spurious Emissions <u>Lowest Channel & Modulation : 8DPSK</u>







Conducted Spurious Emissions <u>Lowest Channel & Modulation : 8DPSK</u>



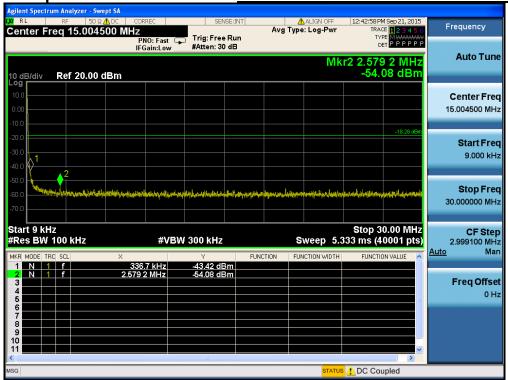


Reference for limit

Middle Channel & Modulation: 8DPSK



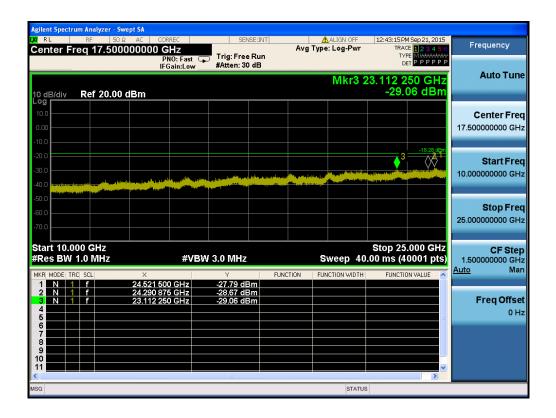
Conducted Spurious Emissions Middle Channel & Modulation : 8DPSK





Conducted Spurious Emissions <u>Middle Channel & Modulation : 8DPSK</u>







High Band-edge

Highest Channel & Modulation: 8DPSK



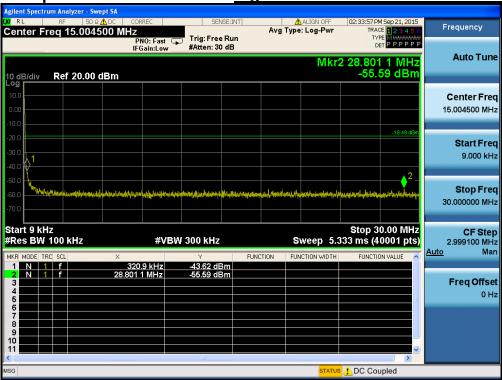
High Band-edge

Hopping mode & Modulation: 8DPSK





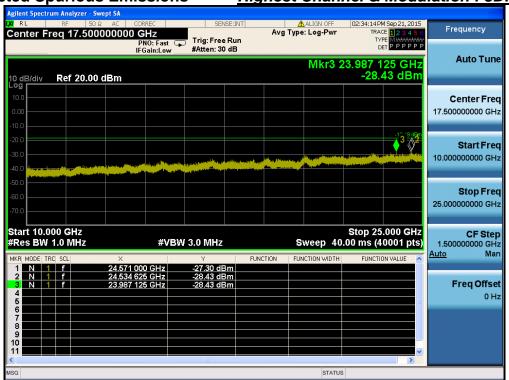
Conducted Spurious Emissions <u>Highest Channel & Modulation : 8DPSK</u>







Conducted Spurious Emissions <u>Highest Channel & Modulation : 8DPSK</u>





8. Transmitter AC Power Line Conducted Emission

8.1 Test Setup

Refer to test setup photo.

8.2 Limit

According to §15.207(a) for an intentional radiator that is designed to be connected to the public utility (AC) power line, the radio frequency voltage that is conducted back onto the AC power line on any frequency or frequencies, within the band 150 kHz to 30 MHz, shall not exceed the limits in the following table, as measured using a 50 uH/50 ohm line impedance stabilization network (LISN).

Compliance with the provision of this paragraph shall on the measurement of the radio frequency voltage between each power line and ground at the power terminal. The lower applies at the boundary between the frequency ranges.

Fraguency Bongo (MHz)	Conducted Limit (dBuV)				
Frequency Range (MHz)	Quasi-Peak	Average			
0.15 ~ 0.5	66 to 56 *	56 to 46 *			
0.5 ~ 5	56	46			
5 ~ 30	60	50			

^{*} Decreases with the logarithm of the frequency

8.3 Test Procedures

Conducted emissions from the EUT were measured according to the ANSI C63.10.

- 1. The test procedure is performed in a 6.5 m × 3.5 m × 3.5 m (L × W × H) shielded room. The EUT along with its peripherals were placed on a 1.0 m (W) × 1.5 m (L) and 0.8 m in height wooden table and the EUT was adjusted to maintain a 0.4 meter space from a vertical reference plane.
- 2. The EUT was connected to power mains through a line impedance stabilization network (LISN) which provides 50 ohm coupling impedance for measuring instrument and the chassis ground was bounded to the horizontal ground plane of shielded room.
- 3. All peripherals were connected to the second LISN and the chassis ground also bounded to the horizontal ground plane of shielded room.
- 4. The excess power cable between the EUT and the LISN was bundled. The power cables of peripherals were unbundled. All connecting cables of EUT and peripherals were moved to find the maximum emission.



8.4 Test Results

AC Line Conducted Emissions (Graph) = Modulation : <u>GFSK</u>

Results of Conducted Emission

 DTNC
 Date : 2015-09-16

 Order No.
 :
 Referrence No.
 :

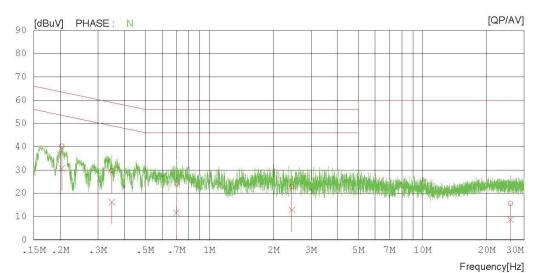
 Model No.
 :
 SPP-R310
 Power Supply
 :
 120V 60Hz

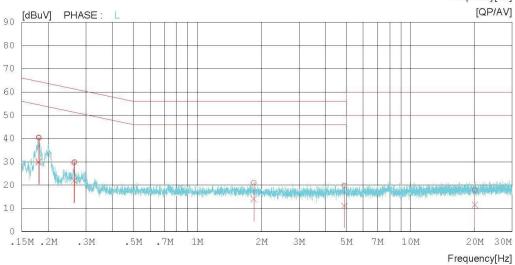
 Serial No.
 :
 Identical prototype
 Temp/Humi.
 :
 22 °C 42 % R.H.

 Test Condition
 :
 BT /Hopping
 Operator
 :
 C.M.KIM

LIMIT : FCC P15.207 QP FCC P15.207 AV

Memo







AC Line Conducted Emissions (List) = Modulation : <u>GFSK</u>

Results of Conducted Emission

DTNC Date: 2015-09-16

Order No. Model No. Serial No. Test Condition

Referrence No. :
SPP-R310 Power Supply : 120V 60Hz
Identical prototype Temp/Humi. : 22 'C 42 % R.H.
BT Operator : C.M.KIM

Memo : BT /Hopping

LIMIT : FCC P15.207 QP FCC P15.207 AV

NO	FREQ	READ QP [dBuV]	ING AV [dBuV]	C.FACTOR	QP	ULT AV [dBuV]	LII QP [dBuV]	MIT AV][dBuV]	MA QP [dBuV	RGIN AV][dBuV]	PHASE
1	0.20279	30.1	20.5	10.1	40.2	30.6	63.5	53.5	23.3	22.9	N
2	0.34795	19.6	6.1	10.1	29.7	16.2	59.0	49.0	29.3	32.8	N
3	0.70104	13.9	1.5	10.1	24.0	11.6	56.0	46.0	32.0	34.4	N
4	2.43920	12.6	2.8	10.2	22.8	13.0	56.0	46.0	33.2	33.0	N
5	25.88180	4.7	-2.1	10.8	15.5	8.7	60.0	50.0	44.5	41.3	N
6	0.18054	30.1	20.1	10.1	40.2	30.2	64.5	54.5	24.3	24.3	L
7	0.26400	19.4	11.7	10.1	29.5	21.8	61.3	51.3	31.8	29.5	L
8	1.83900	10.8	3.8	10.2	21.0	14.0	56.0	46.0	35.0	32.0	L
9	4.89280	9.4	0.8	10.3	19.7	11.1	56.0	46.0	36.3	34.9	L
10	20.07960	7.0	0.7	10.8	17.8	11.5	60.0	50.0	42.2	38.5	L
11	0.17994	30.3	19.6	10.1	40.4	29.7	64.5	54.5	24.1	24.8	L
12	0.26452	19.9	11.8	10.1	30.0	21.9	61.3	51.3	31.3	29.4	L





9. Antenna Requirement

Describe how the EUT complies with the requirement that either its antenna is permanently attached, or that it employs a unique antenna connector, for every antenna proposed for use with the EUT.

Conclusion: Comply

The antenna is permanently attached.(Refer to Internal Photo file.)

- Minimum Standard:

An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator shall be considered sufficient to comply with the provisions.

FCC ID: U5MSPP-R310 Report No.: DRTFCC1512-0264



10. Occupied Bandwidth (99 %)

10.1 Test Setup

Refer to the APPENDIX I.

10.2 Limit

Limit: Not Applicable

10.3 Test Procedure

The 99 % power bandwidth was measured with a calibrated spectrum analyzer.

The resolution bandwidth (RBW) shall be in the range of 1 % to 5 % of the occupied bandwidth (OBW) and video bandwidth (VBW) shall be approximately $3 \times RBW$.

Spectrum analyzer plots are included on the following pages.

10.4 Test Results

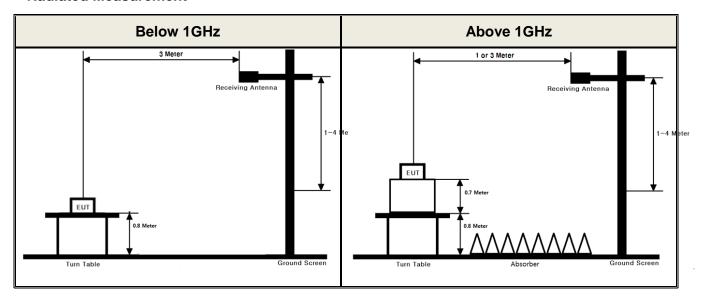
Not Applicable



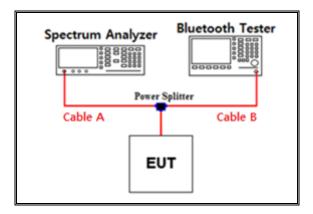
APPENDIX I

Test set up diagrams

Radiated Measurement



Conducted Measurement



Path loss information

Frequency (GHz)	Path Loss (dB)	Frequency (GHz)	Path Loss (dB)
0.03	6.30	15	10.63
1	7.35	20	11.94
2.402 & 2.441 & 2.480	7.85	25	13.22
5	9.75	-	-
10	10.35	-	-

Note 1 : The path loss from EUT to Spectrum analyzer were measured and used for test.

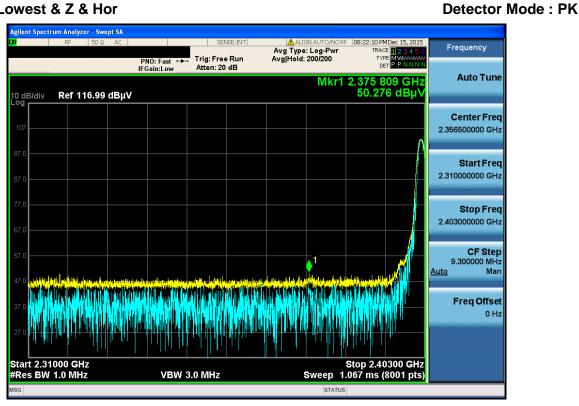
Path loss (S/A's Correction factor) = Cable A



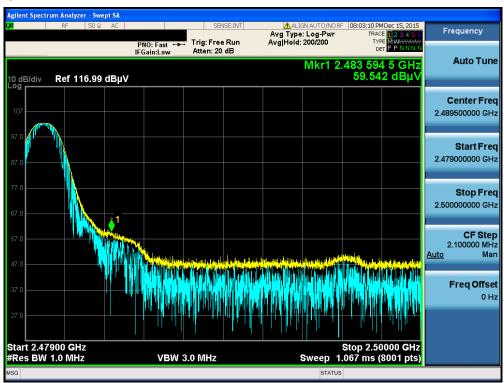
APPENDIX II

Unwanted Emissions (Radiated) Test Plot

GFSK & Lowest & Z & Hor



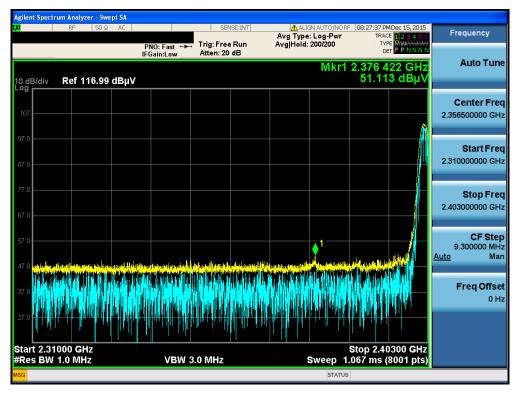
GFSK & Highest & Z & Hor



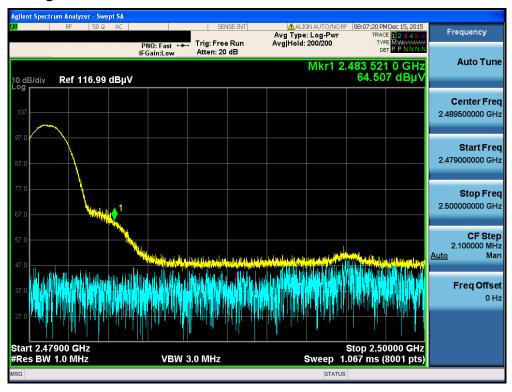


π/4DQPSK & Lowest & Z & Hor

Detector Mode: PK



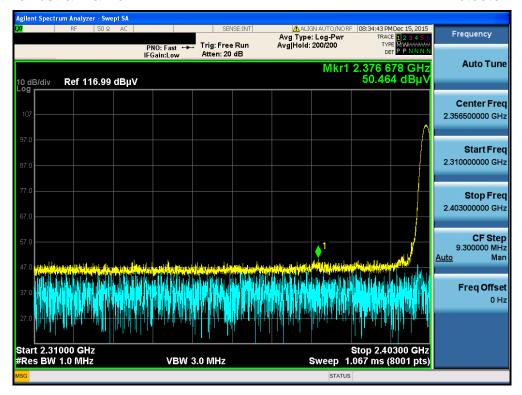
$\pi/4DQPSK$ & Highest & Z & Hor



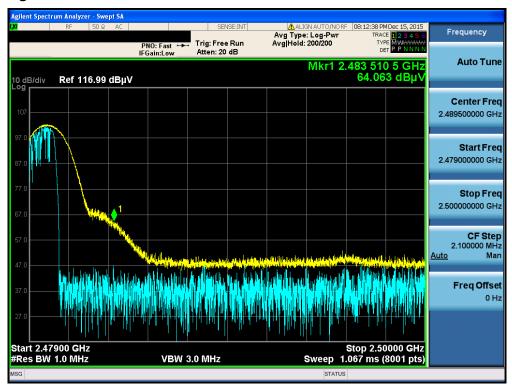


8DPSK & Lowest & Z & Hor

Detector Mode: PK



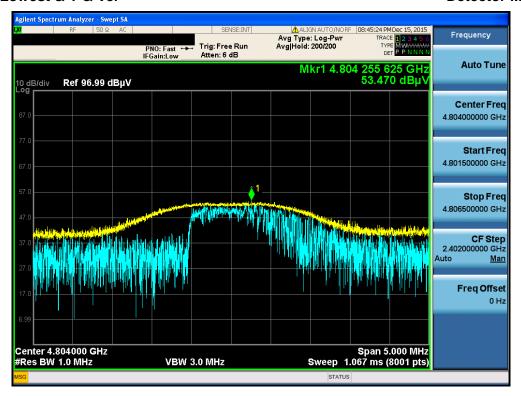
8DPSK & Highest & Z & Hor



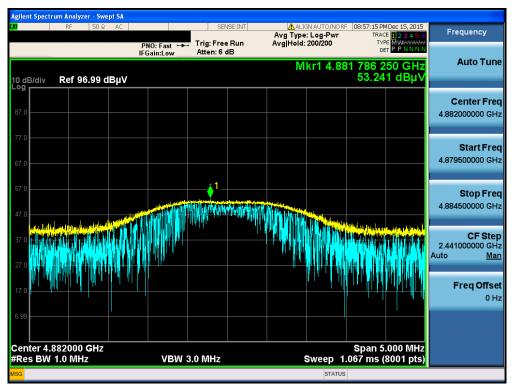


GFSK & Lowest & Y & Ver

Detector Mode: PK



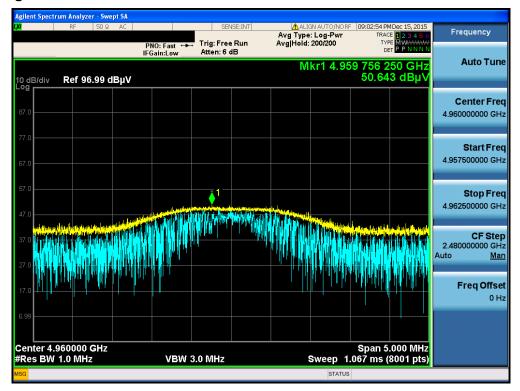
GFSK & Middle & X & Hor



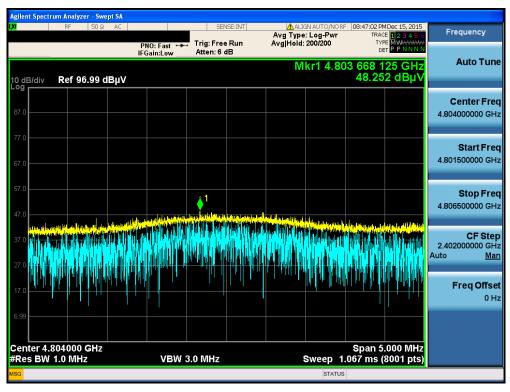


GFSK & Highest & Y & Ver

Detector Mode: PK



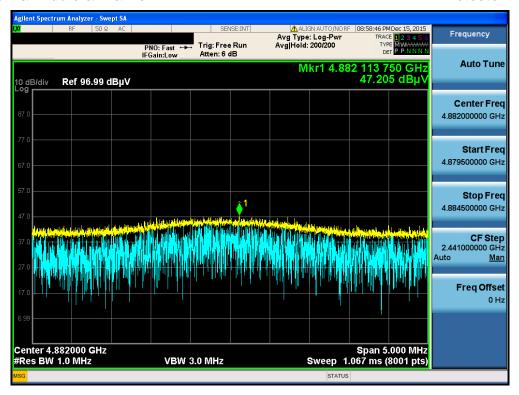
π/4DQPSK & Lowest & Y & Ver



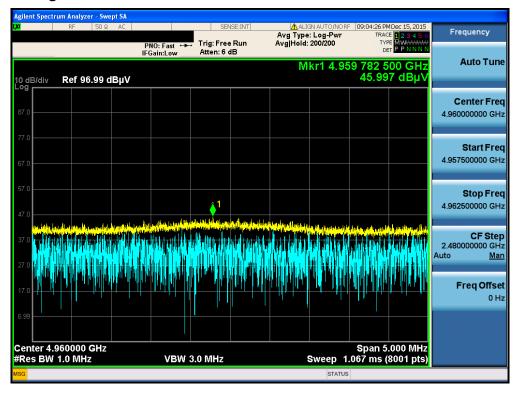


π/4DQPSK & Middle & X & Hor

Detector Mode: PK



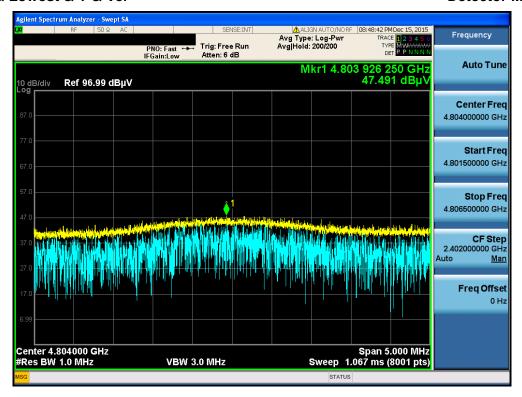
$\pi/4DQPSK$ & Highest & Y & Ver



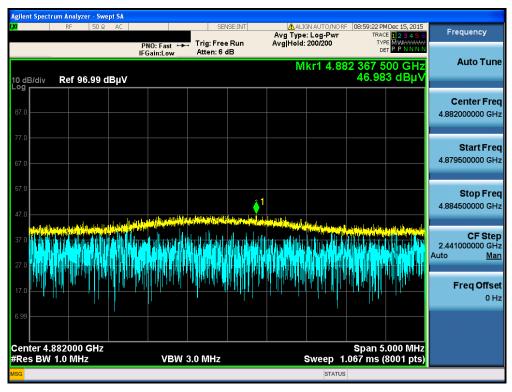


8DPSK & Lowest & Y & Ver

Detector Mode: PK



8DPSK & Middle & X & Hor





8DPSK & Highest & Y & Ver

