

Hi Jackie

I just finished reviewing the updated information sent last Thursday.

- 1 Please note that block diagrams are to be in accordance with 2.1033(a)(5) which states, "A block diagram showing the frequency of all oscillators in the device. The signal path and frequency shall be indicated at each block. The tuning range(s) and intermediate frequency(ies) shall be indicated at each block. A schematic diagram is also required for intentional radiators." Please note that the revised block diagram provided does not address the issues stated above in that no frequencies are indicated. The Block diagram is to provide ALL clocks. No frequency or tuning information as required by 2.1033 was provided on the block diagram. As this is a permit but ask device, the FCC will review this documentation and will probably has issue with the missing information. Please correct.
2. The comments asked for a clearer external picture of the device. The external photos provided in response look nothing like the device in the original photos. I have no idea what the new photos are for. Please explain.
3. Please explain why page 3 of the new report references a PC1. Is this not a new submittal?

Thanks

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From: Jackie Pillai (mflom) <mailto:jackie@mflom.com>; [hoosam@mflom.com](mailto:hoosam@mflom.com)

Hello, is there an update available on the status of this filing? Any news would help. Thank you.