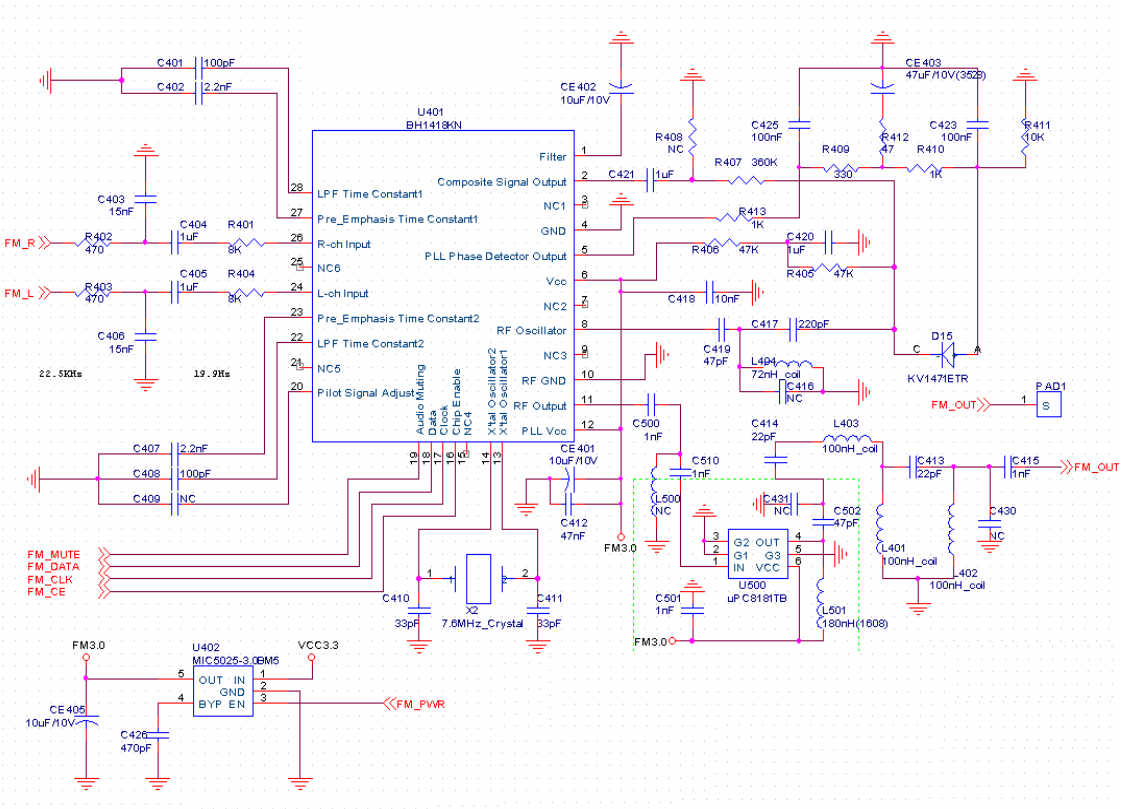
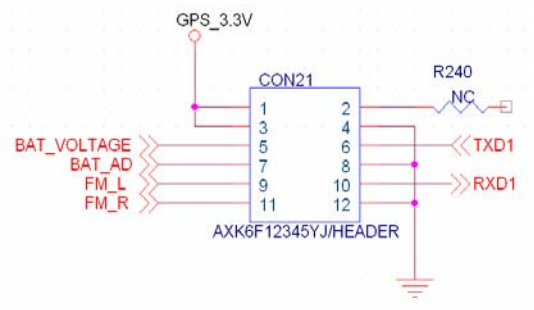
■ FINEDIRVE 400 FMT SCHEMATIC

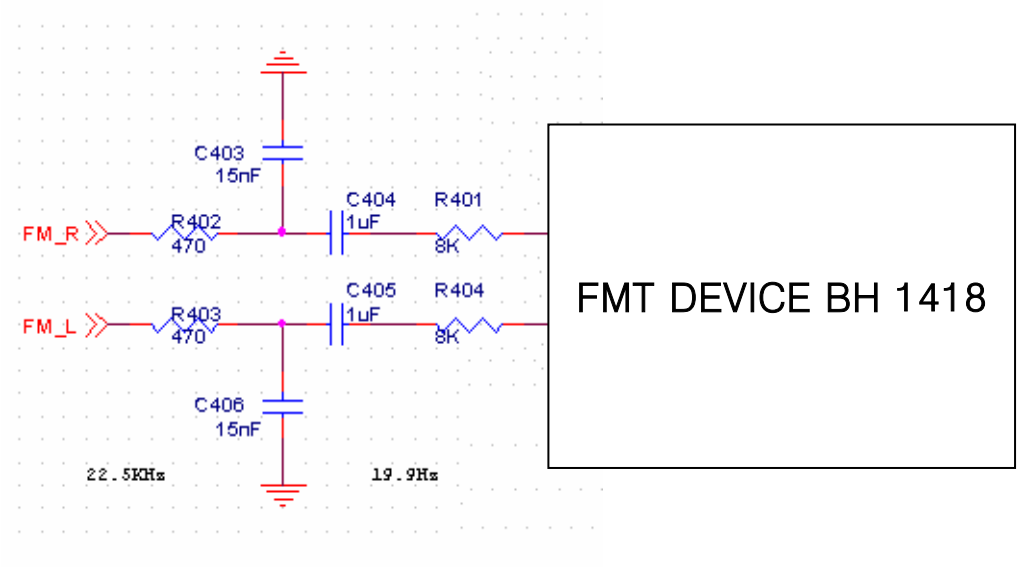


■ FTM AUDIO INPUT SECTION

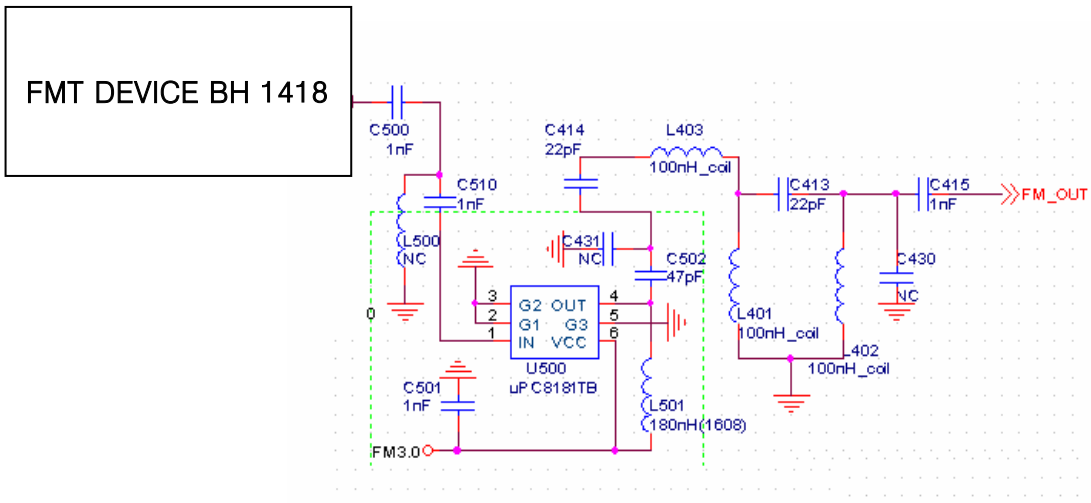


STEREO AUDIO INPUT IS FM_L(LEFT SOUND) AND FM_R (RIGHT SOUND)
PIN NUMBER CON21 = 9PIN, 11PIN

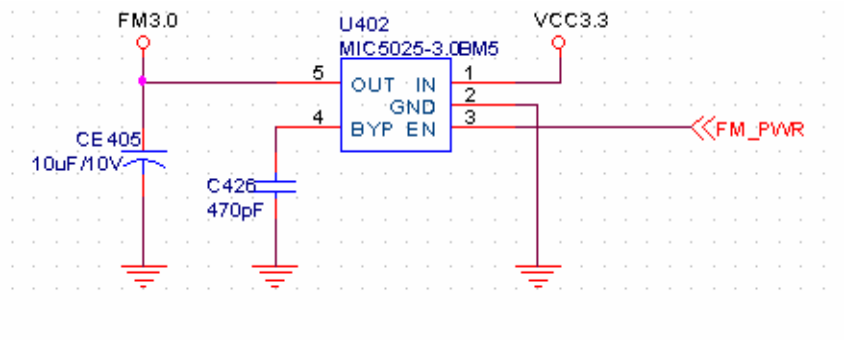
■ SOUND IN PUT CONNECTOR TO FMT DEVICE INTERFACE SECTION



■ FMT DEVIEC OUTPUT AND FILTER SECTION

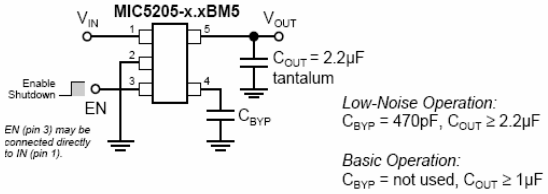


■ FMT POWER SECTION



VCC3.3 INPUT 3.3V
FM3.0 OUTPUT 3.0V
LDO IS 150mA LOW-Noise LDO Regulator

Typical Application



Ultra-Low-Noise Regulator Application

■ FMT SPEC

TYPE **BH1418KN**

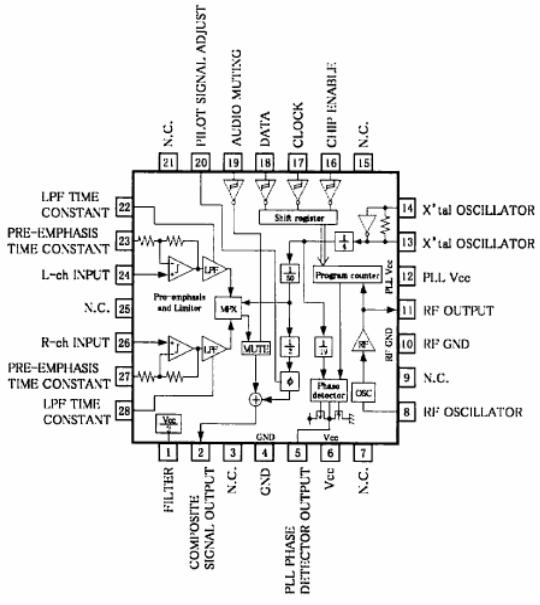
FEATURES
- It is possible to attempt to improve a timbre because it has the pre-emphasis circuit, limiter circuit and low-pass filter circuit.
- Built-in the pilot-tone system FM stereo modulator circuit.
- The transmission frequency is stable because it has PLL system FM transmitter circuit.
- PLL data input (CE, CK, DA) by serial input.
- It is possible for the monaural mode.
- Built-in the sound muting circuit.

○ Absolute Maximum Ratings (Ta=25℃)

Parameter	Symbol	Limits	Unit	Conditions
Supply voltage	Vcc	+7.0	V	Pin 6, 12
Data input voltage	V_{IN-D}	-0.3 ~ Vcc+0.3	V	Pin 16, 17, 18, 19
Phase comparator output voltage	V_{OUT-P}	-0.3 ~ Vcc+0.3	V	Pin 5
Power dissipation	Pd	370	mW	(*1)
Storage temperature	Tstg	-55 ~ +125	℃	

(*1) To use at a temperature higher than Ta=25℃, derate 3.7mW per 1℃.

○ Block Diagram



○ Pin No. · Pin Name

No.	Name	No.	Name
1	FILTER	15	N.C.
2	COMPOSITE SIGNAL OUTPUT	16	CHIP ENABLE
3	N.C.	17	CLOCK
4	GND	18	DATA
5	PLL PHASE DETECTOR OUTPUT	19	AUDIO MUTING
6	Vcc	20	PILOT SIGNAL ADJUST
7	N.C.	21	N.C.
8	RF OSCILLATOR	22	LPF TIME CONSTANT
9	N.C.	23	PRE-EMPHASIS TIME CONSTANT
10	RF GND	24	L-ch INPUT
11	RF OUTPUT	25	N.C.
12	PLL Vcc	26	R-ch INPUT
13	X'tal OSCILLATOR	27	PRE-EMPHASIS TIME CONSTANT
14	X'tal OSCILLATOR	28	LPF TIME CONSTANT