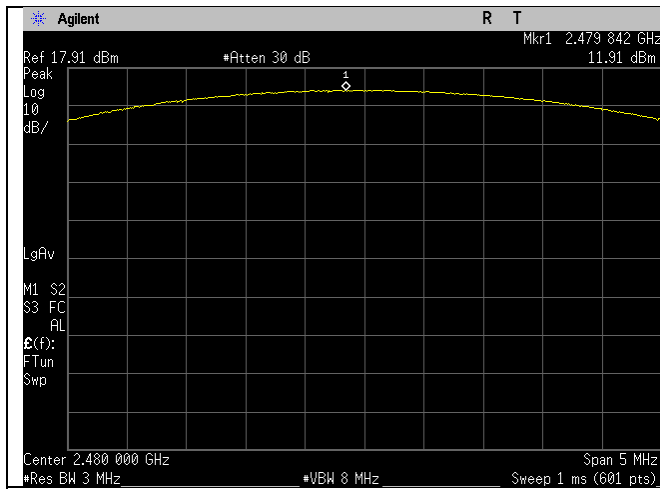


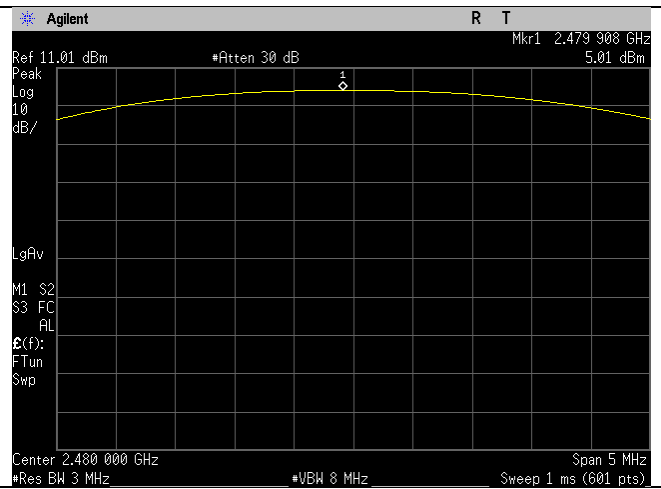
Annex B - Output Power

Naming Convention:

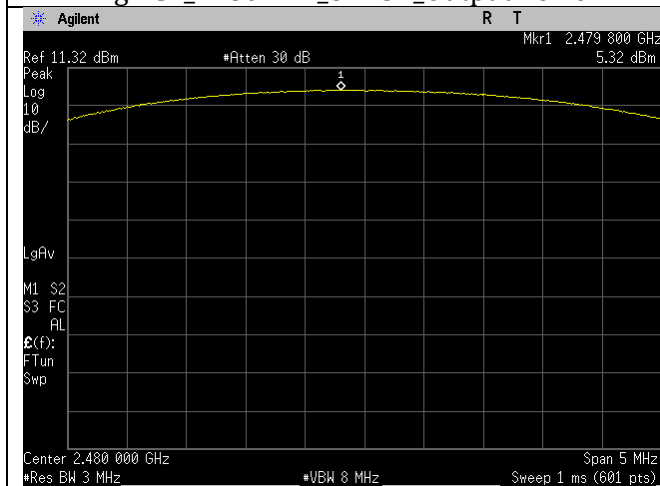
Channel (Low, Mid, High)_Frequency (MHz)_Modulation_ Measurement_(Output Power)



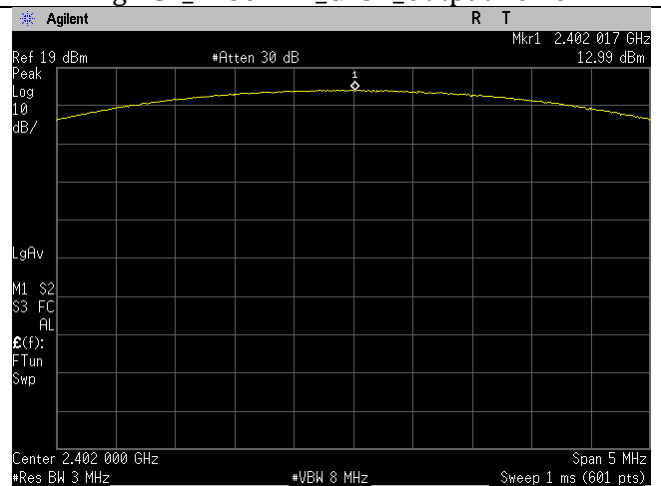
High Ch_2480MHz_8DPSK_Output Power



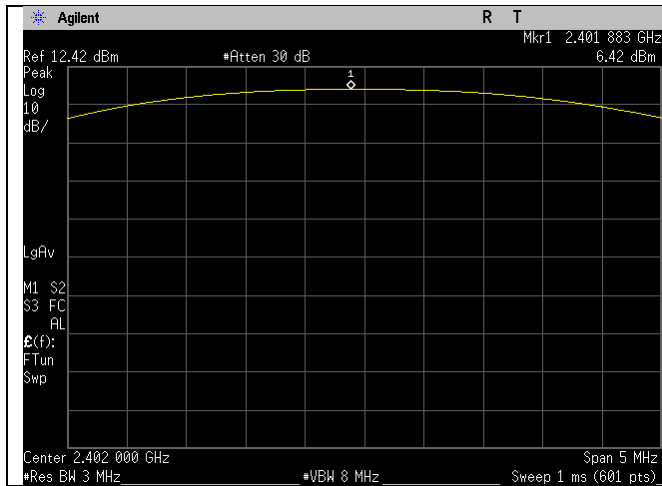
High Ch_2480MHz_GFSK_Output Power



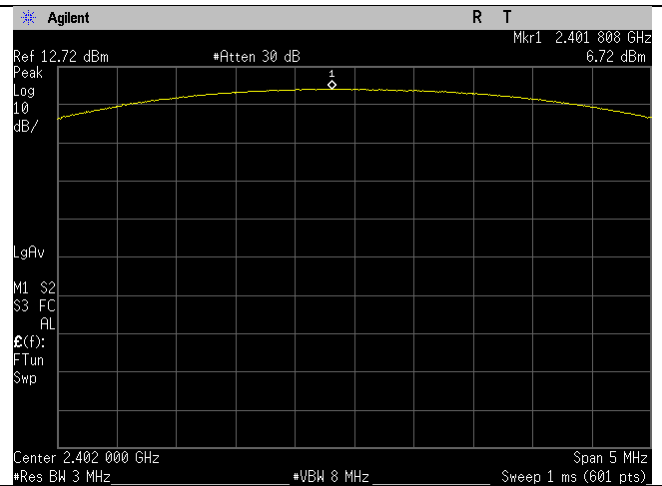
High Ch_2480MHz_Pi_4DQPSK_Output Power



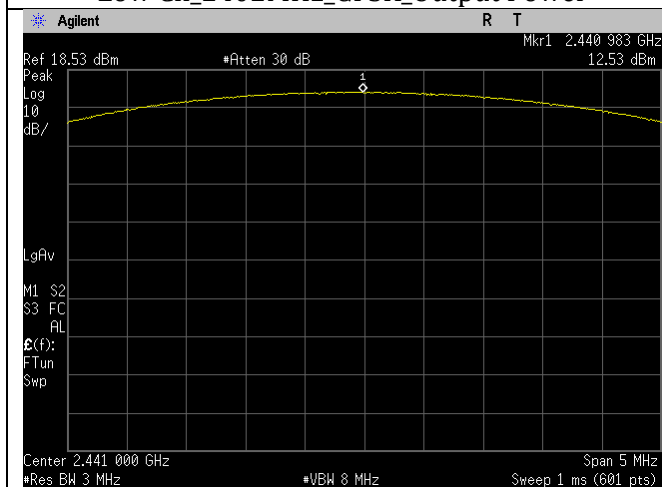
Low Ch_2402MHz_8DPSK_Output Power



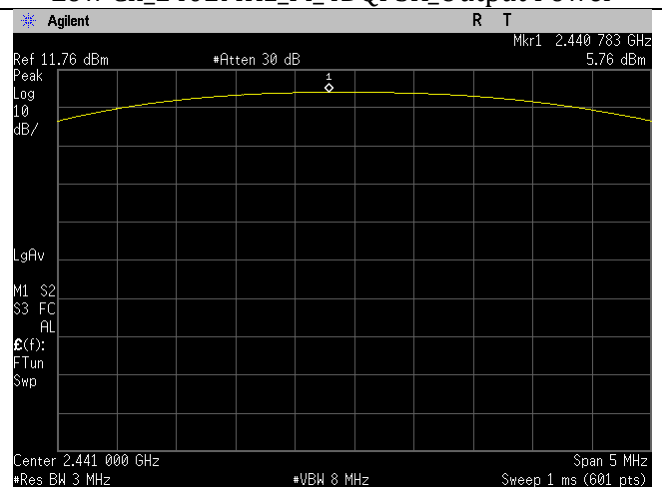
Low Ch_2402MHz_GFSK_Output Power



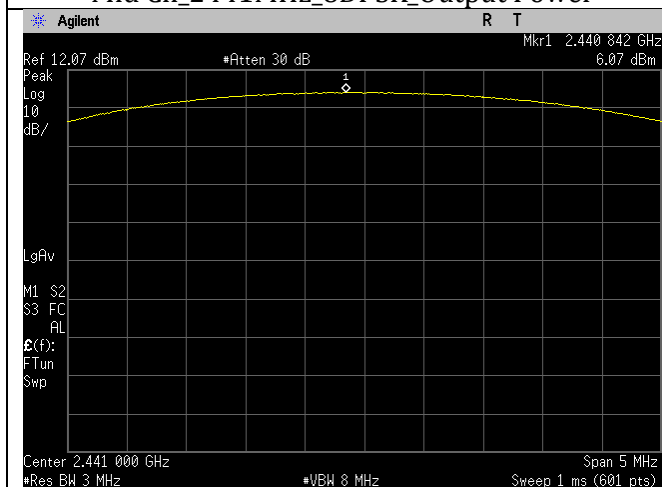
Low Ch_2402MHz_Pi_4DQPSK_Output Power



Mid Ch_2441MHz_8DPSK_Output Power



Mid Ch_2441MHz_GFSK_Output Power



Mid Ch_2441MHz_Pi_4DQPSK_Output Power