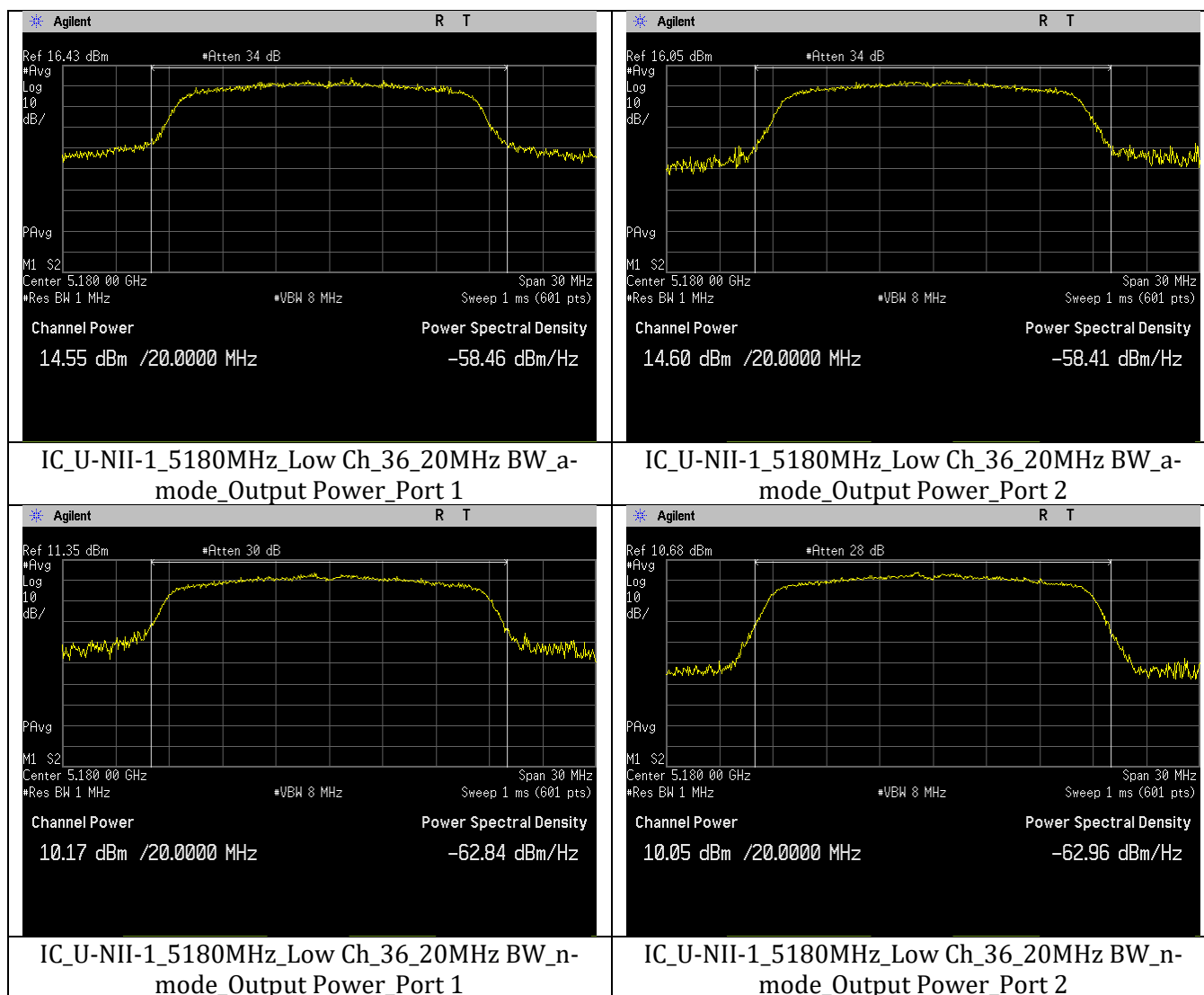
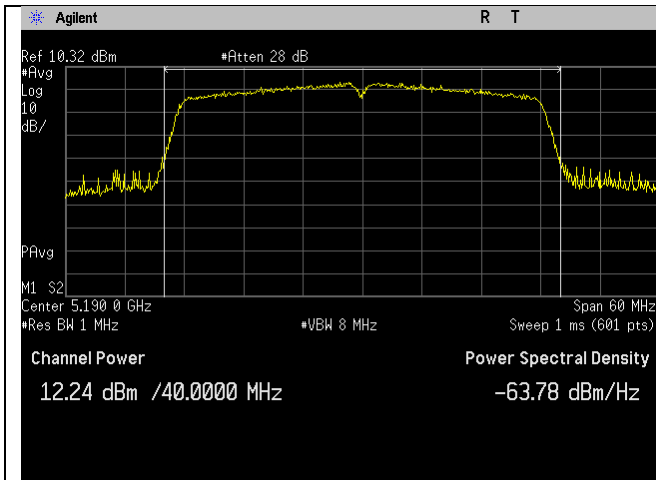


Annex B - OP

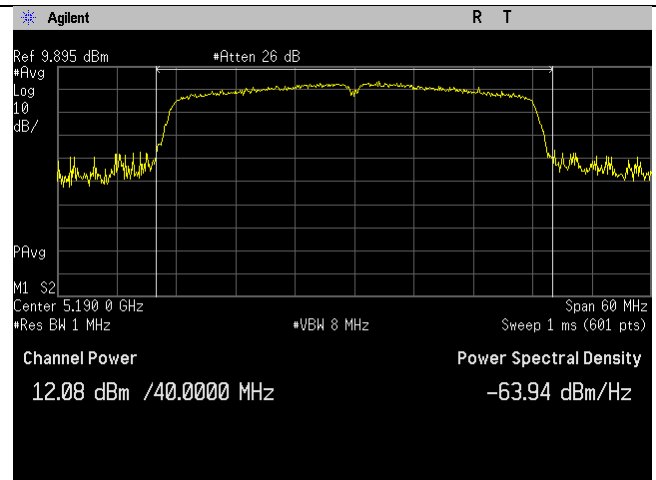
[Naming Convention]

Standard (IC, FCC)_Band (UNII-1, UNII-2A, UNII-2C, UNII-3)_Frequency (MHz)_Channel
(Number)_Bandwidth (MHz)_Modulation (a-mode, n-mode, ac-mode, ax-mode)_Measurement (Output
Power)_Port (1, 2)

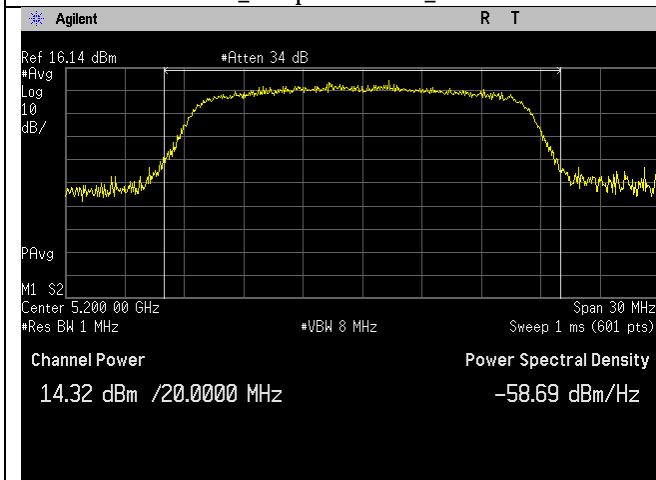




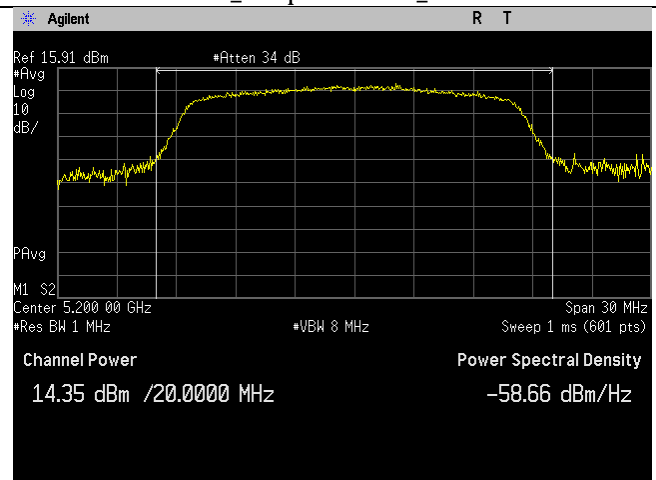
IC_U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_Output Power_Port 1



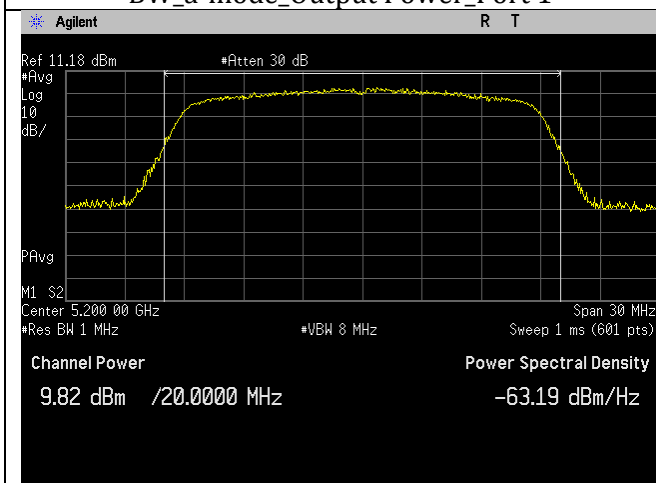
IC_U-NII-1_5190MHz_Low Ch_38_40MHz BW_n-mode_Output Power_Port 2



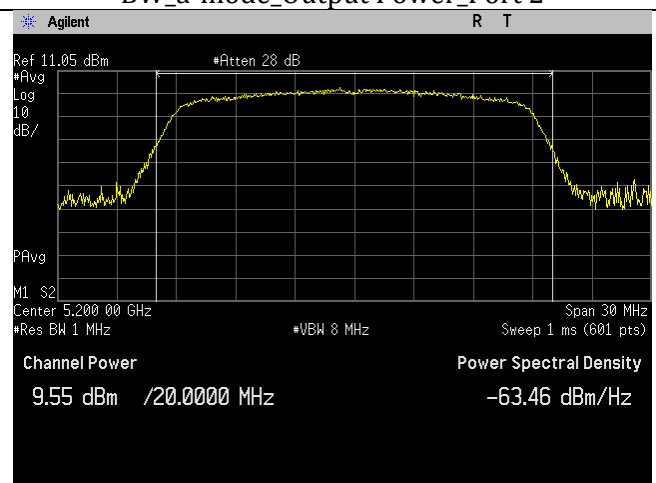
IC_U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_a-mode_Output Power_Port 1



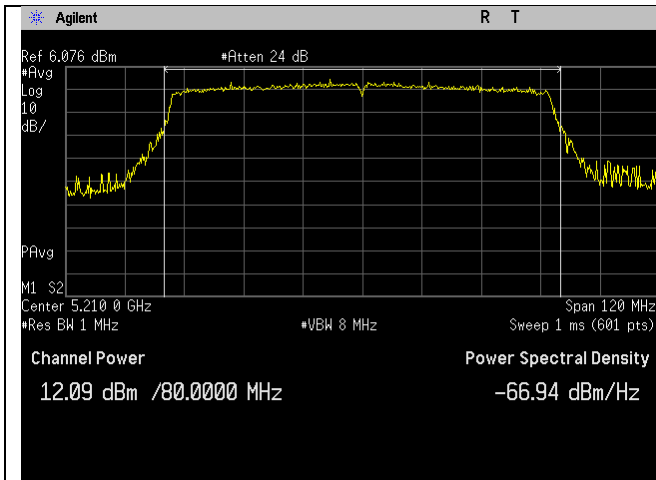
IC_U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_a-mode_Output Power_Port 2



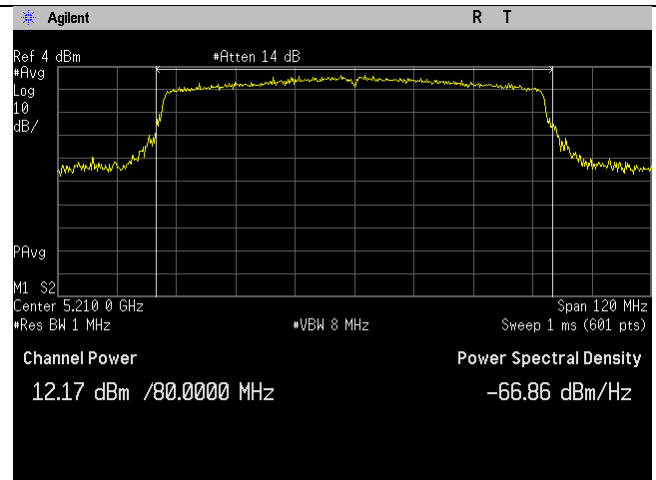
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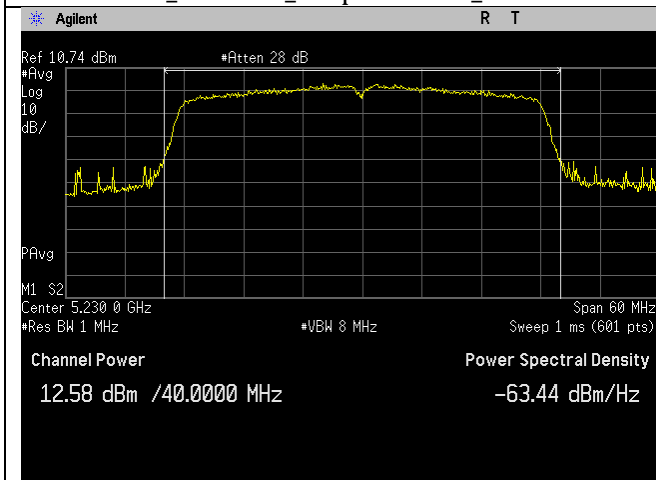
IC_U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_n-mode_Output Power_Port 2



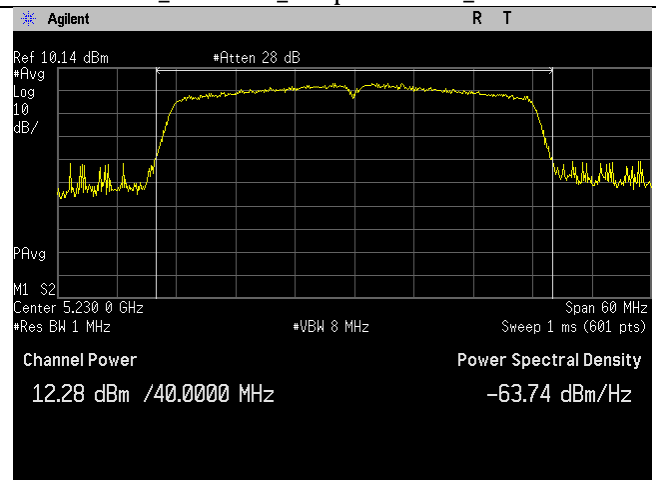
IC_U-NII-1_5210MHz_Low Mid Ch_42_80MHz
BW_ac-mode_Output Power_Port 1



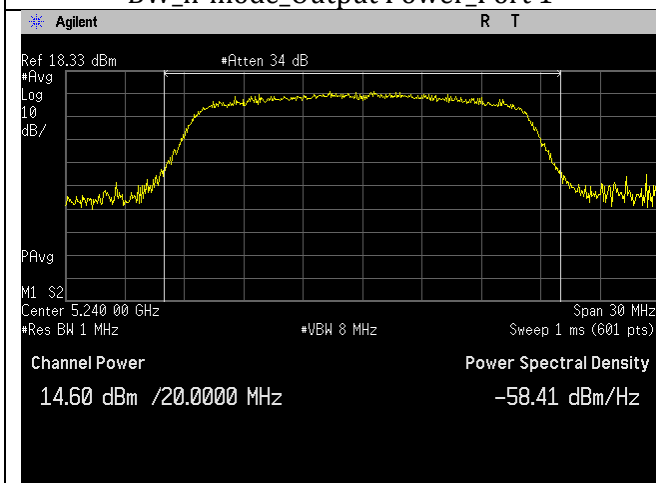
IC_U-NII-1_5210MHz_Low Mid Ch_42_80MHz
BW_ac-mode_Output Power_Port 2



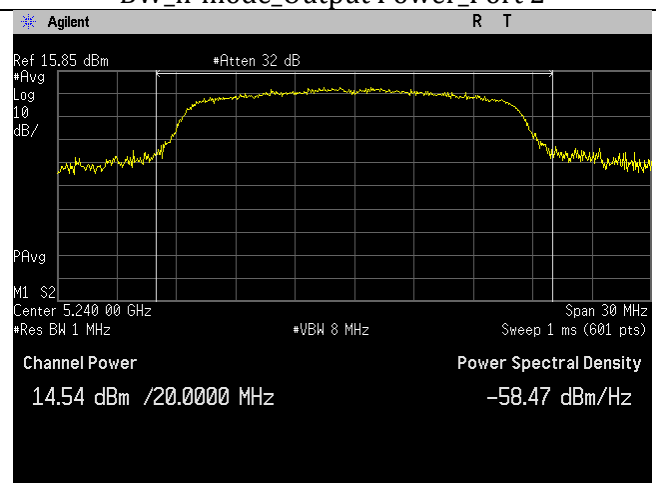
IC_U-NII-1_5230MHz_high Mid Ch_46_40MHz
BW_n-mode_Output Power_Port 1



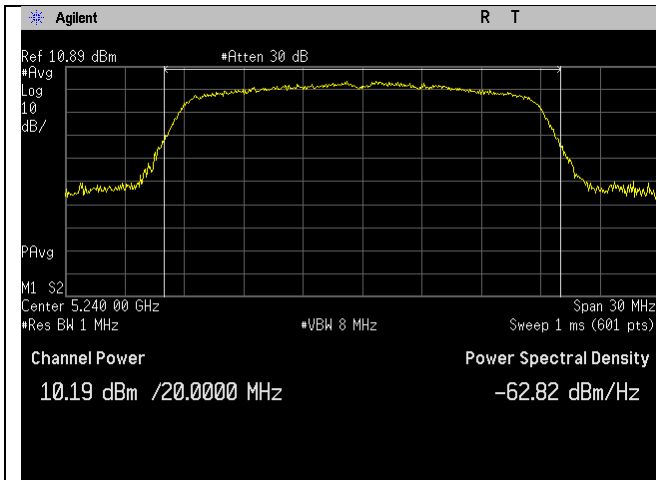
IC_U-NII-1_5230MHz_high Mid Ch_46_40MHz
BW_n-mode_Output Power_Port 2



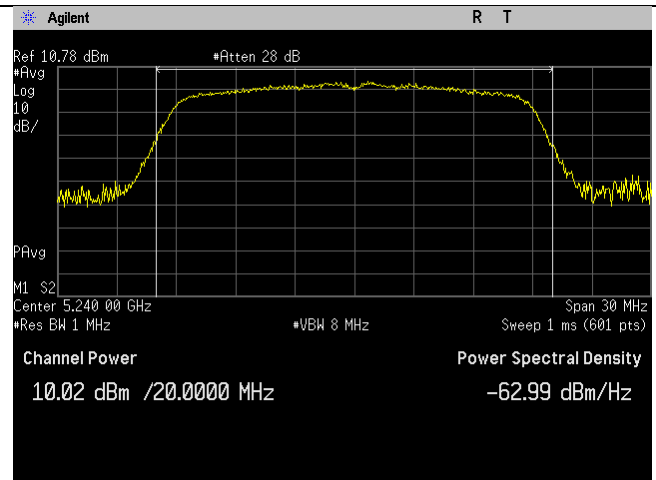
IC_U-NII-1_5240MHz_high_mid Ch_48_20MHz
BW_a-mode_Output Power_Port 1



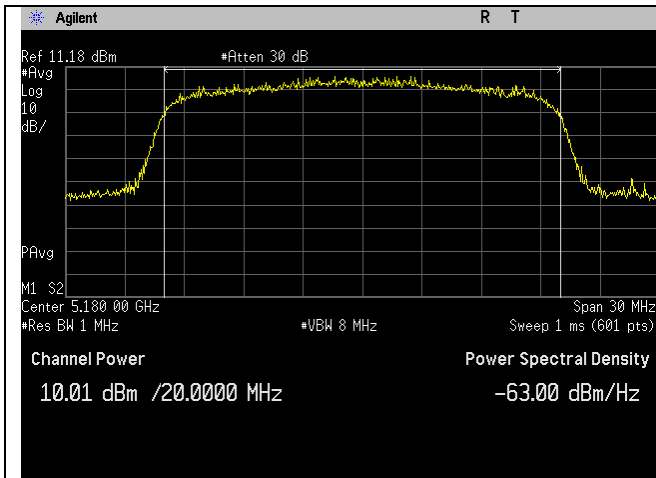
IC_U-NII-1_5240MHz_high_mid Ch_48_20MHz
BW_a-mode_Output Power_Port 2



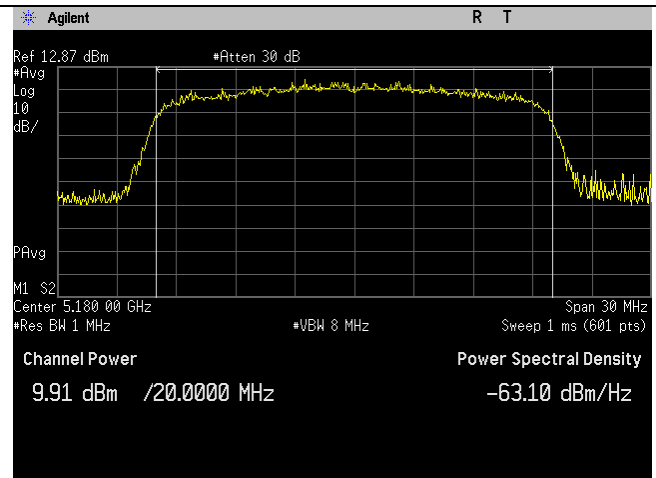
IC_U-NII-1_5240MHz_high_mid Ch. 48.20MHz
BW_n-mode_Output Power_Port 1



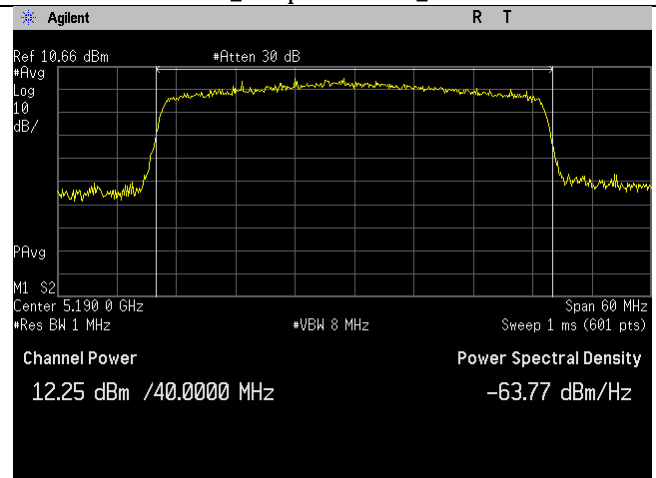
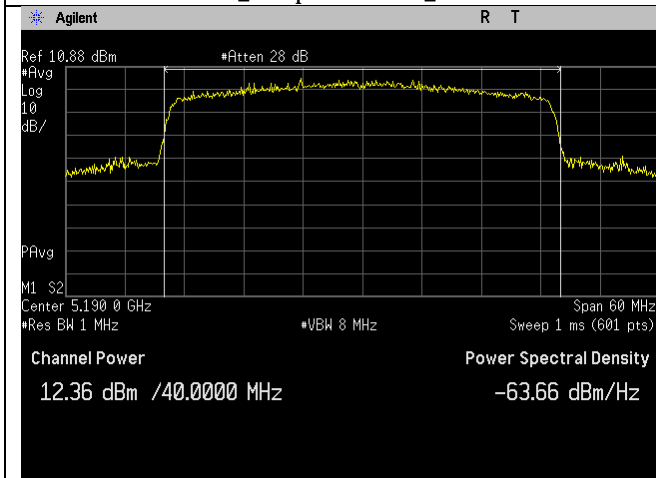
IC_U-NII-1_5240MHz_high_mid Ch. 48.20MHz
BW_n-mode_Output Power_Port 2

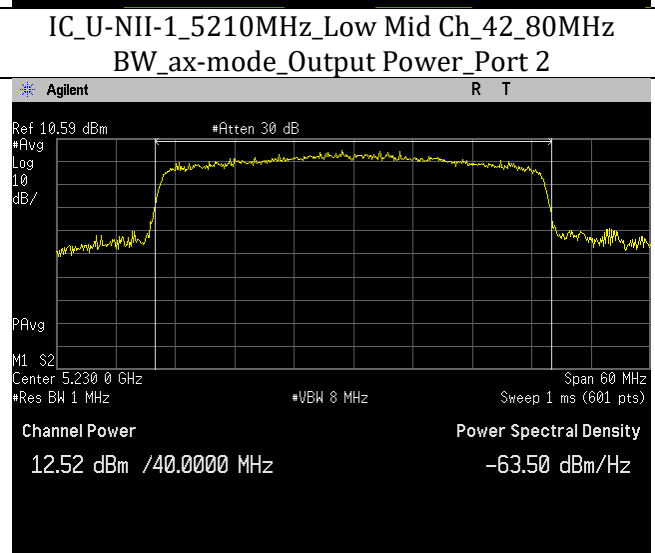
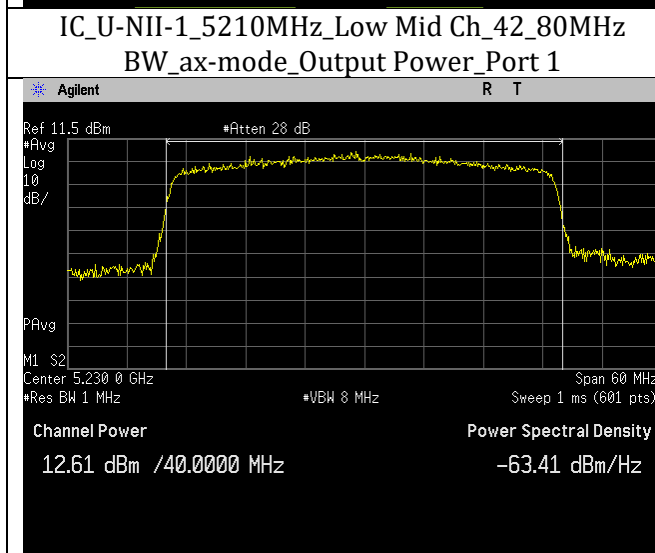
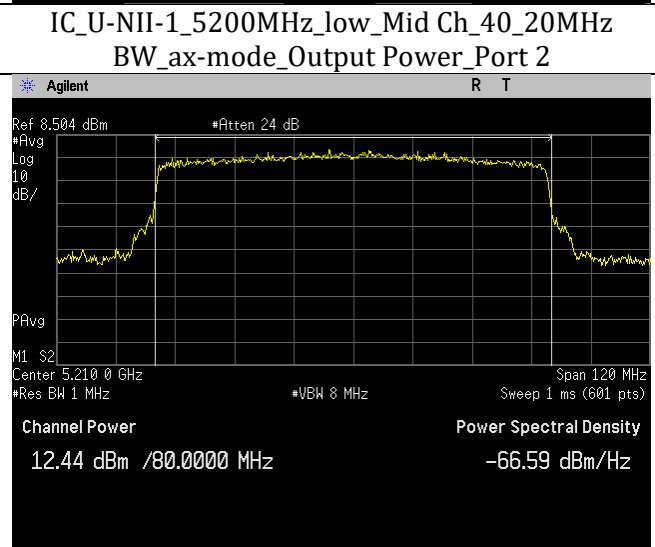
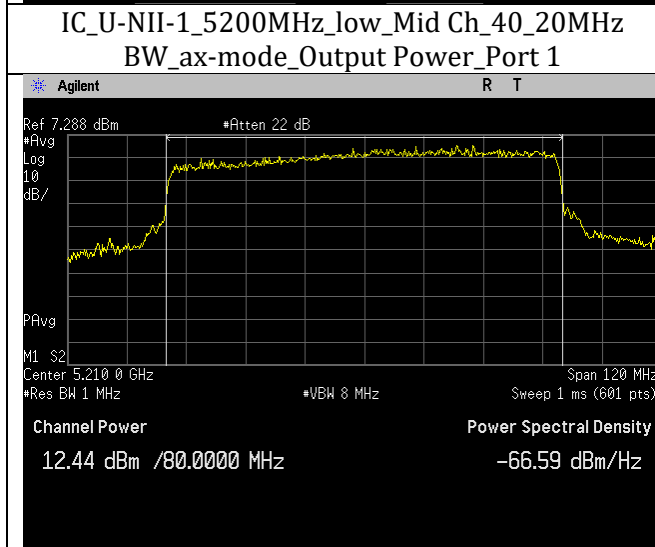
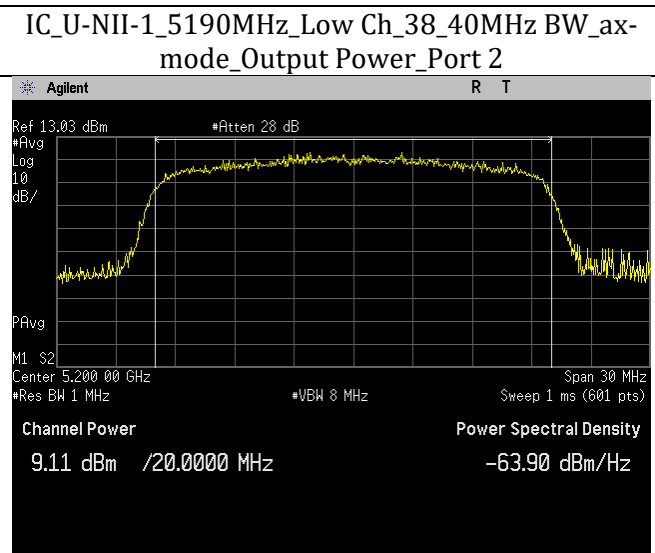
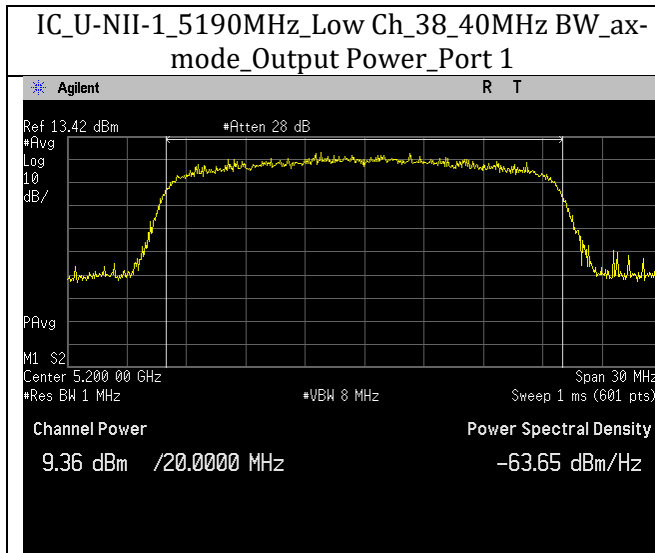


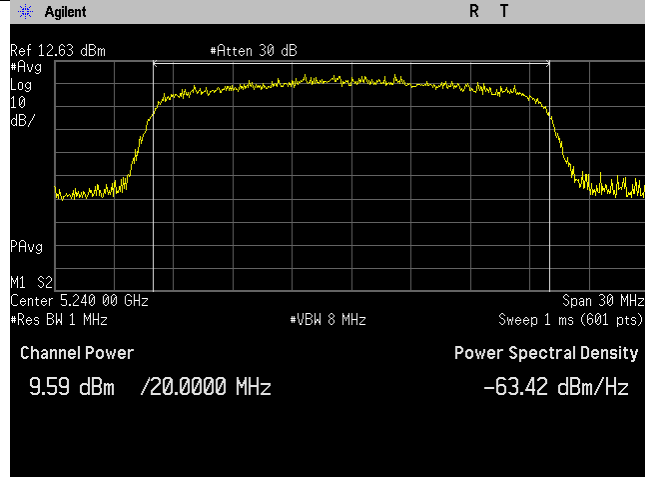
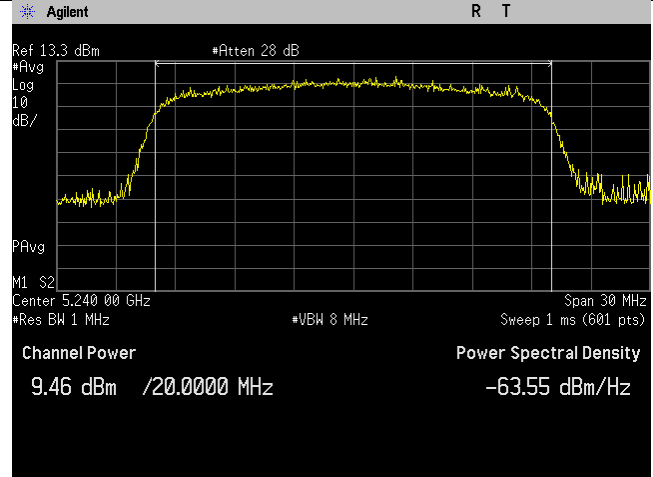
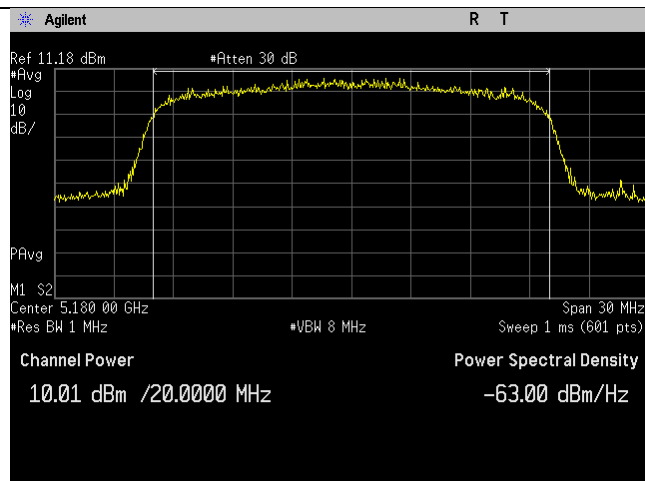
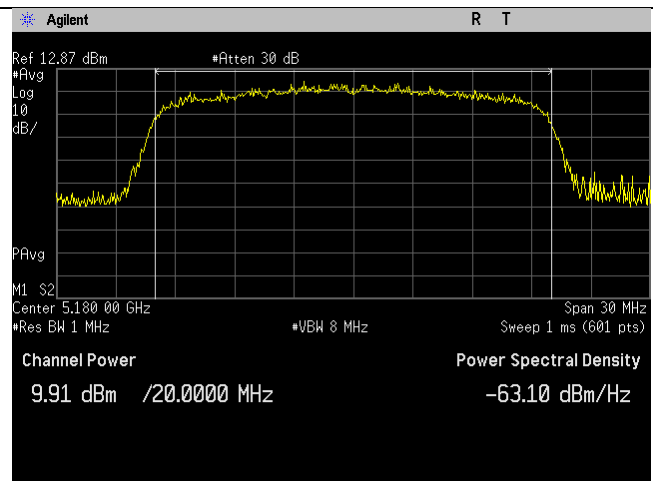
IC_U-NII-1_5180MHz_Low Ch. 36.20MHz BW_ax-
mode_Output Power_Port 1

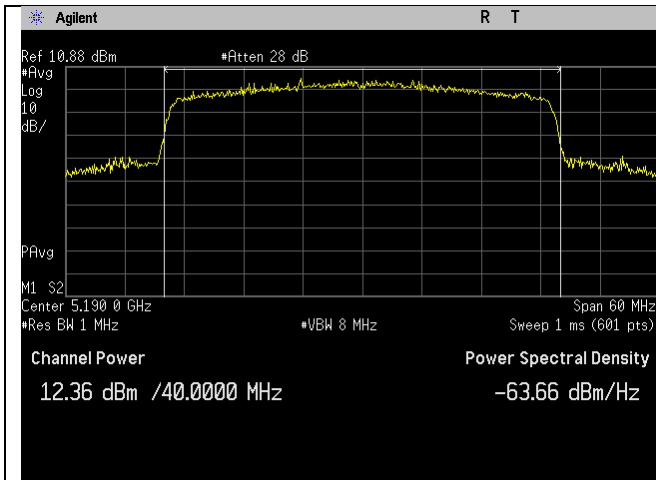


IC_U-NII-1_5180MHz_Low Ch. 36.20MHz BW_ax-
mode_Output Power_Port 2

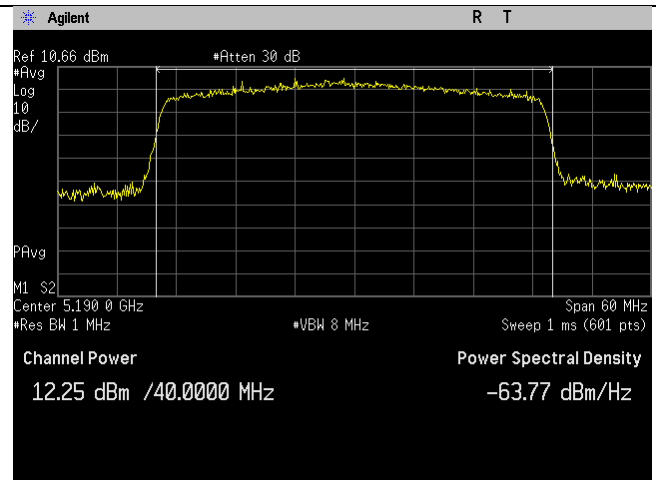




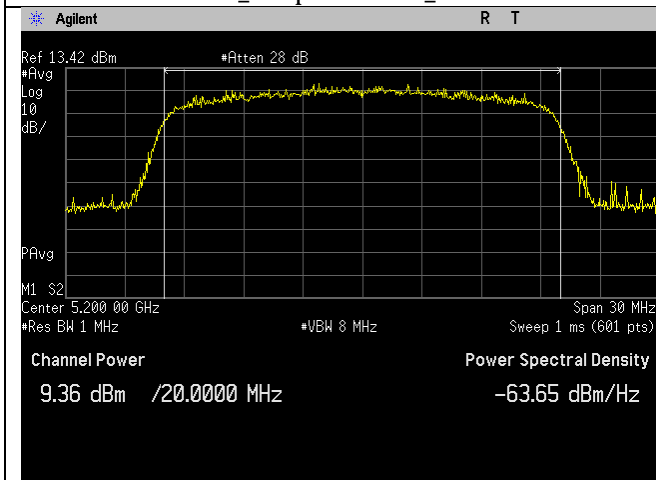
IC_U-NII-1_5230MHz_high_Mid Ch_46_40MHz
BW_ax-mode_Output Power_Port 1IC_U-NII-1_5230MHz_high_Mid Ch_46_40MHz
BW_ax-mode_Output Power_Port 2IC_U-NII-1_5240MHz_high_mid Ch_48_20MHz
BW_ax-mode_Output Power_Port 1IC_U-NII-1_5240MHz_high_mid Ch_48_20MHz
BW_ax-mode_Output Power_Port 2IC_U-NII-1_5180MHz_Low Ch_36_20MHz BW_ax-
mode_Output Power_Port 1IC_U-NII-1_5180MHz_Low Ch_36_20MHz BW_ax-
mode_Output Power_Port 2



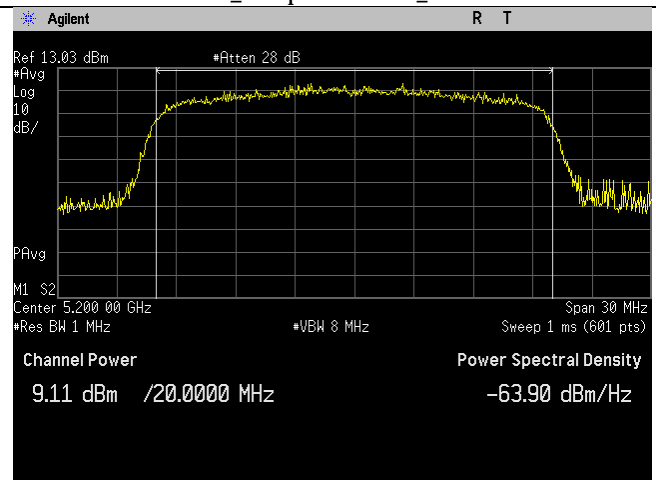
IC_U-NII-1_5190MHz Low Ch_38_40MHz BW_ax-mode_Output Power_Port 1



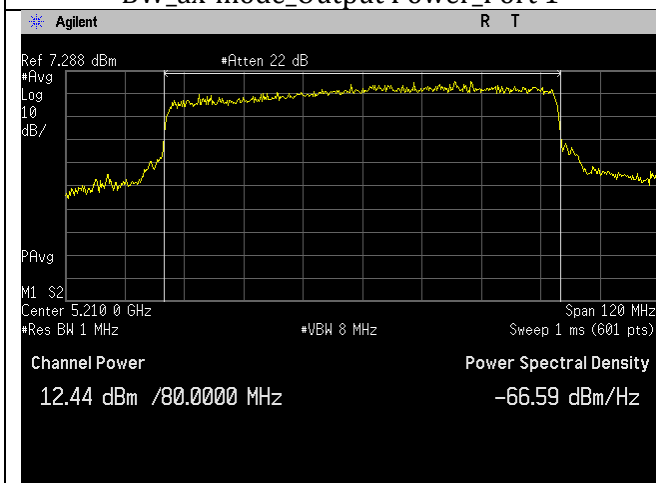
IC_U-NII-1_5190MHz Low Ch_38_40MHz BW_ax-mode_Output Power_Port 2



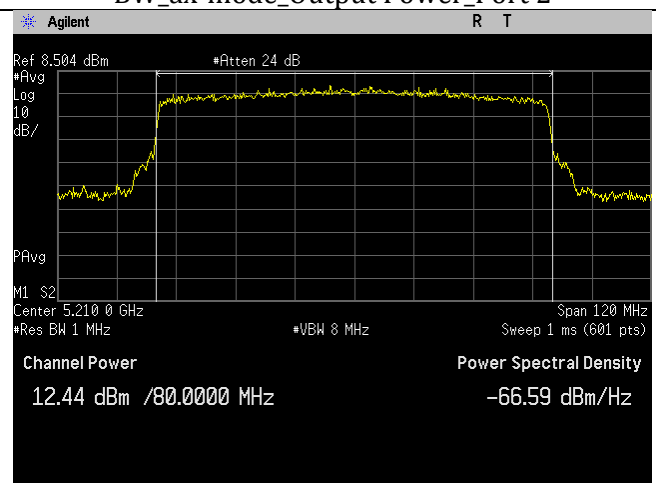
IC_U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ax-mode_Output Power_Port 1



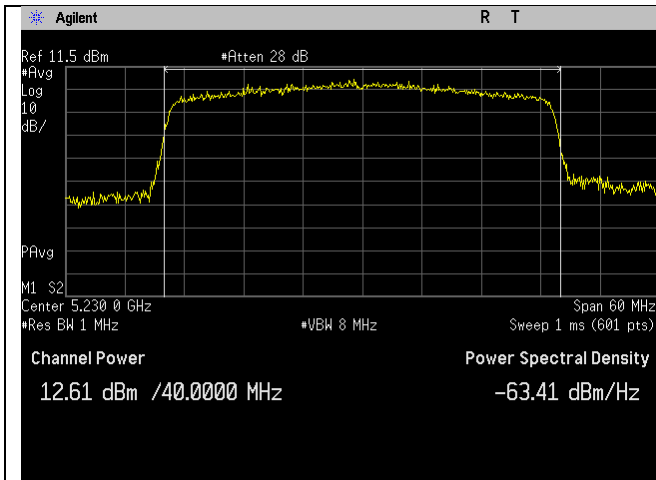
IC_U-NII-1_5200MHz_low_Mid Ch_40_20MHz BW_ax-mode_Output Power_Port 2



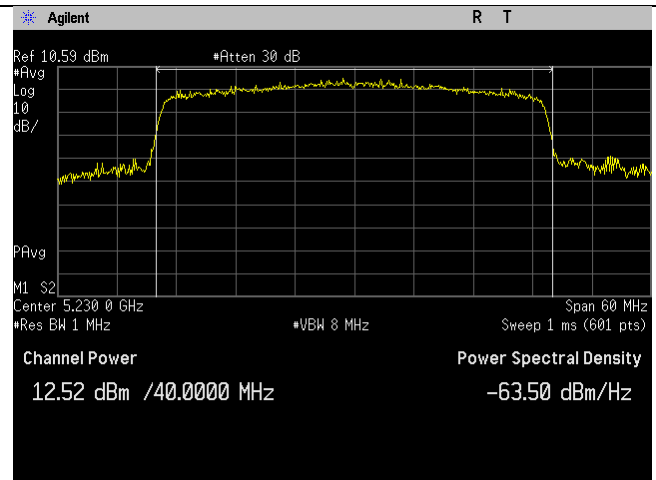
IC_U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ax-mode_Output Power_Port 1



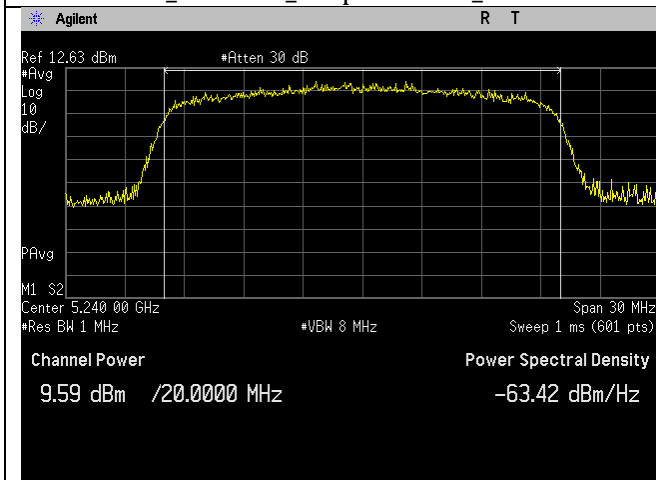
IC_U-NII-1_5210MHz_Low Mid Ch_42_80MHz BW_ax-mode_Output Power_Port 2



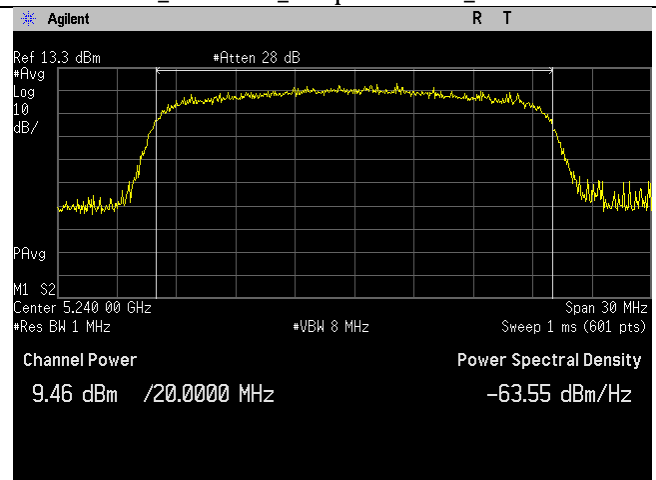
IC_U-NII-1_5230MHz_high_Mid Ch_46_40MHz
BW_ax-mode_Output Power_Port 1



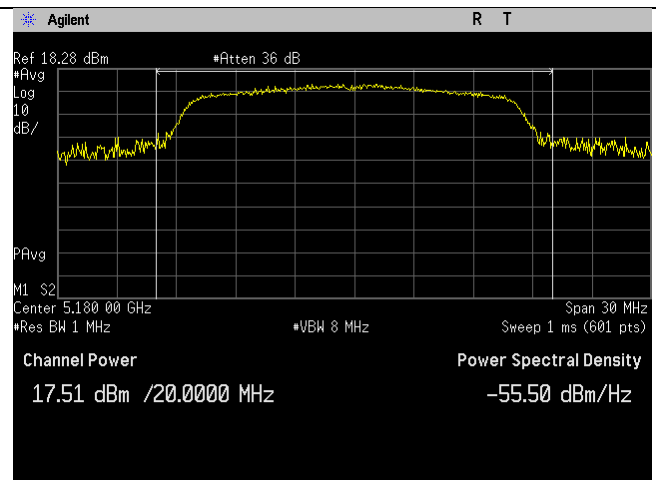
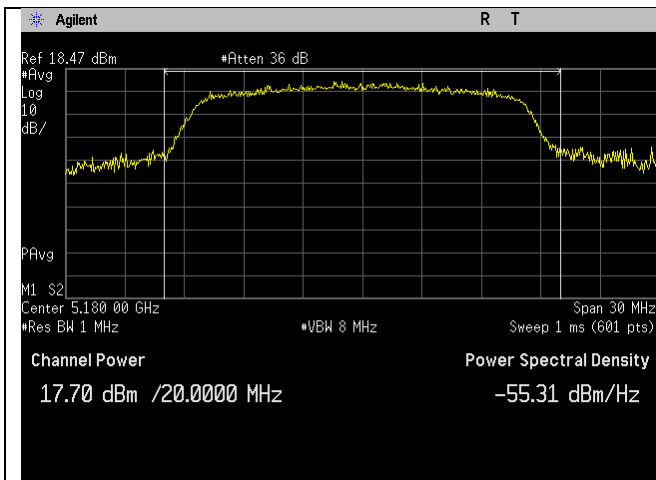
IC_U-NII-1_5230MHz_high_Mid Ch_46_40MHz
BW_ax-mode_Output Power_Port 2

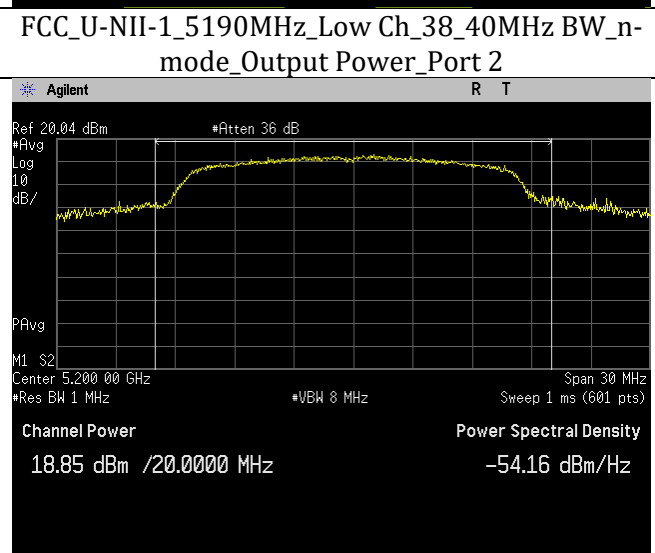
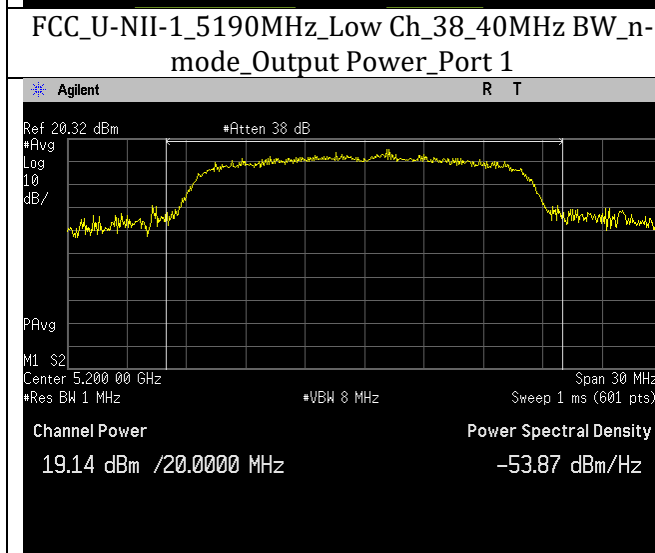
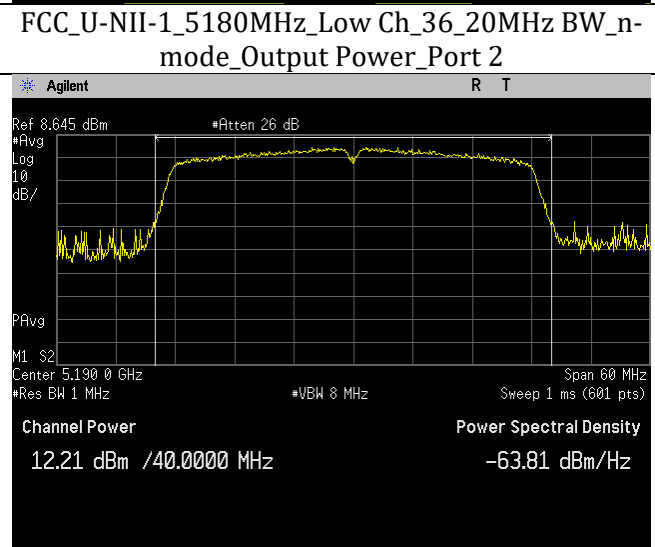
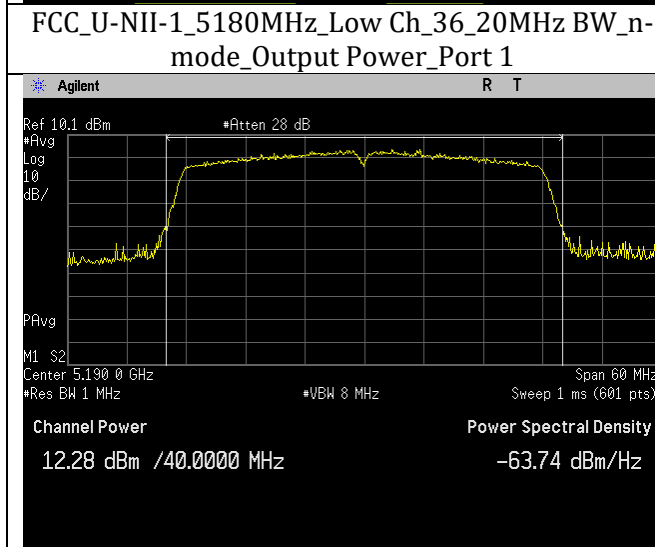
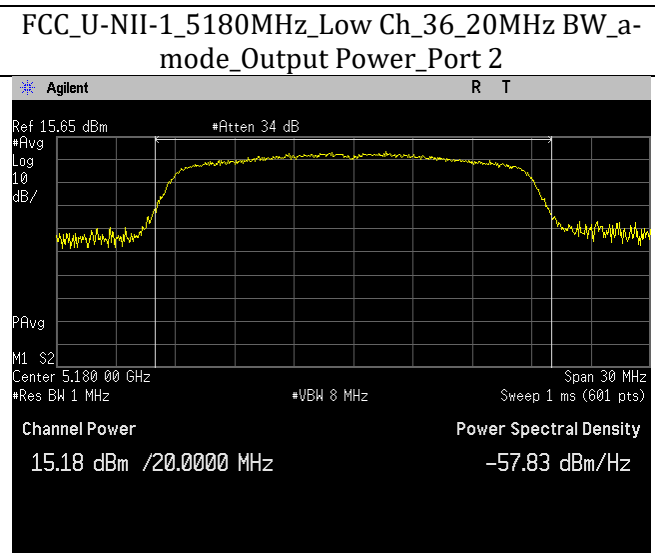
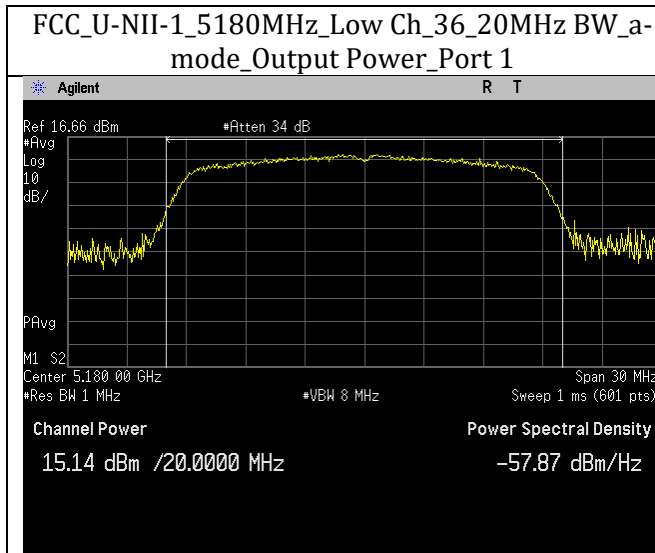


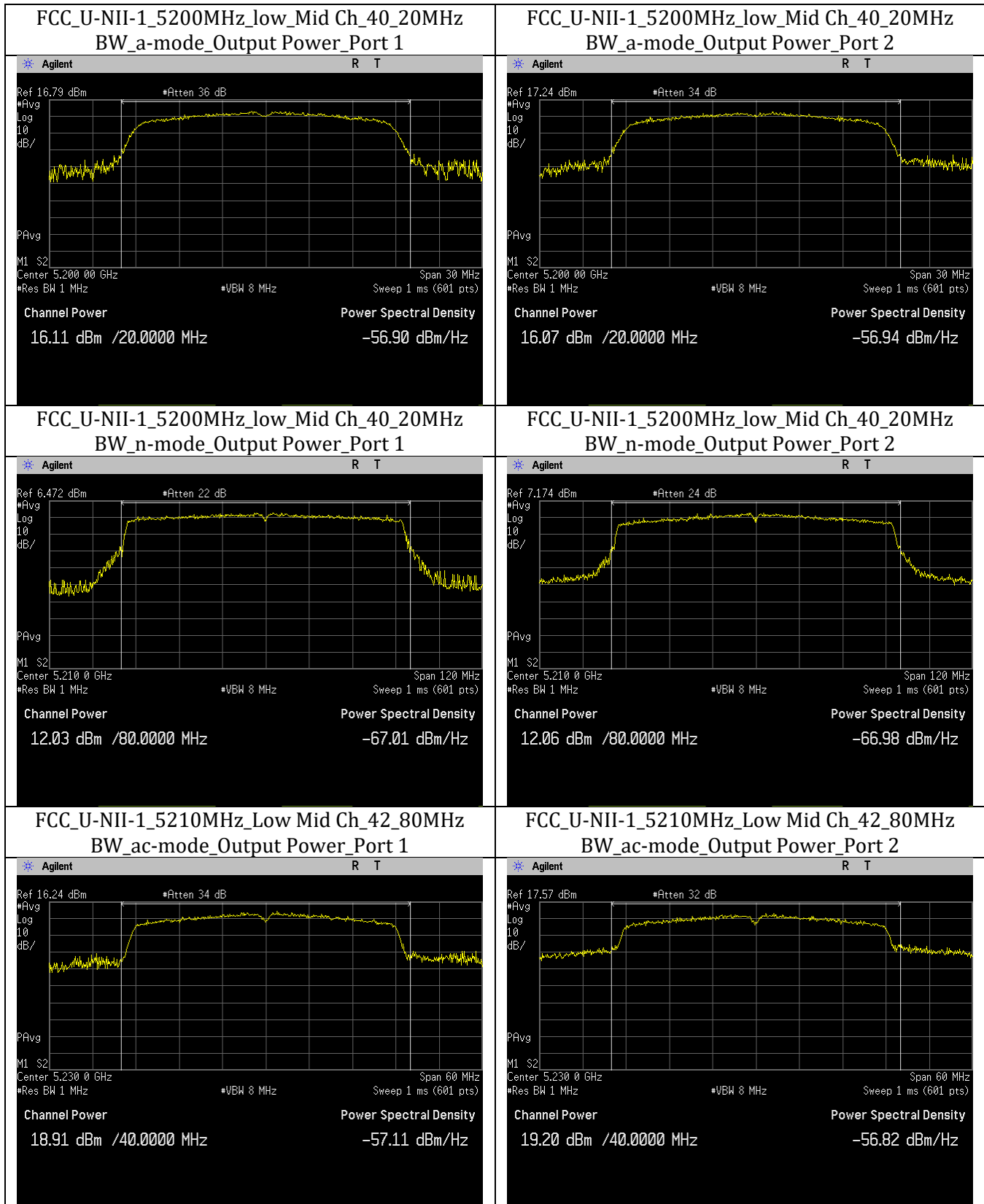
IC_U-NII-1_5240MHz_high_mid Ch_48_20MHz
BW_ax-mode_Output Power_Port 1

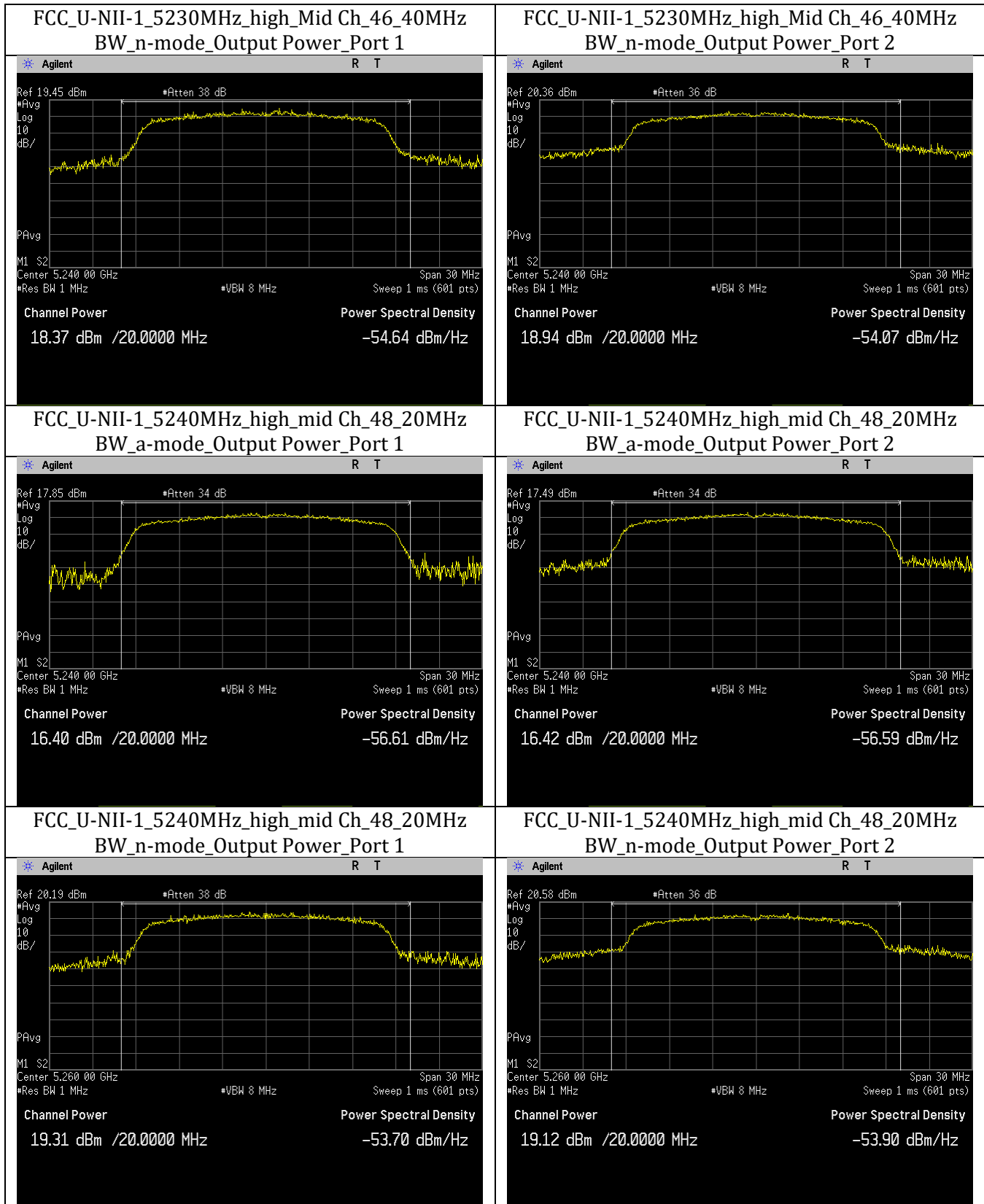


IC_U-NII-1_5240MHz_high_mid Ch_48_20MHz
BW_ax-mode_Output Power_Port 2

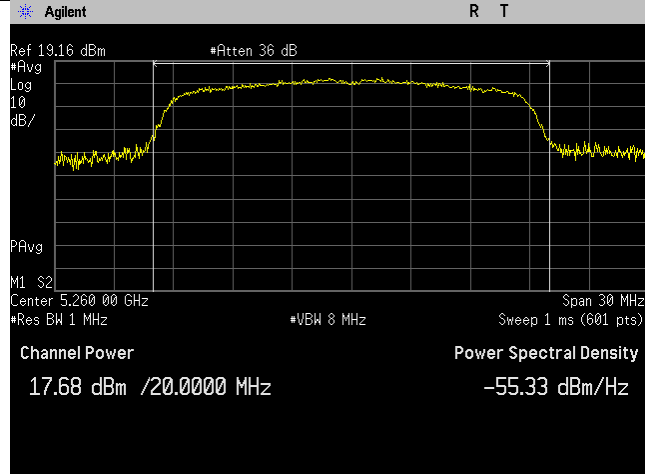




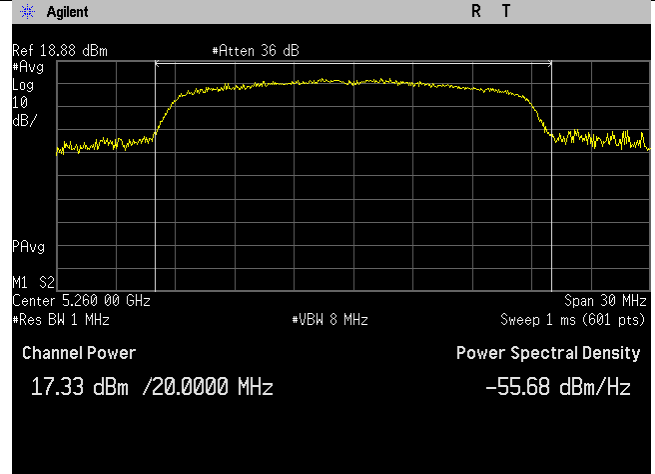




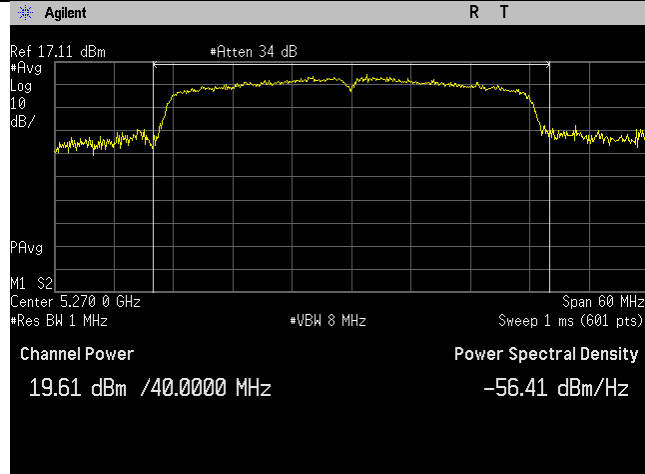
U-NII-2A_5260MHz_low_mid Ch_52_20MHz BW_a-mode_Output Power_Port 1



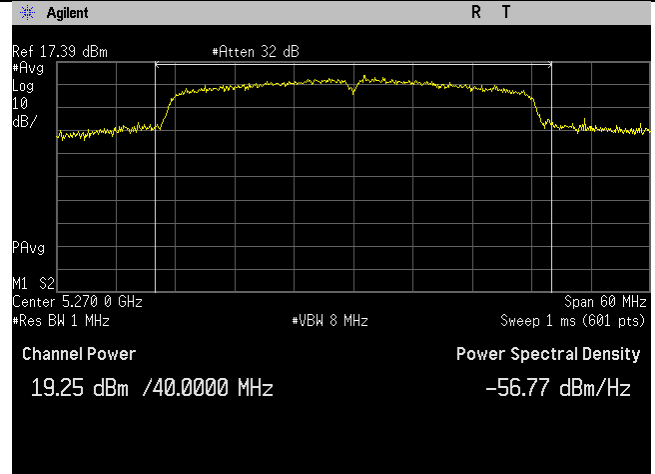
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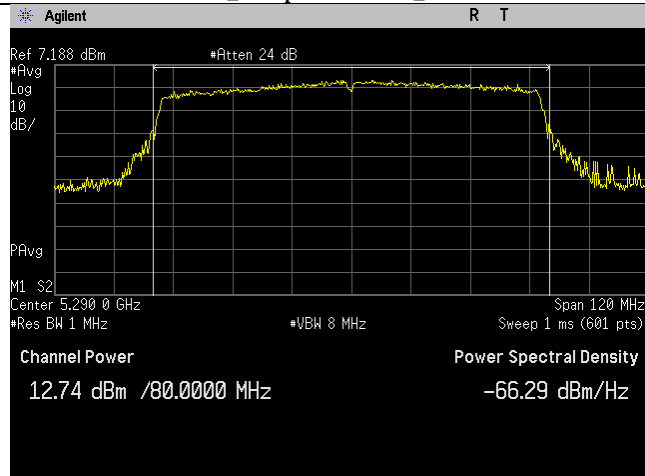
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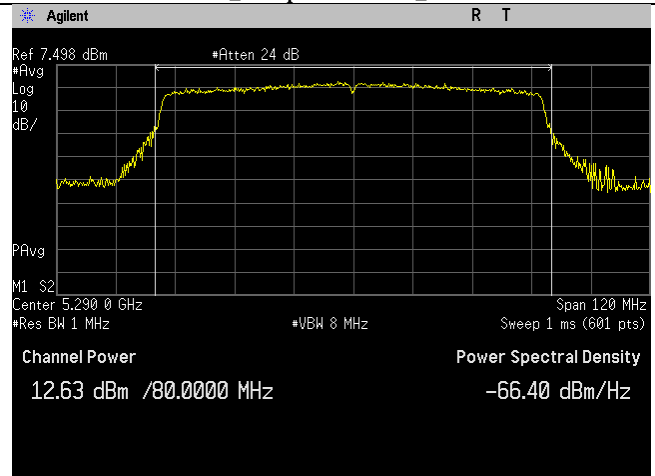
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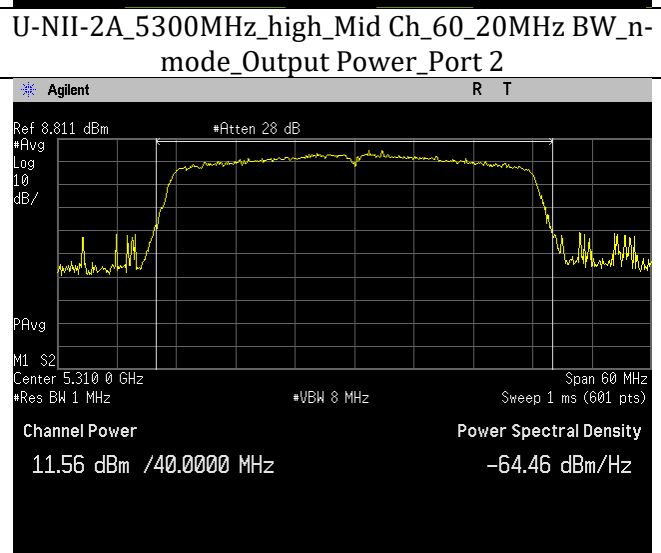
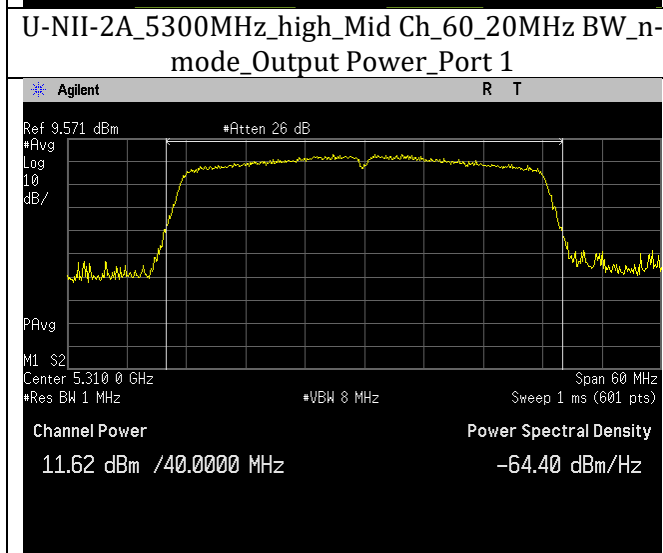
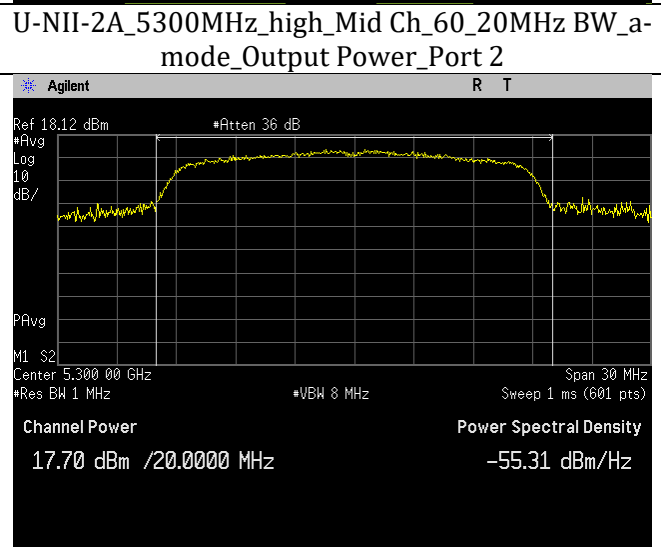
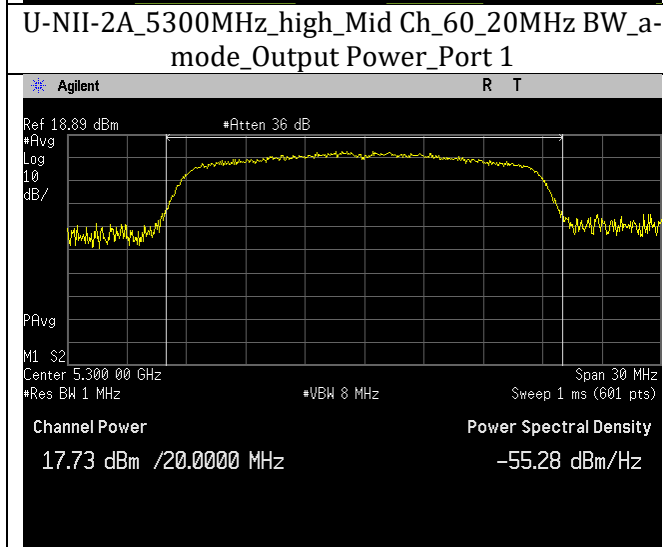
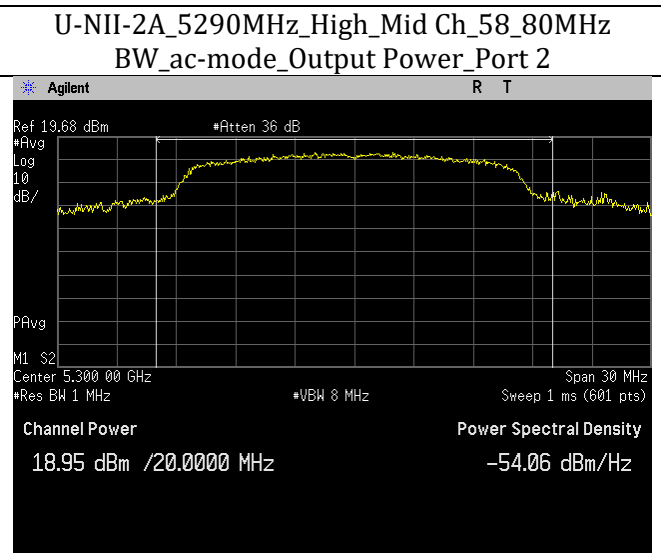
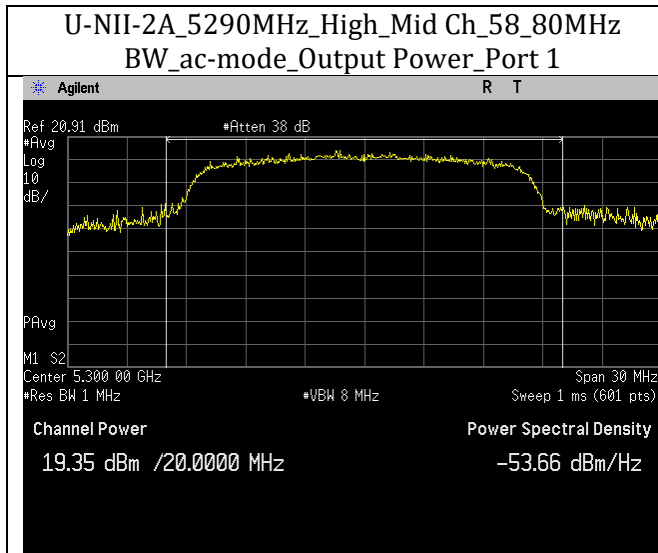


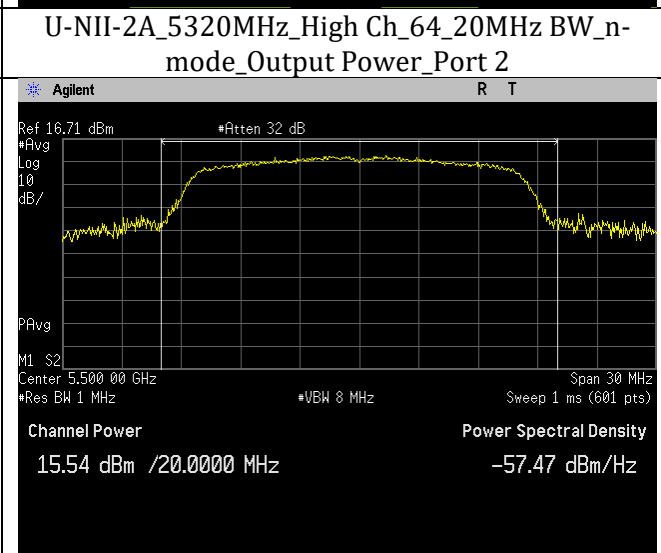
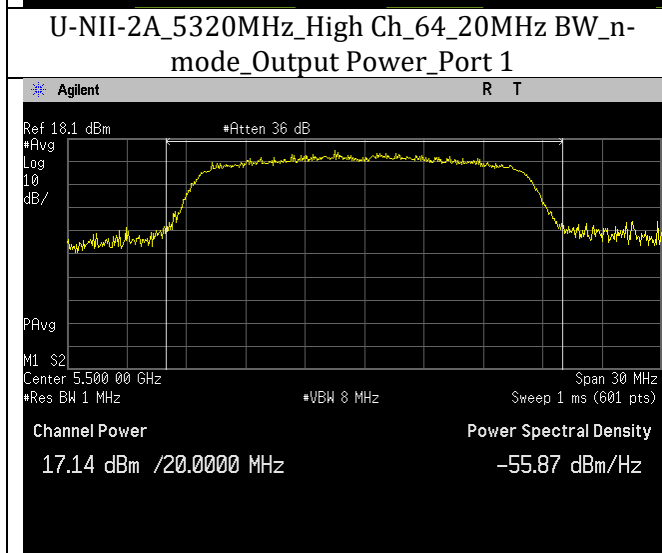
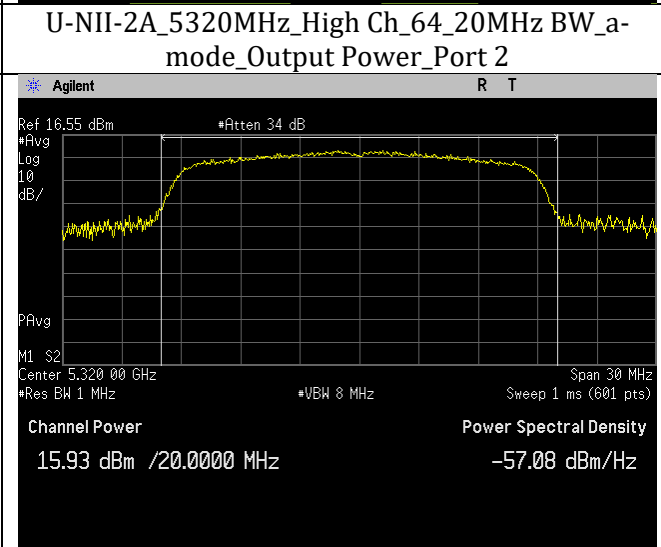
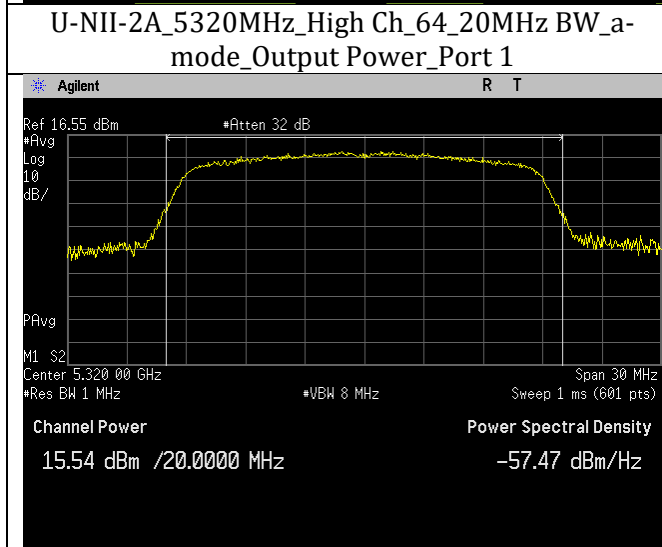
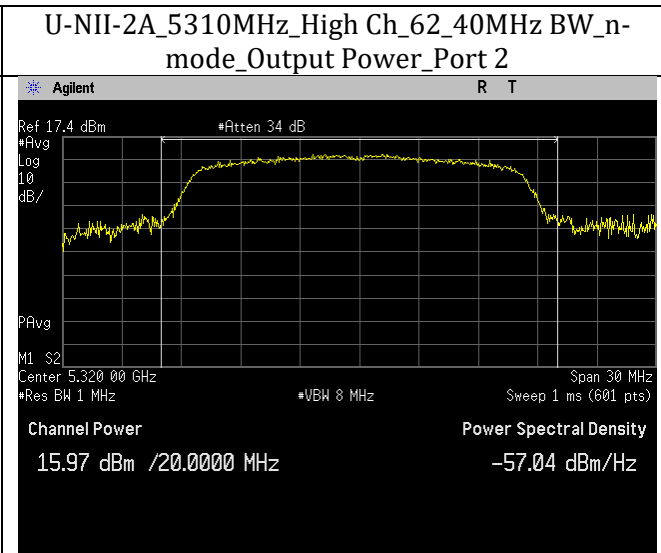
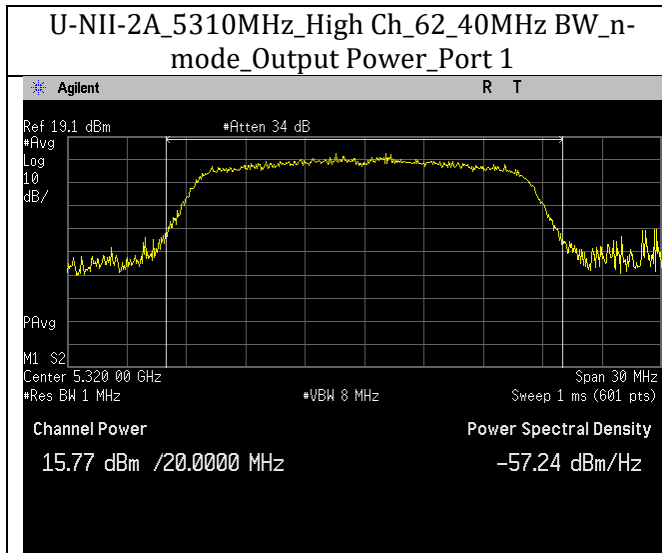
U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_Output Power_Port 1

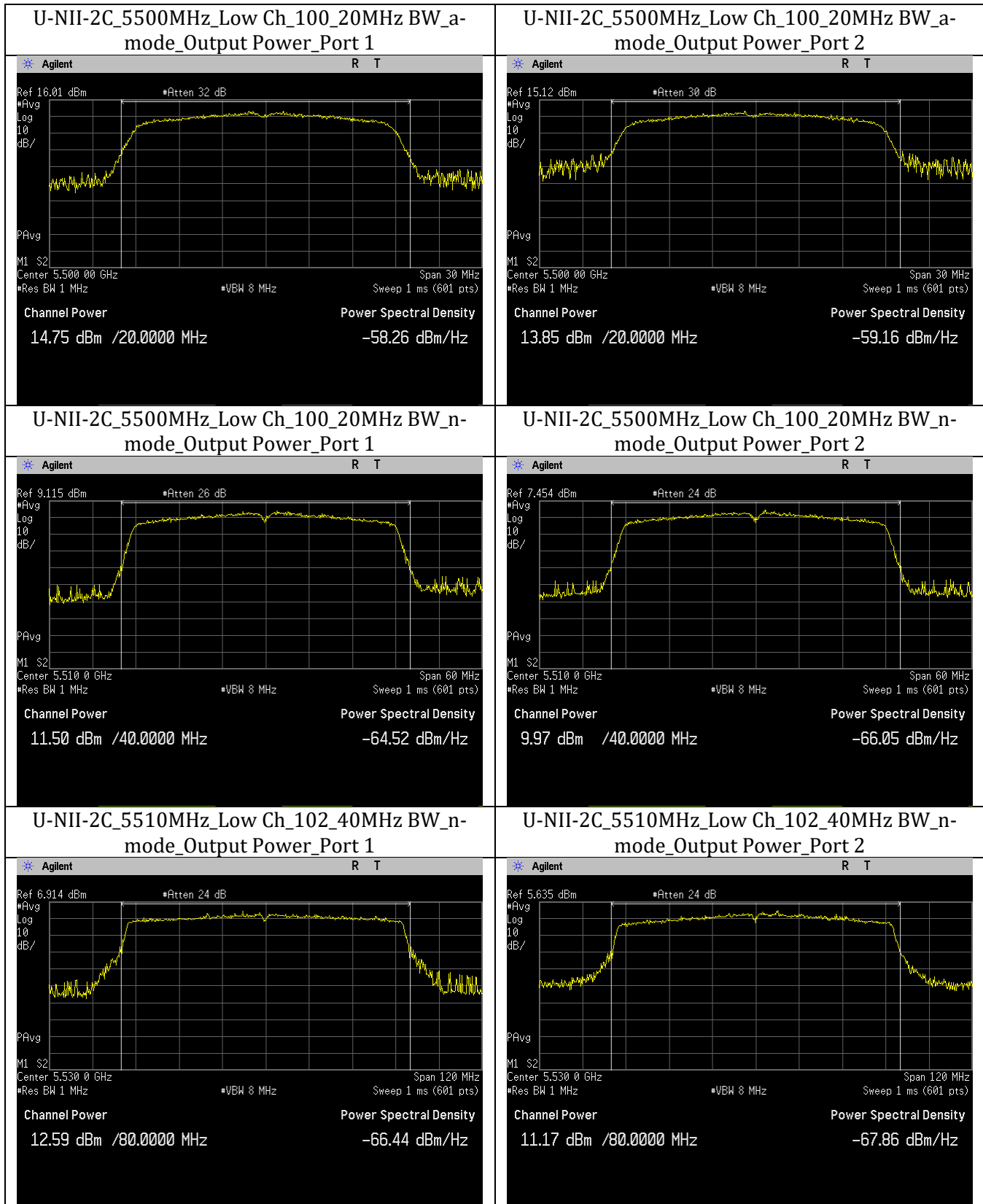


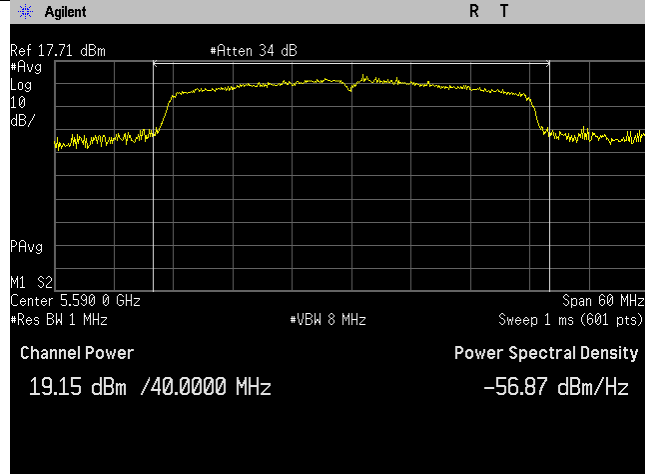
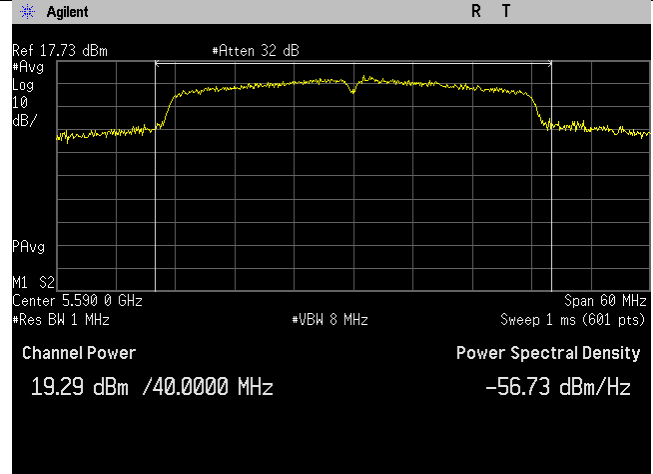
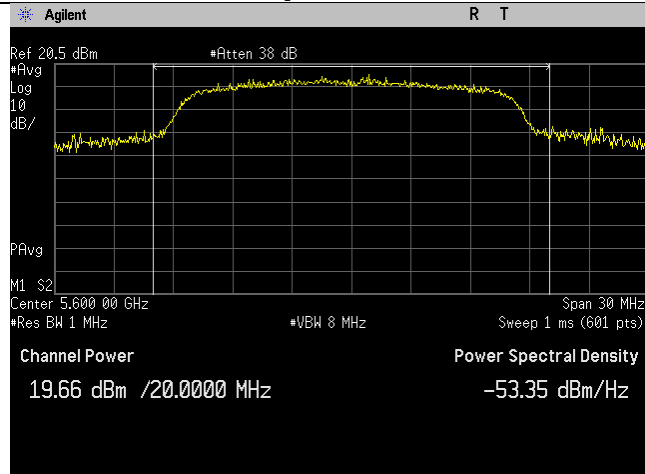
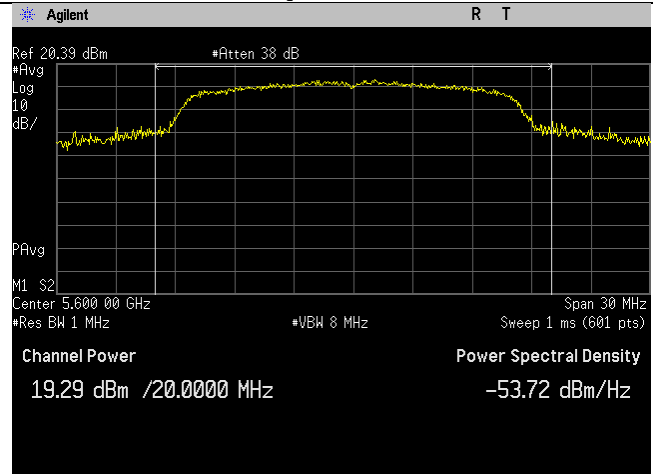
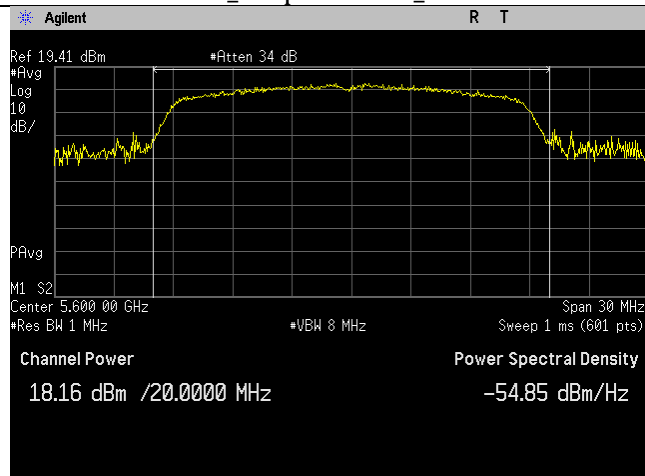
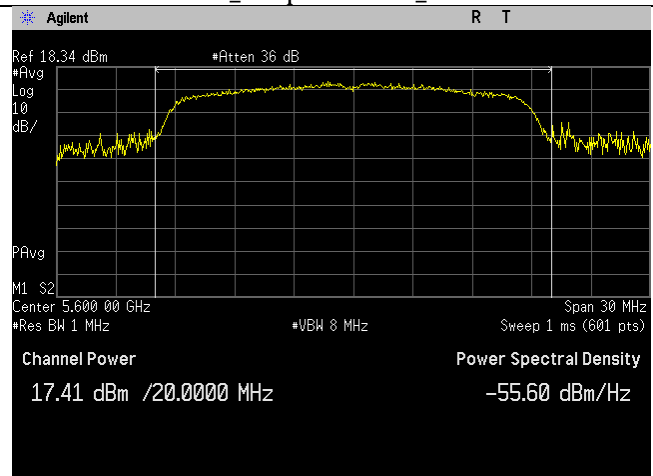
U-NII-2A_5270MHz_low_Mid Ch_54_40MHz BW_n-mode_Output Power_Port 2

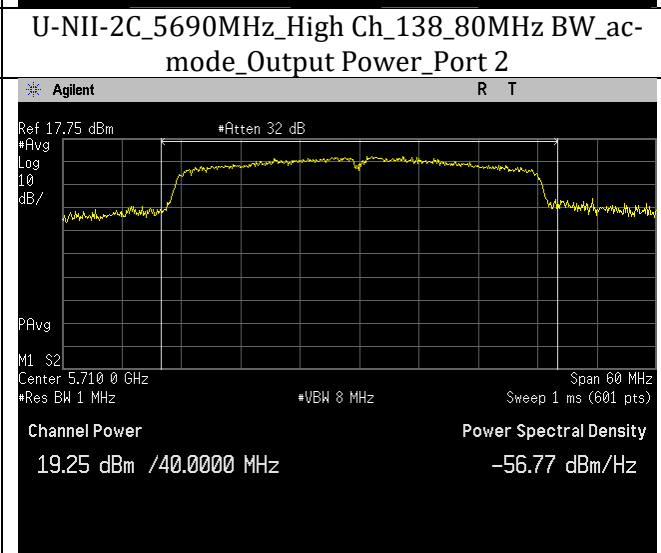
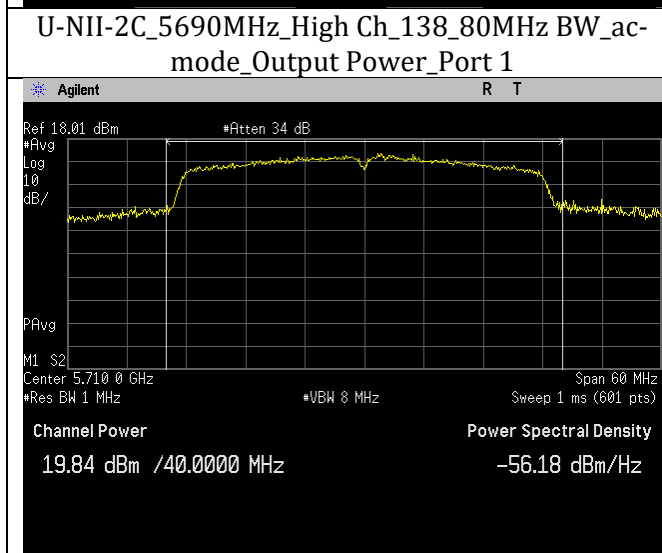
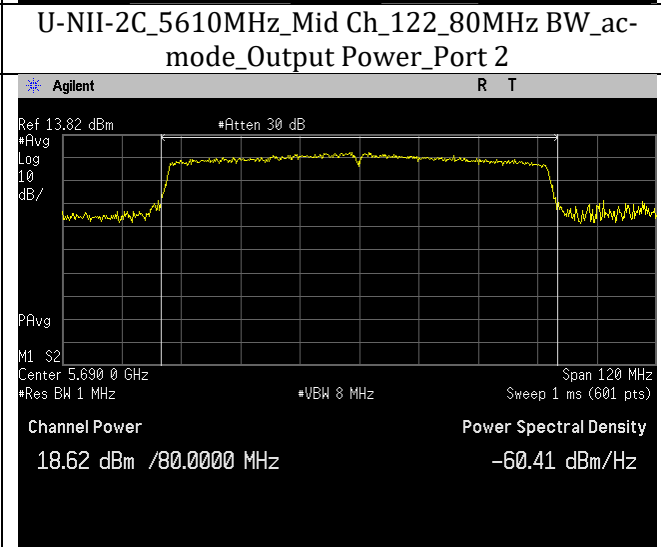
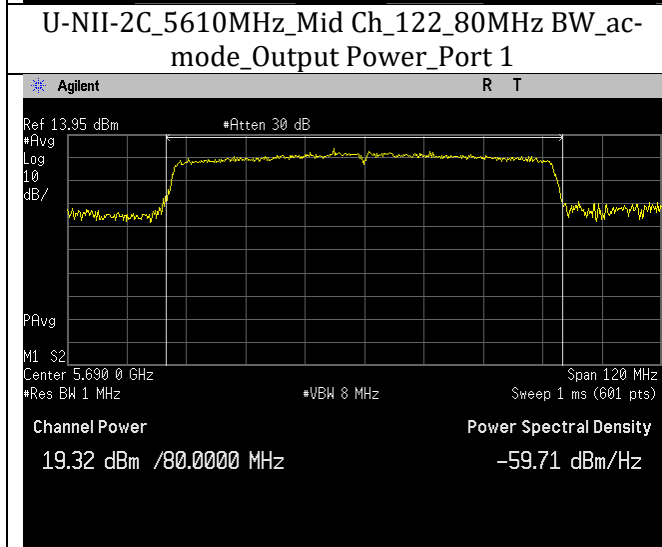
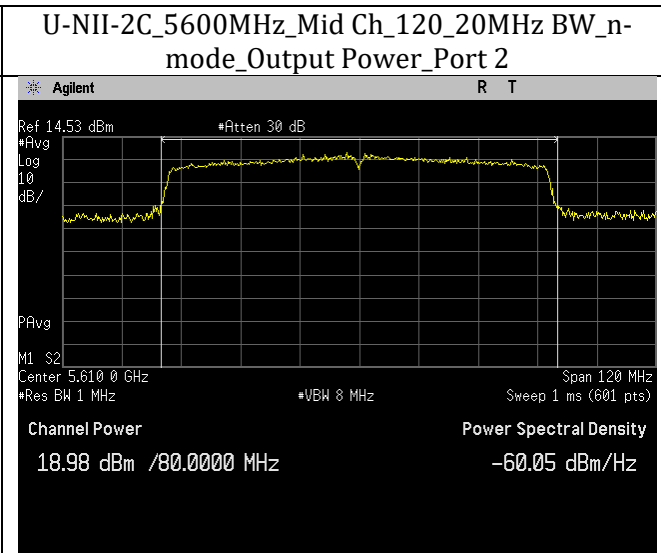
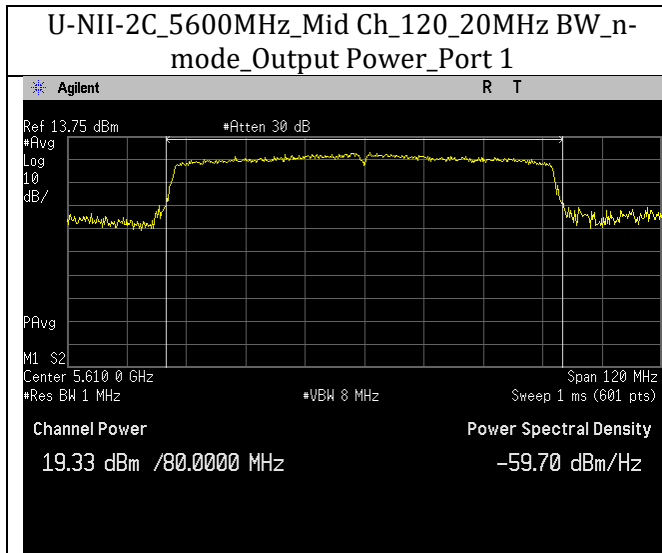


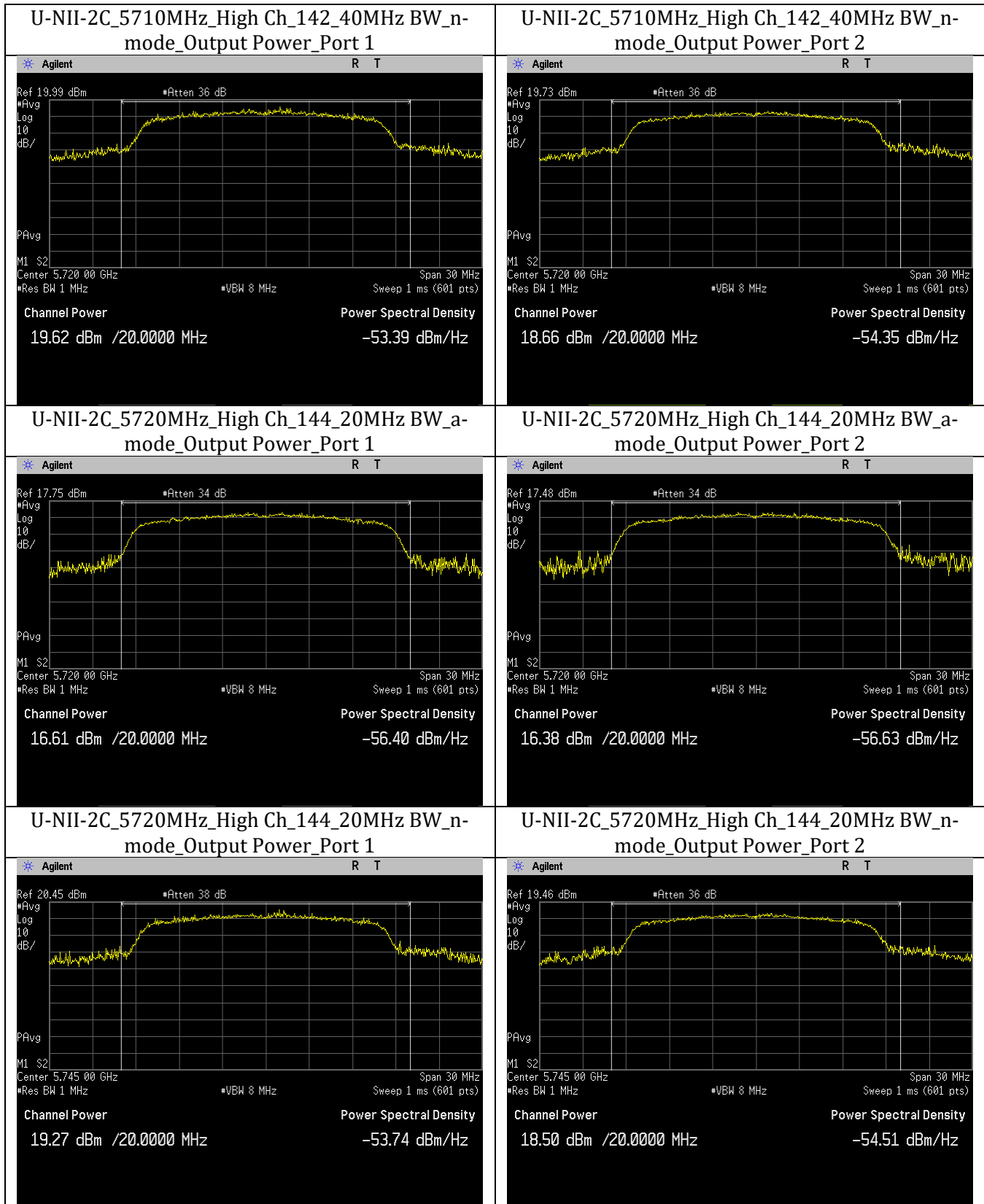




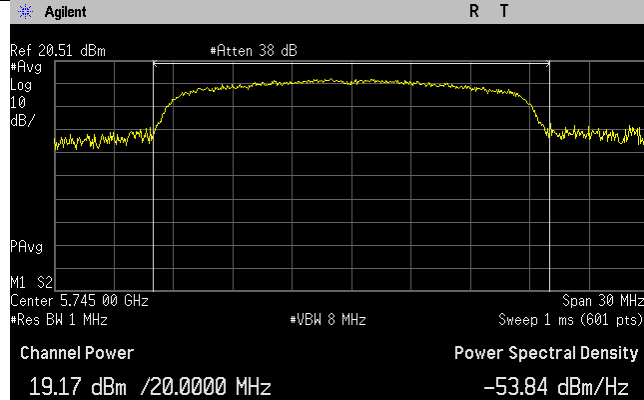


U-NII-2C_5530MHz_Low Ch_106_80MHz BW_ac-
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mode_Output Power_Port 1U-NII-2C_5590MHz_Mid Ch_118_40MHz BW_n-
mode_Output Power_Port 2U-NII-2C_5600MHz_Mid Ch_120_20MHz BW_a-
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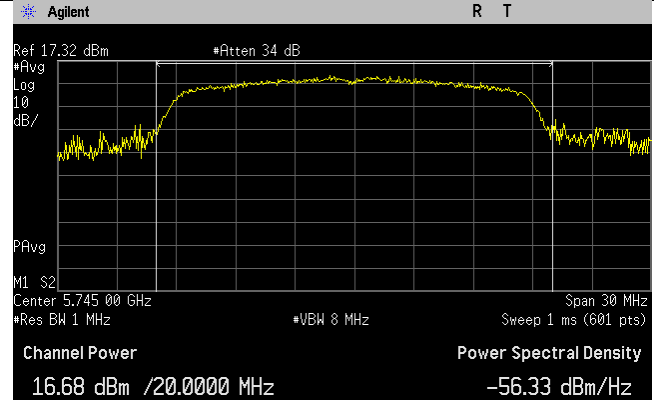




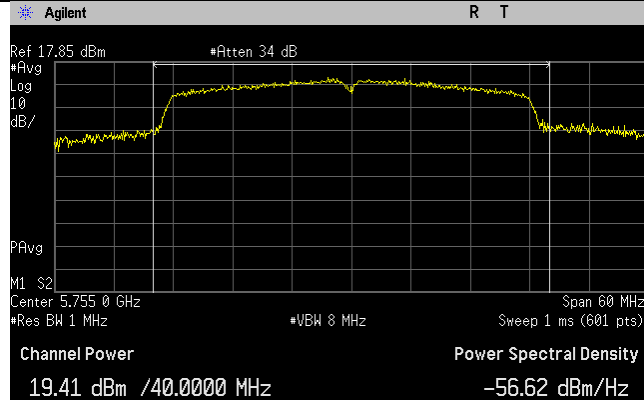
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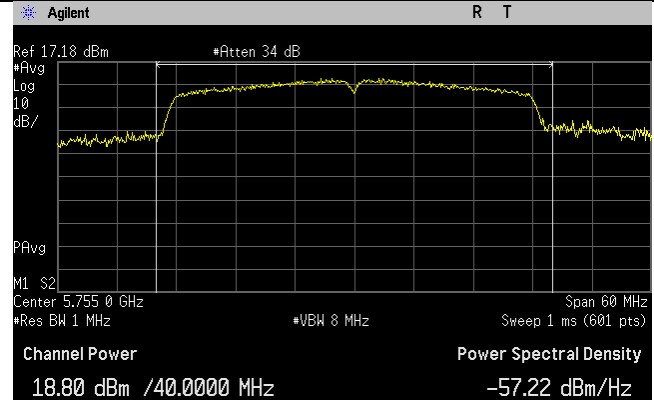
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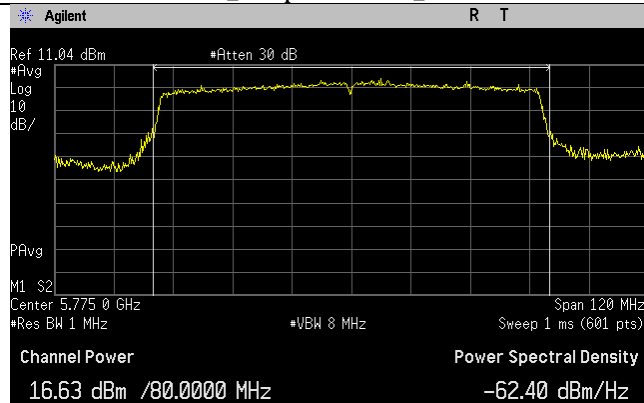
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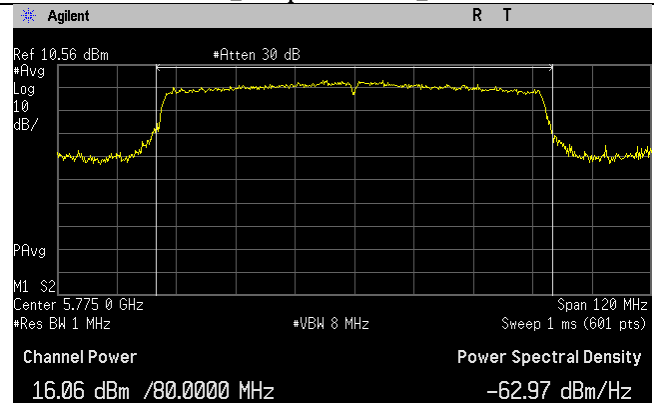
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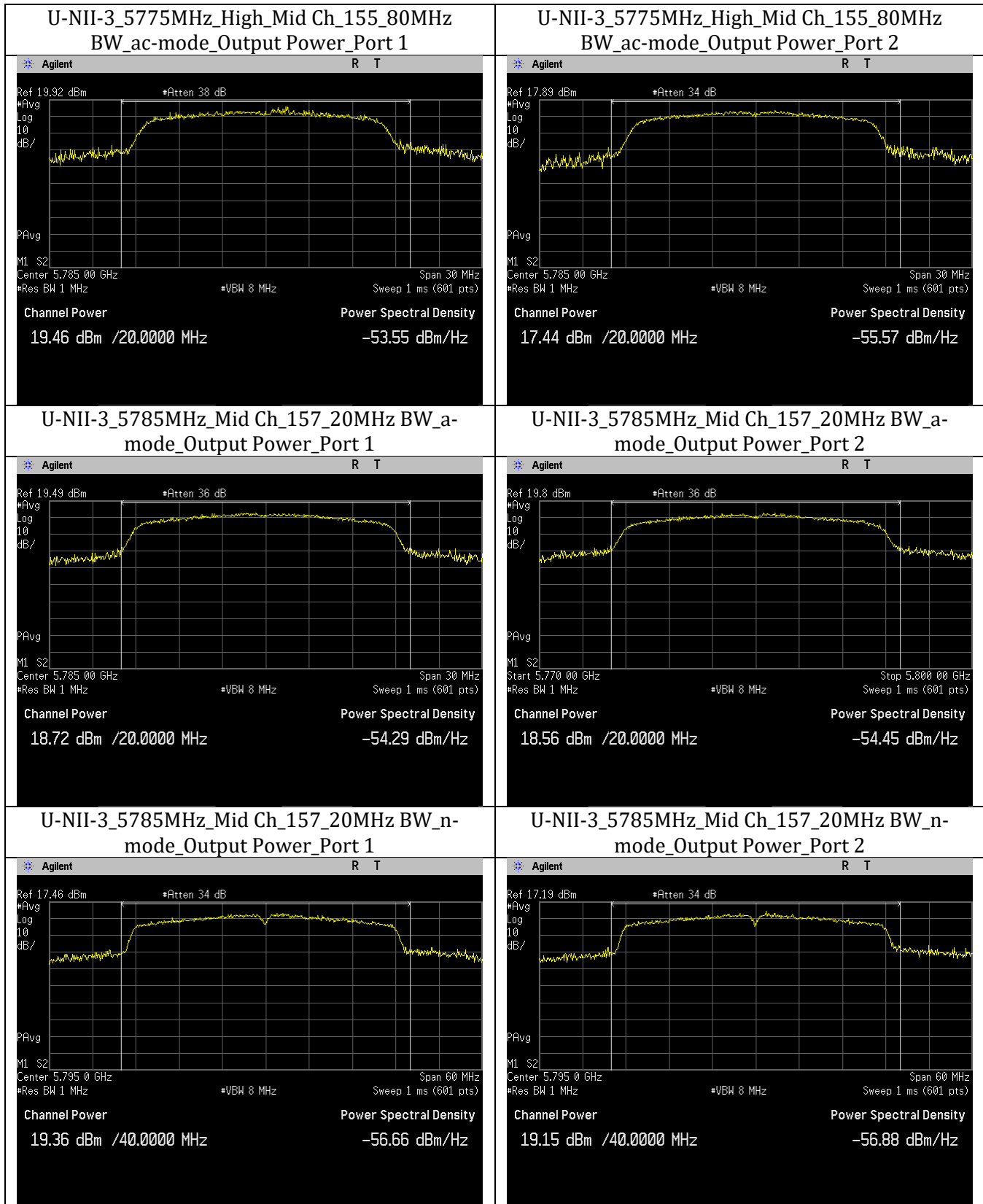


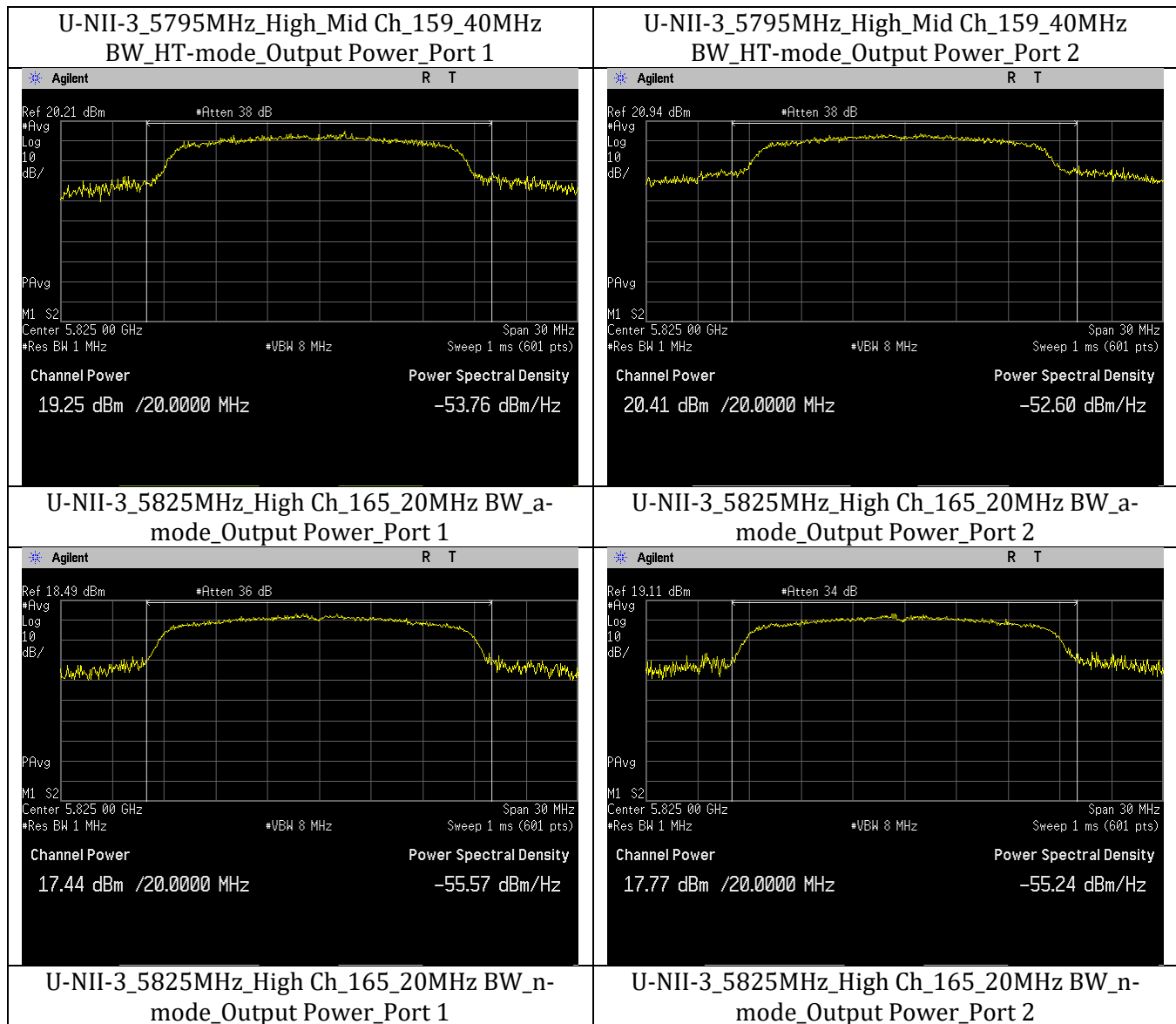
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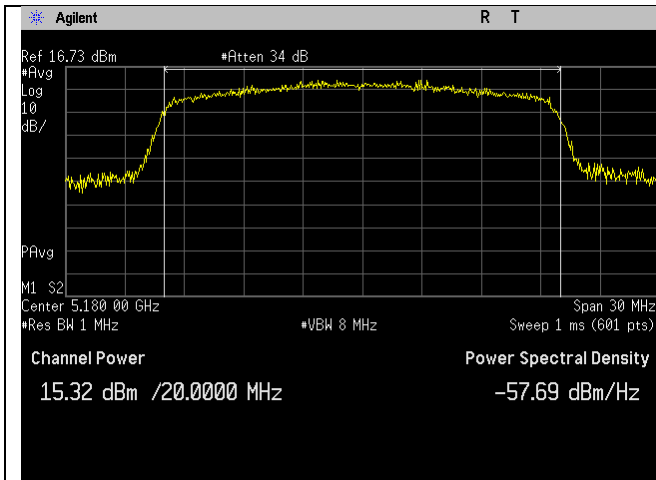


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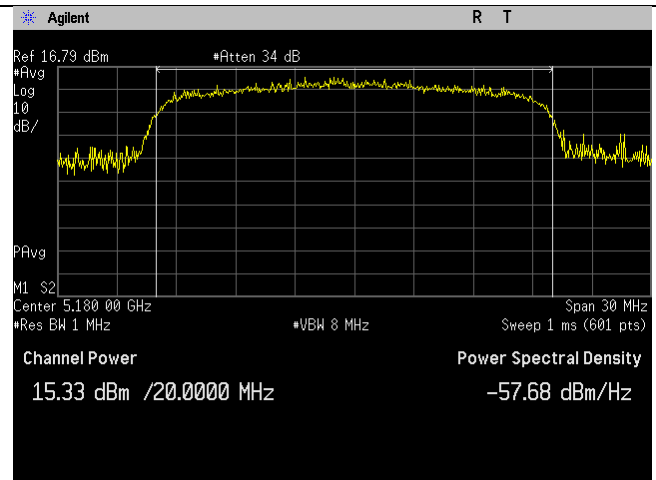




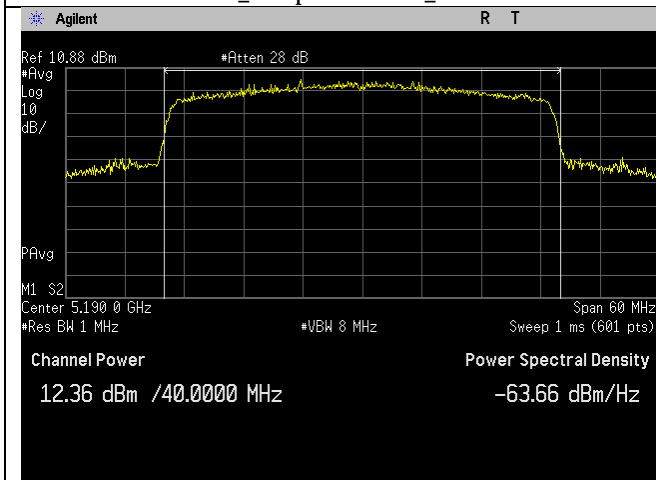




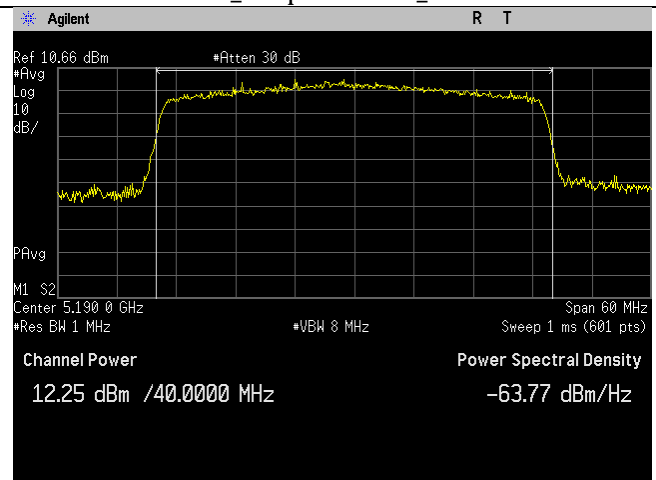
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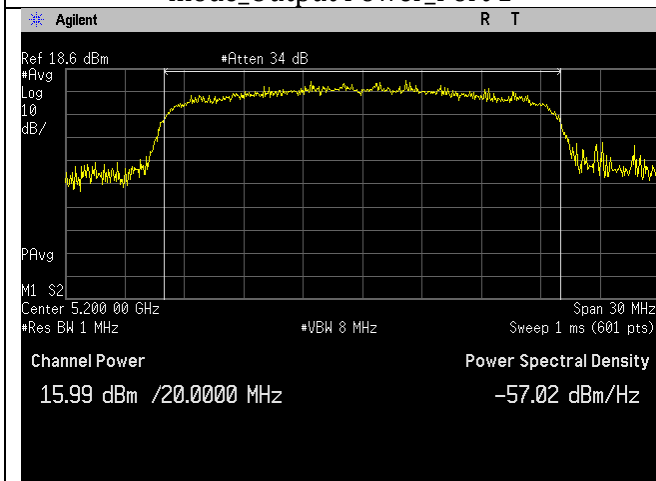
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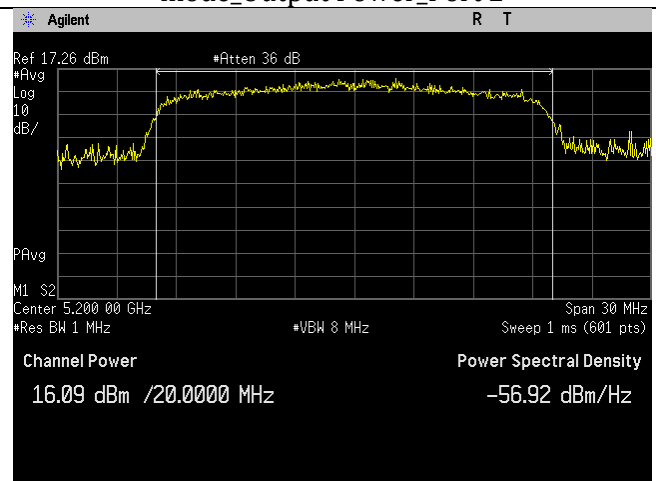
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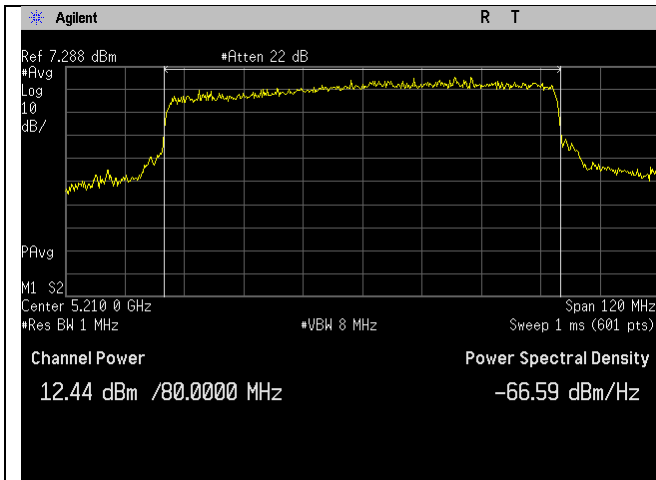
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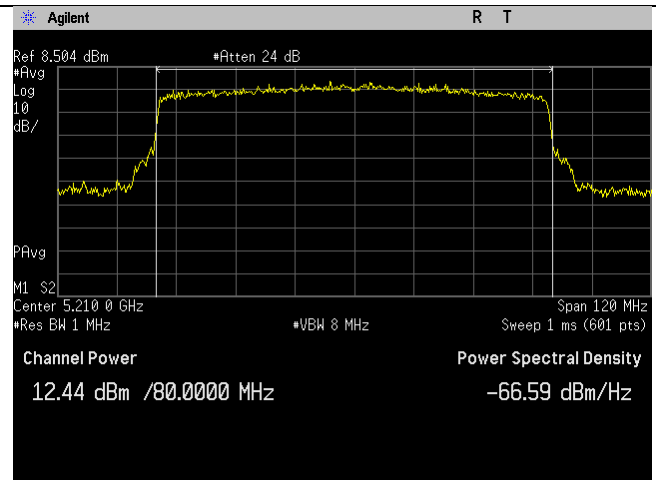
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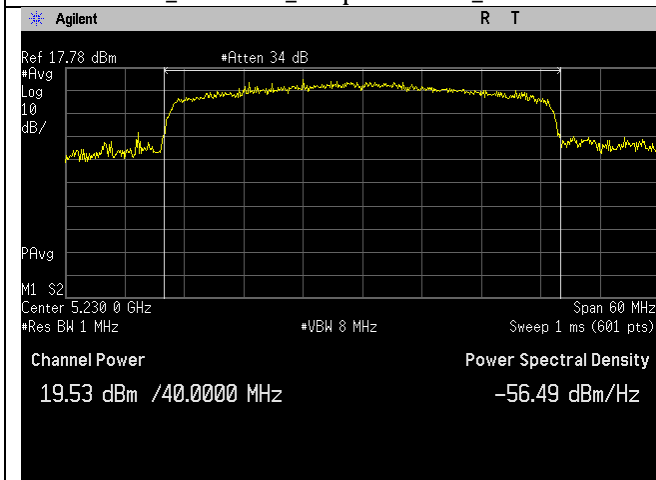
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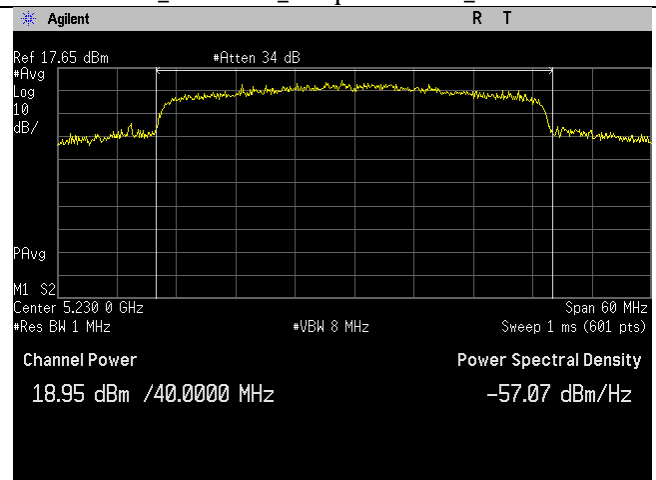
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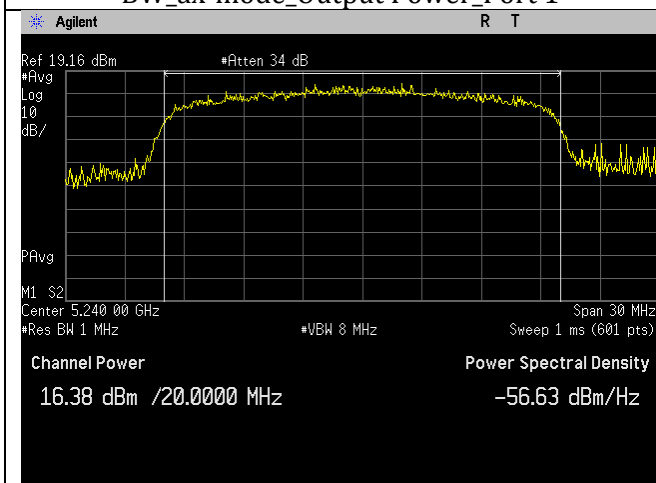
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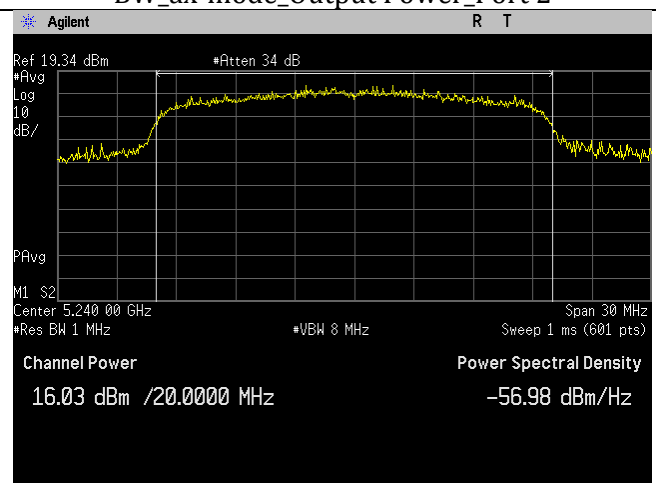
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BW_ax-mode_Output Power_Port 1



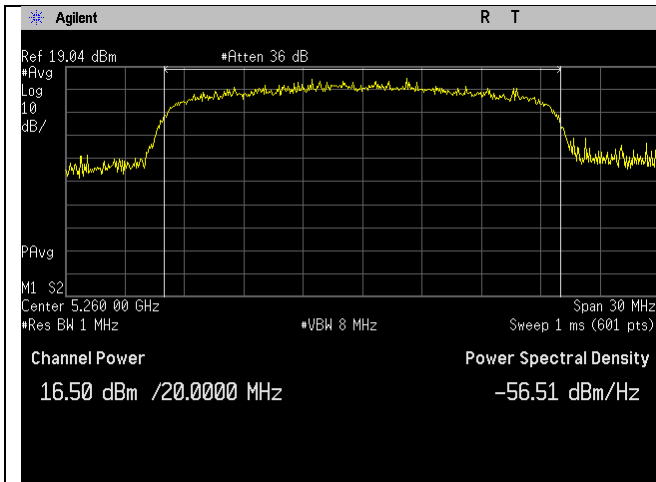
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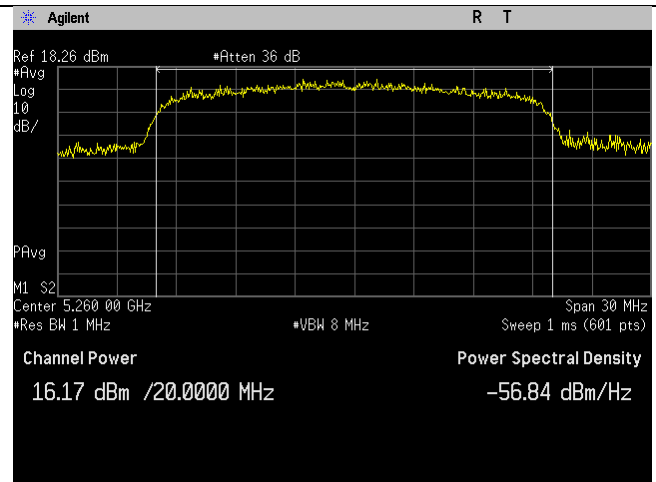
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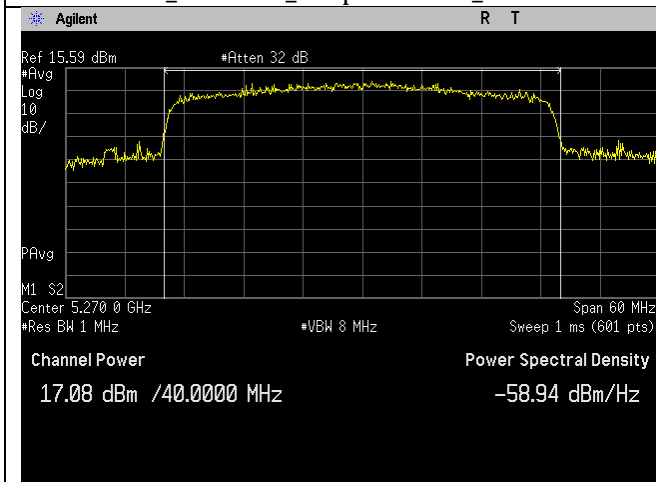
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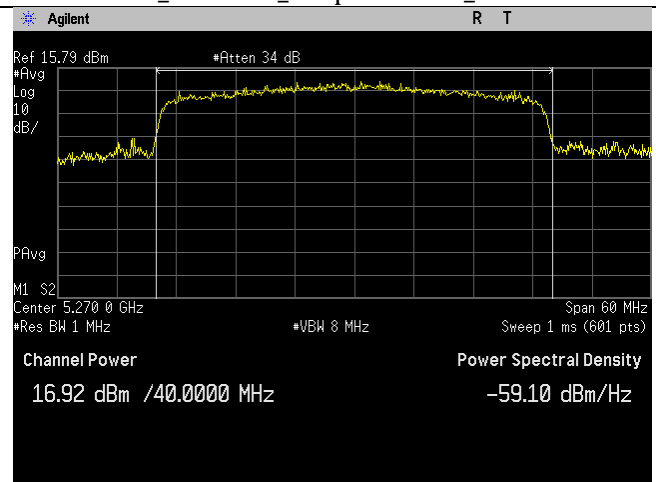
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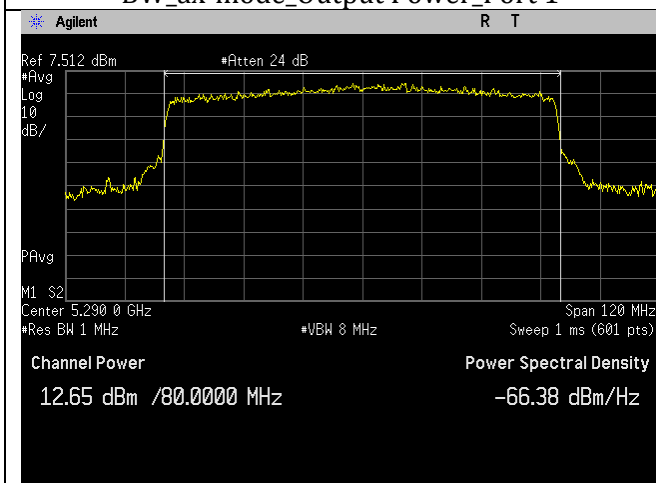
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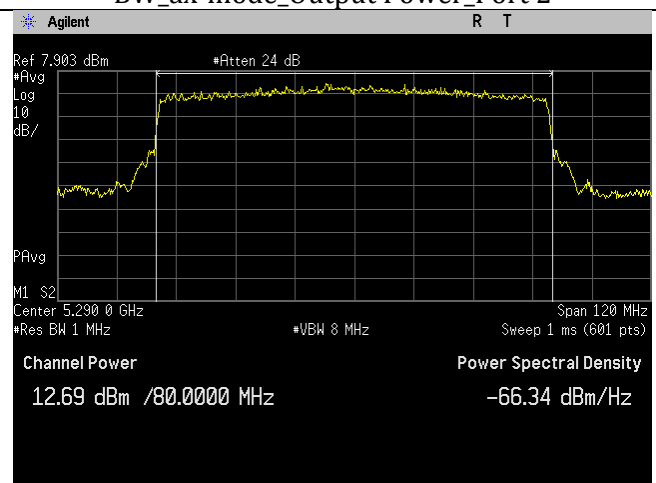
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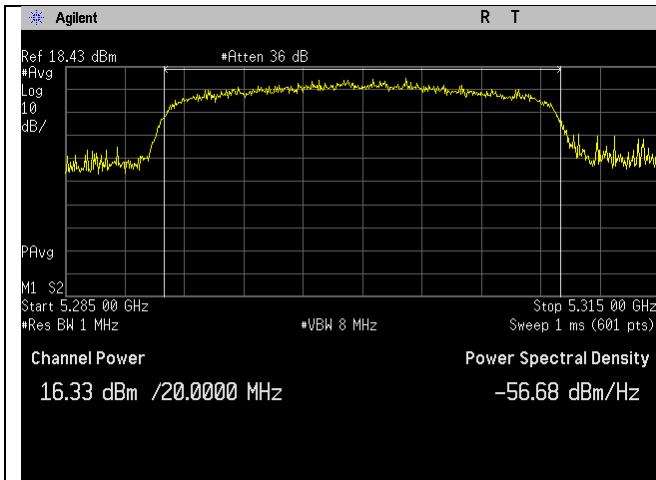
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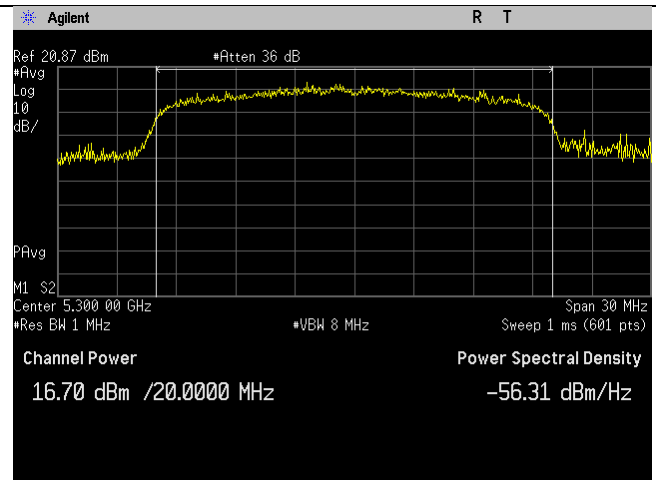
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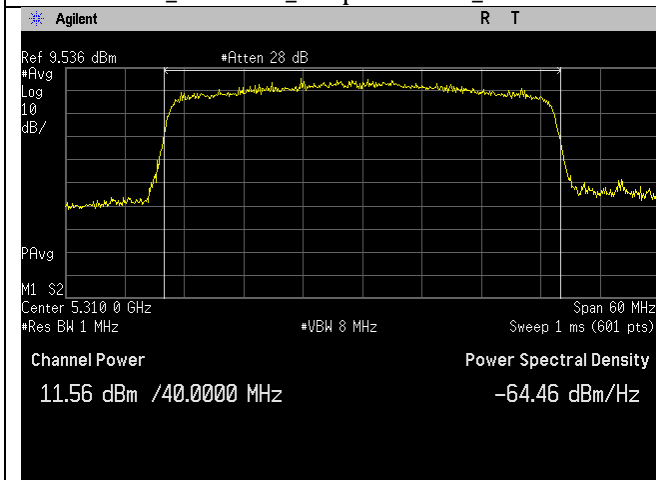
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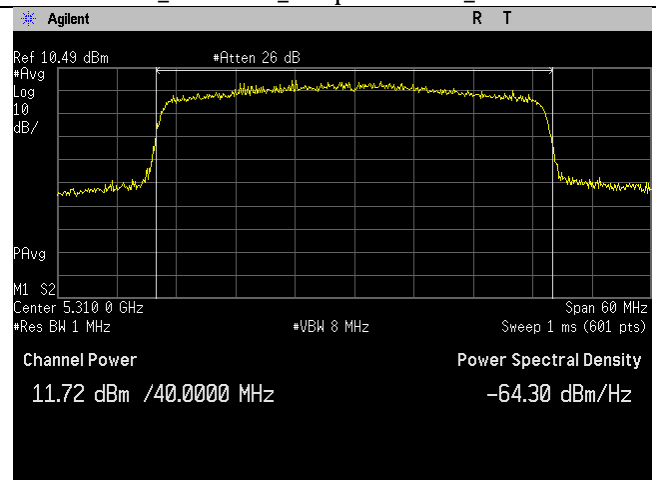
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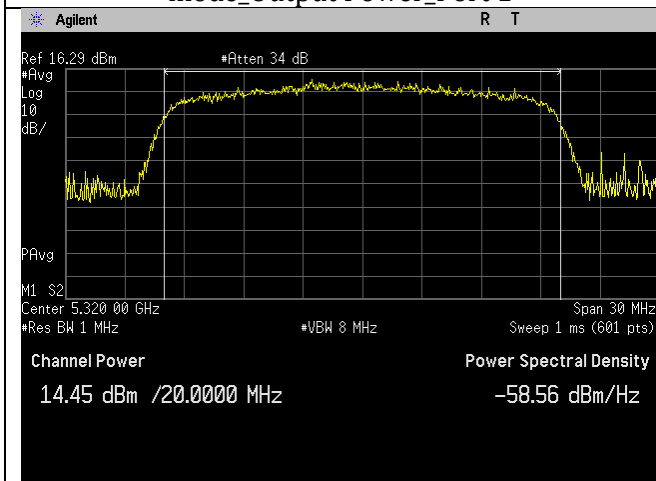
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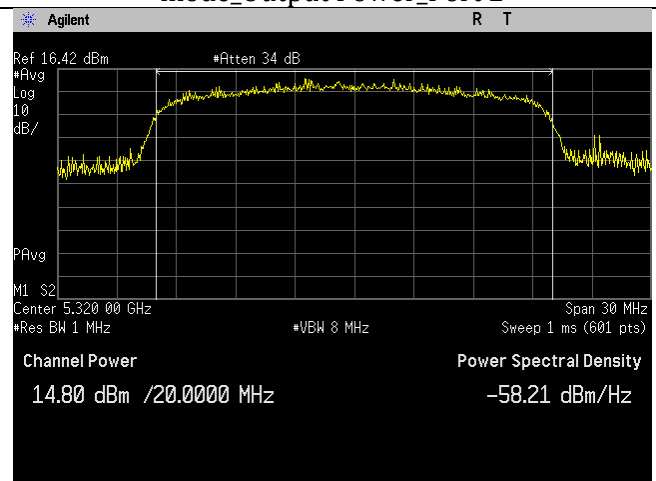
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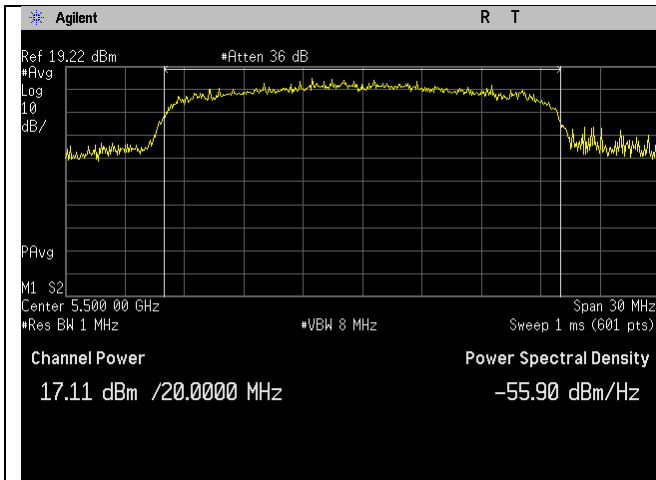
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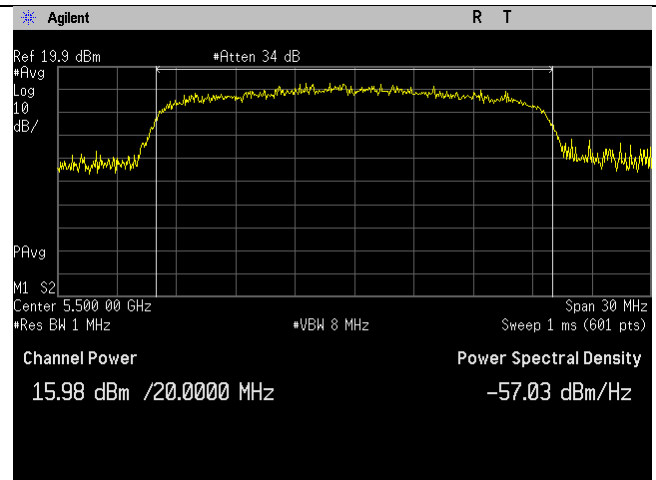
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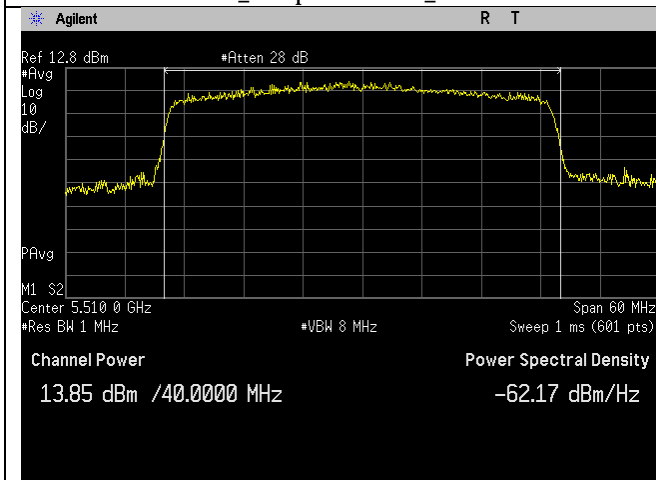
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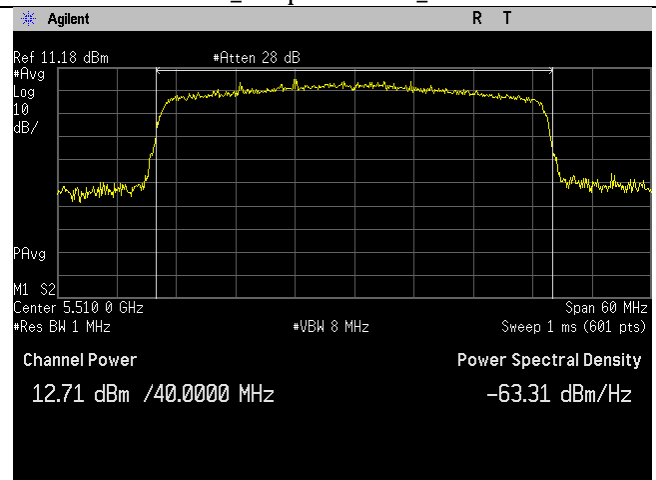
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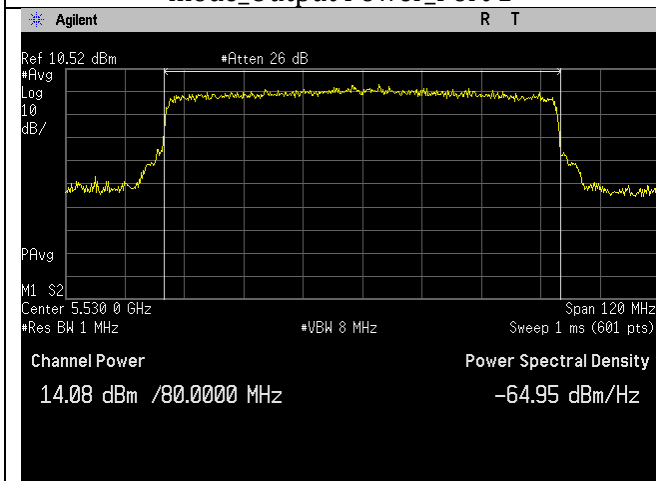
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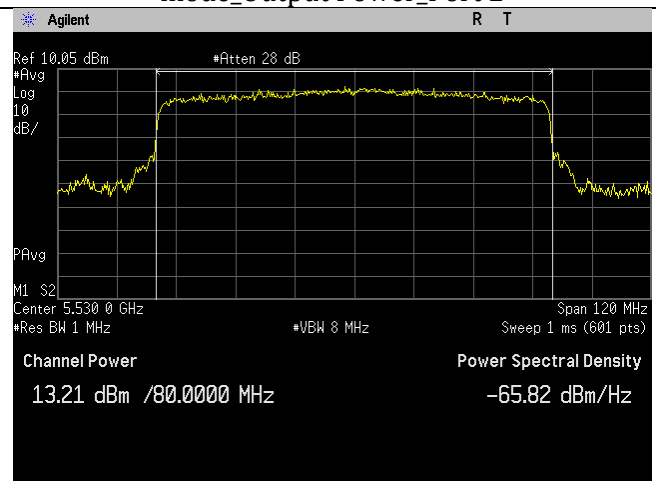
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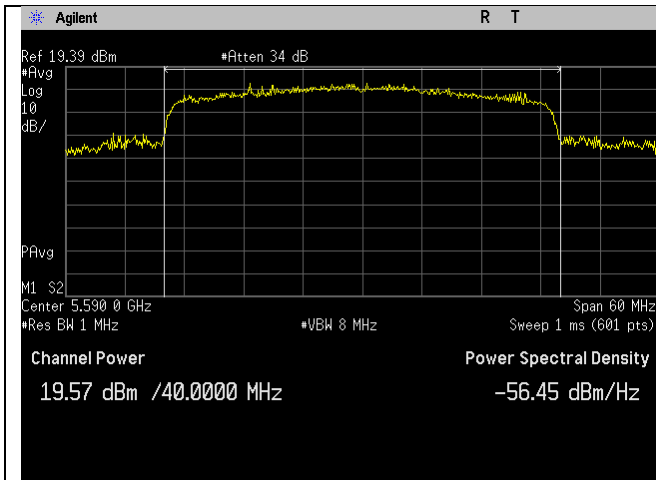
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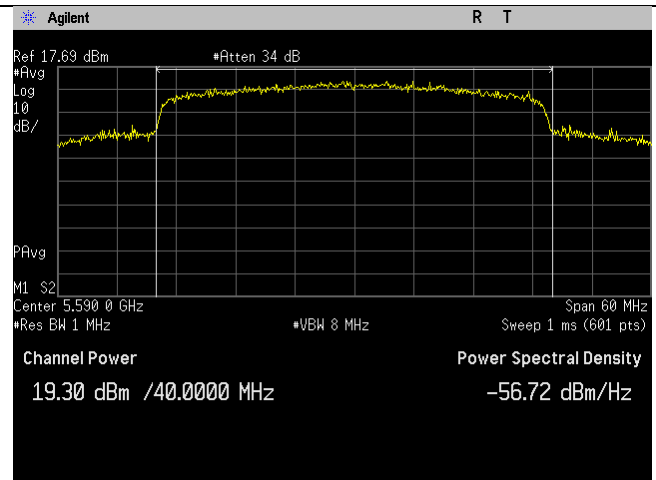
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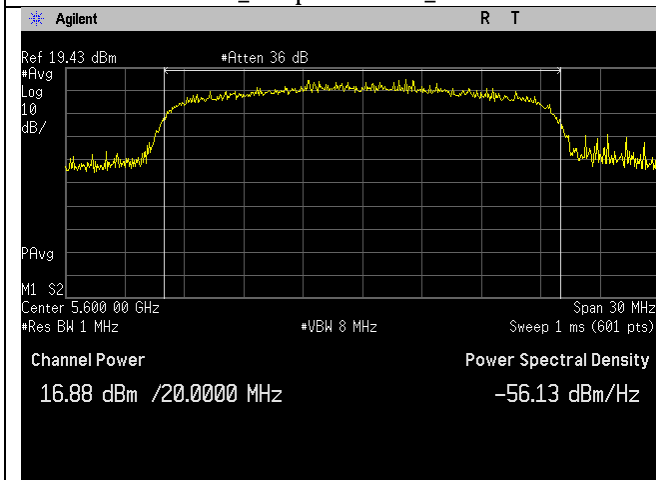
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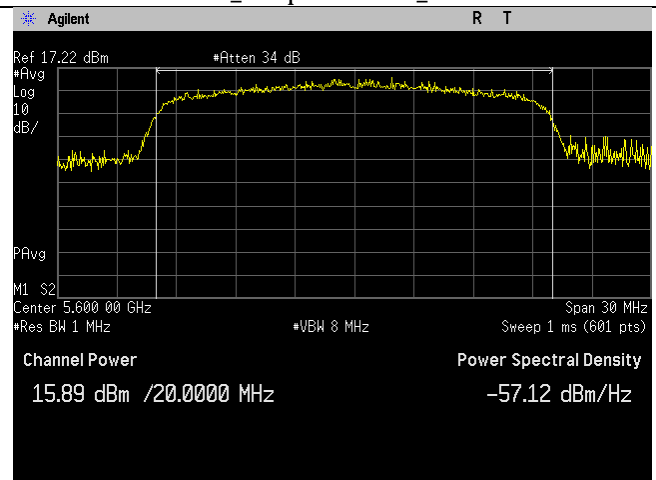
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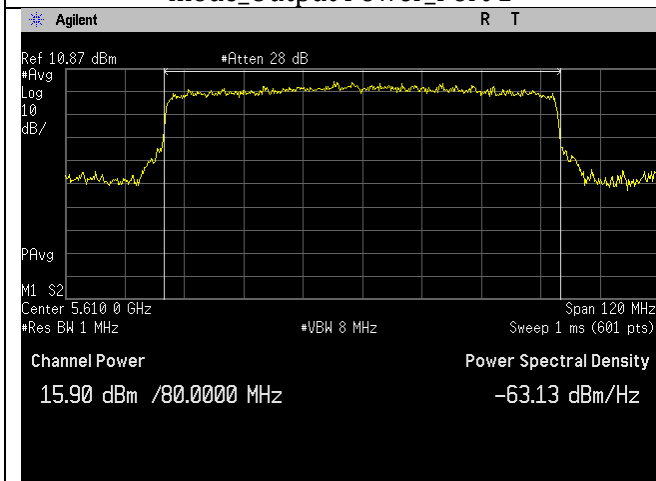
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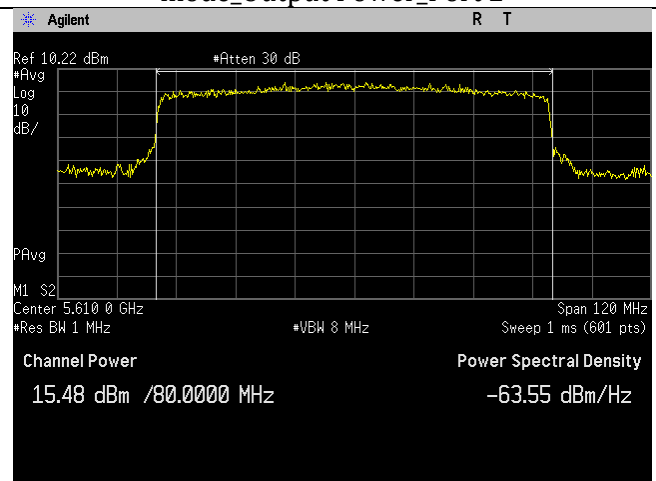
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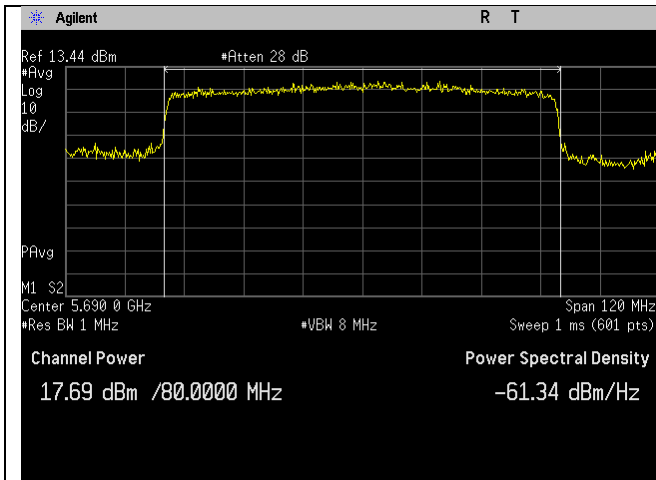
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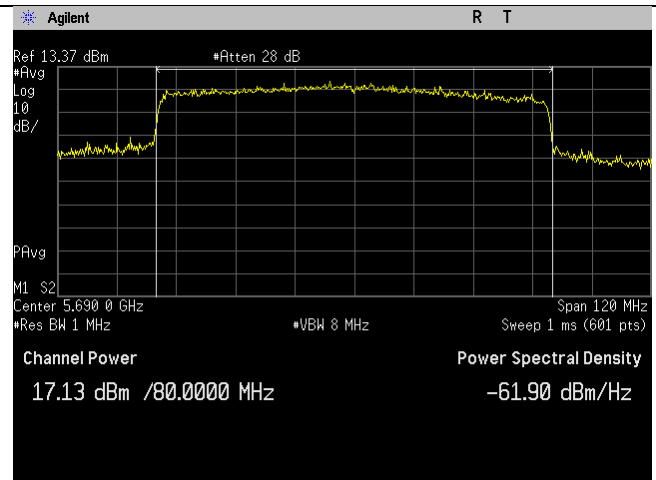
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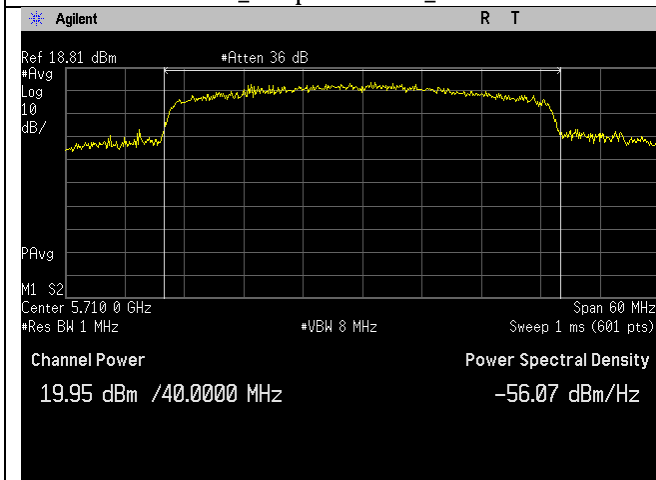
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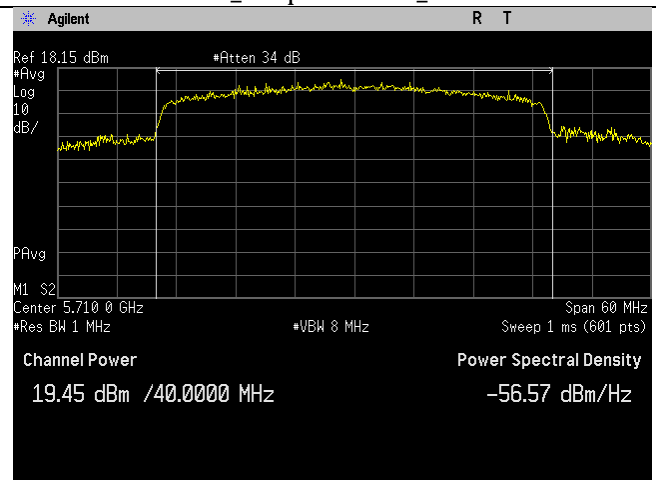
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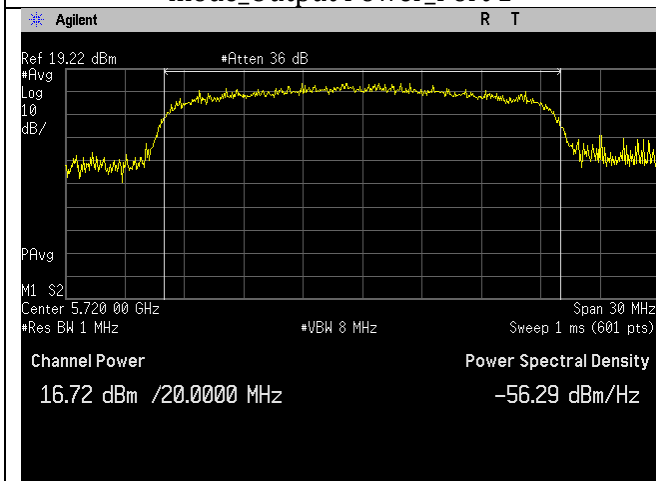
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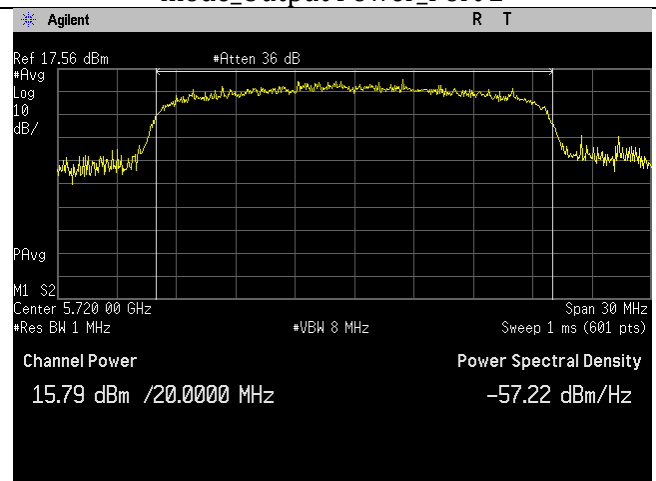
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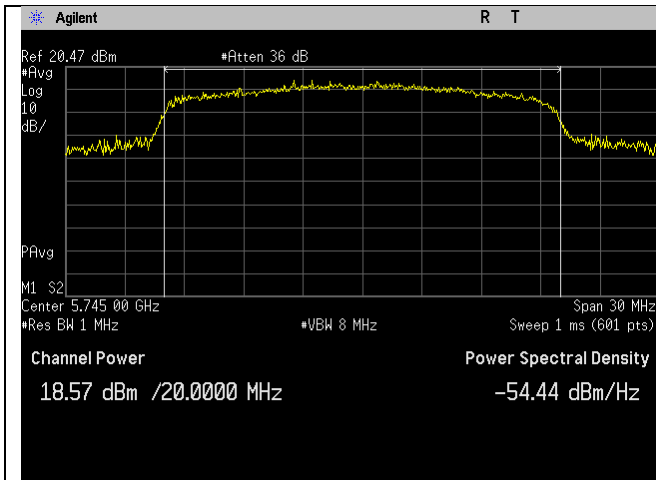
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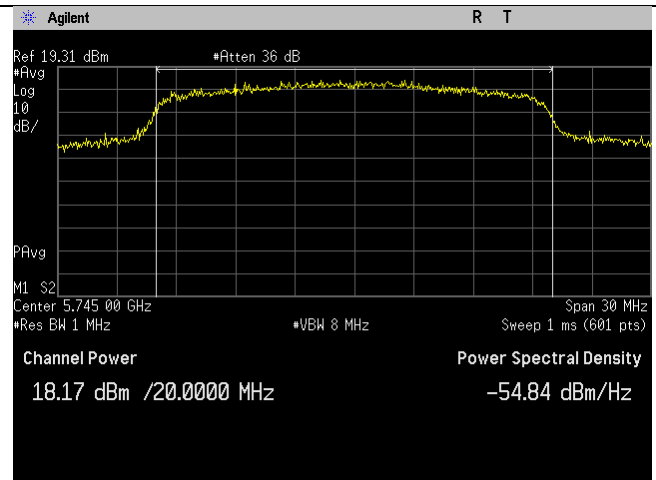
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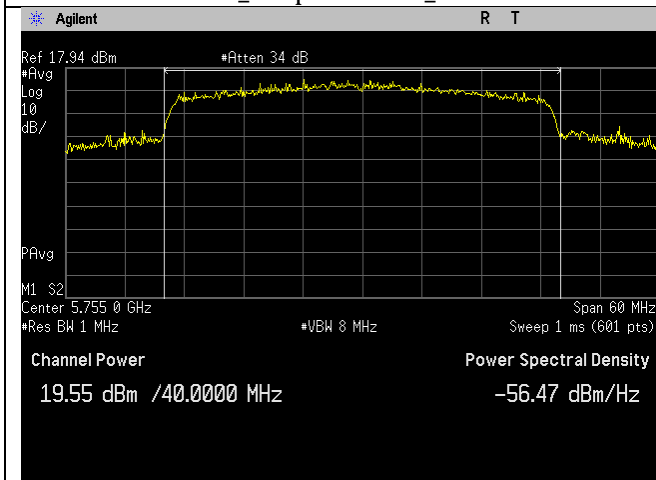
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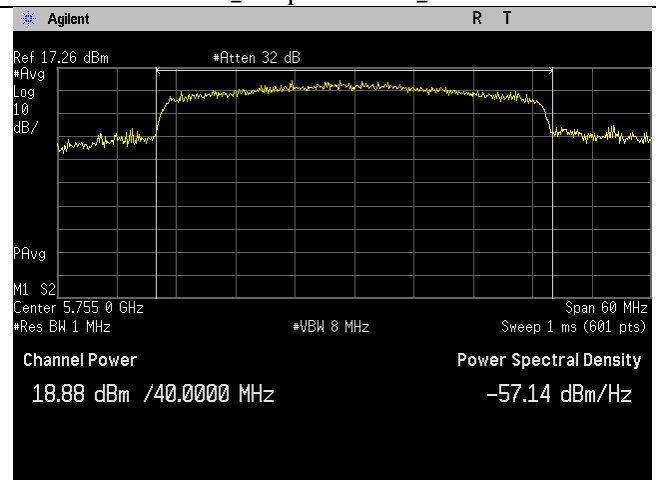
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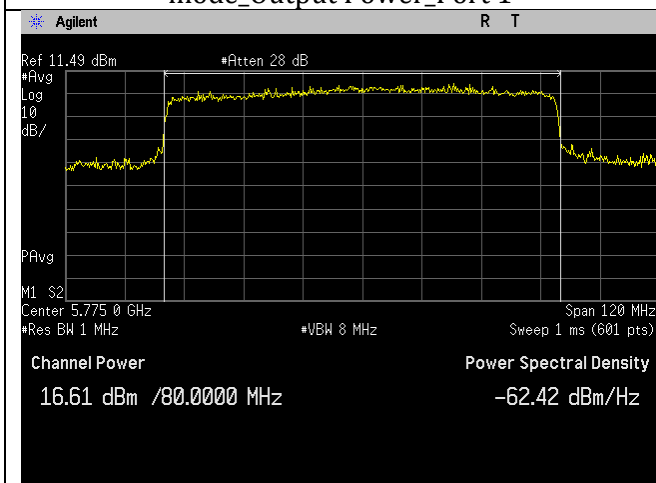
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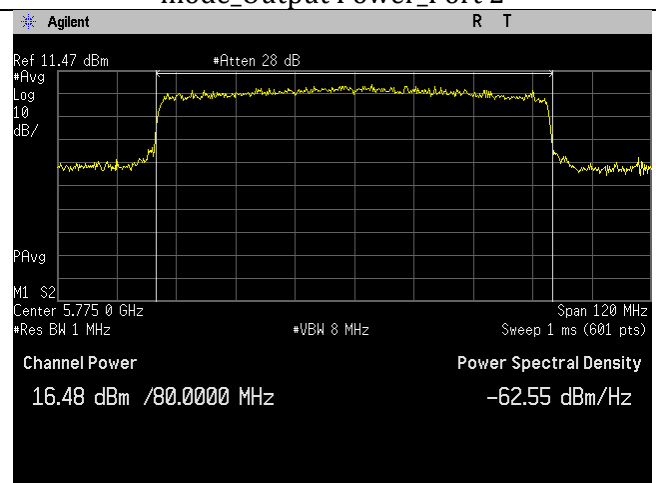
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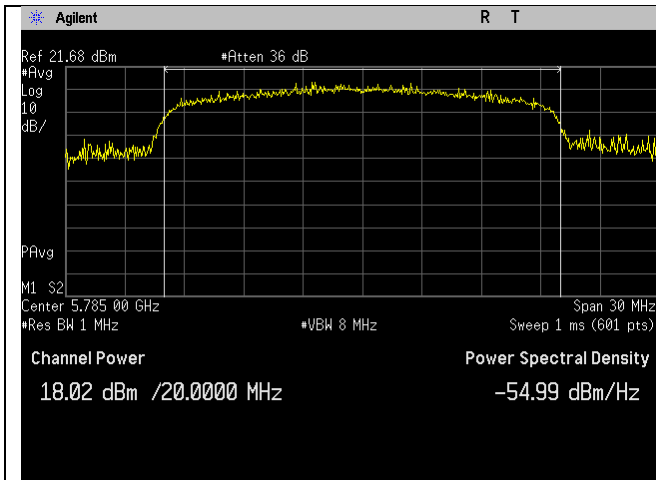
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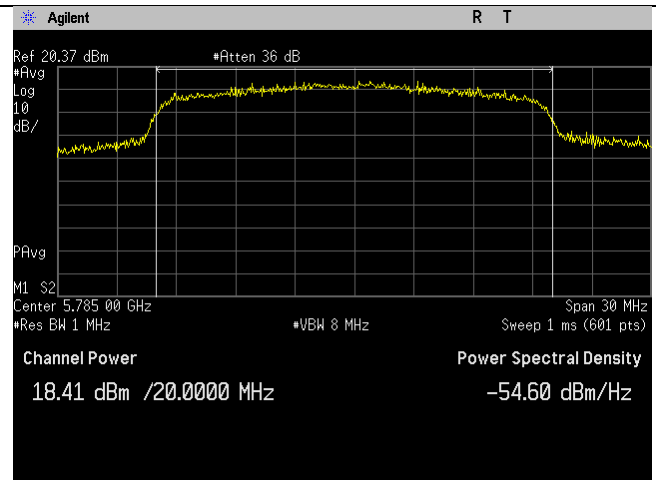
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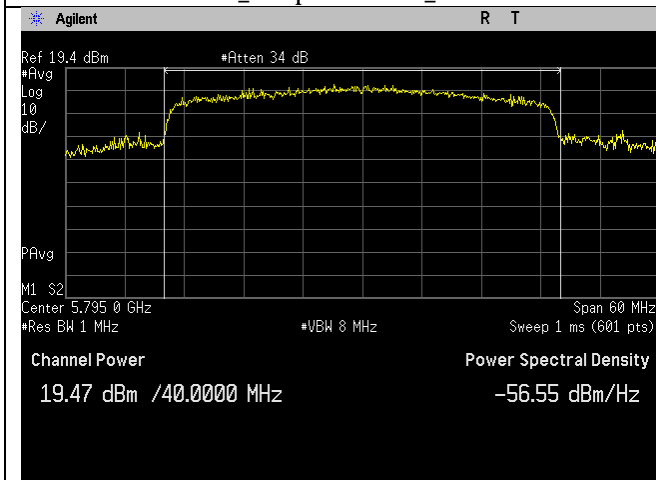
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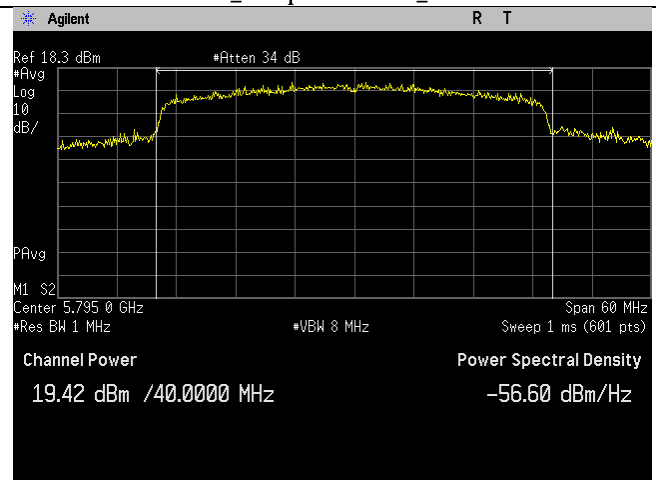
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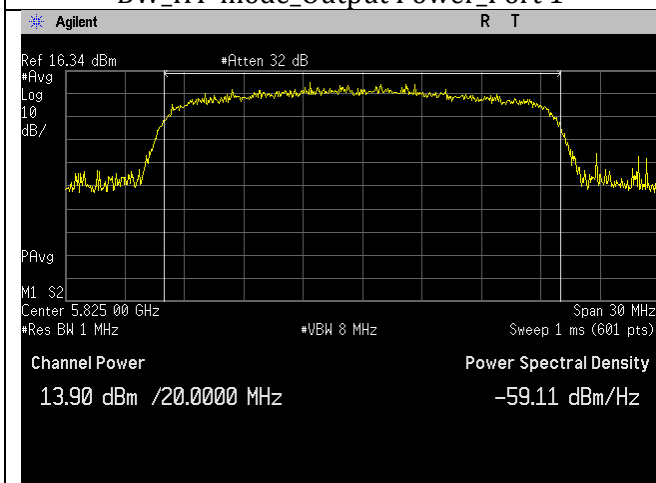
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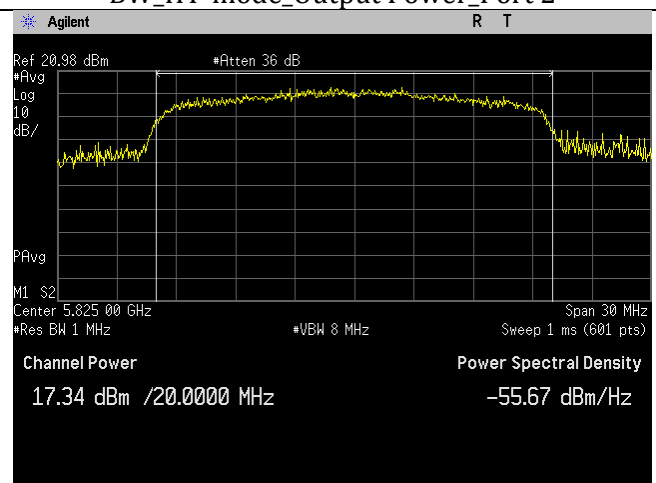
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U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_HT-mode_Output Power_Port 2



U-NII-3_5825MHz_High Ch_165_20MHz BW_ax-mode_Output Power_Port 1



U-NII-3_5825MHz_High Ch_165_20MHz BW_ax-mode_Output Power_Port 2

