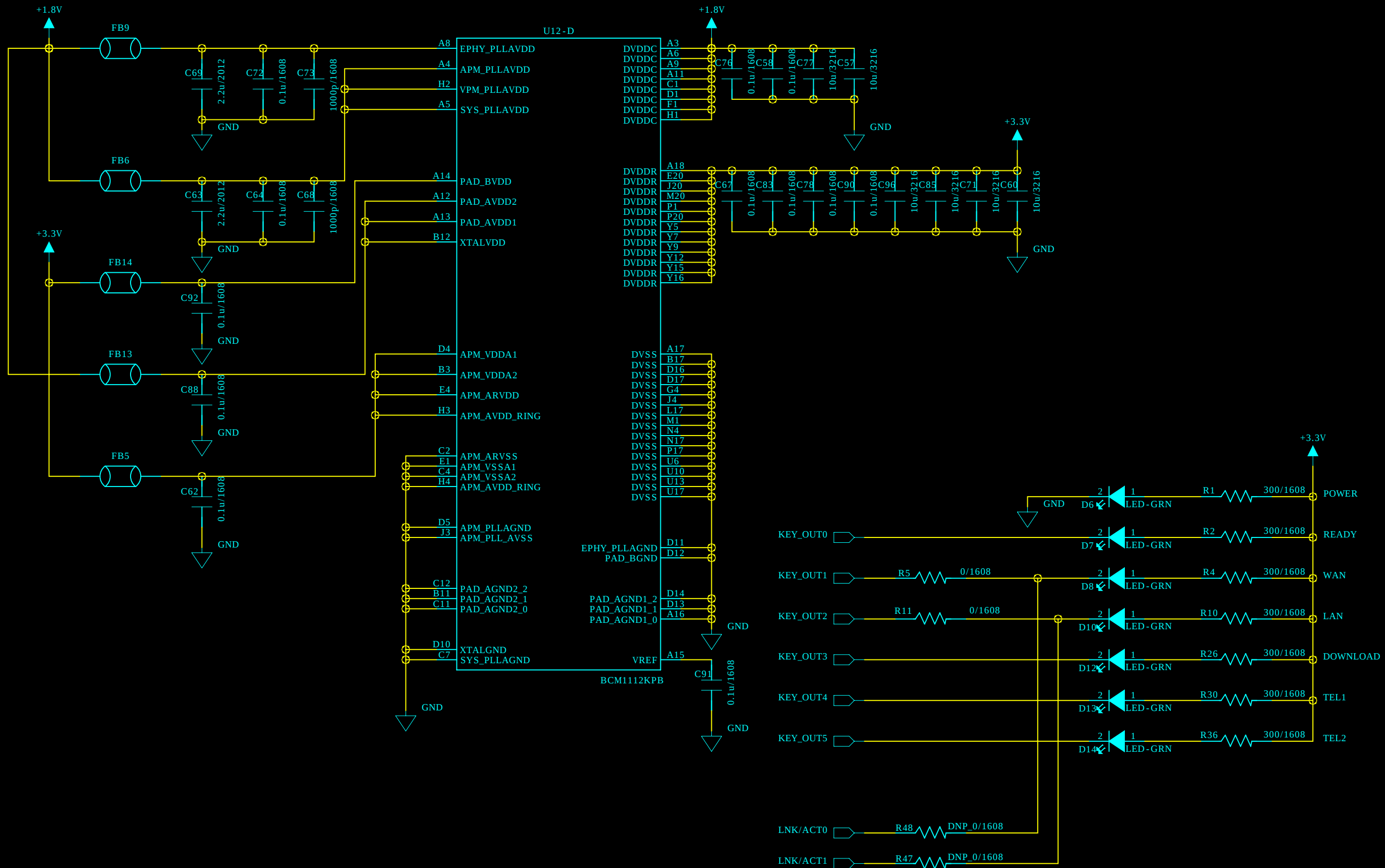


DOC. NAME	HYC -G302 -A -PR3A CIRCUIT DIAGRAM	DOC. NUMBER	HYC -G302 -A -0002	1 / 6
				DESIGNED BY : MOONBOK KANG
				CHECKED BY :
				APPROVED BY : JEFF HWANG
				DATE : 2006.04.21
				VERSION : PR3A
				NOTE :

NOTE :



DESIGNED BY :
MOONBOK KANG

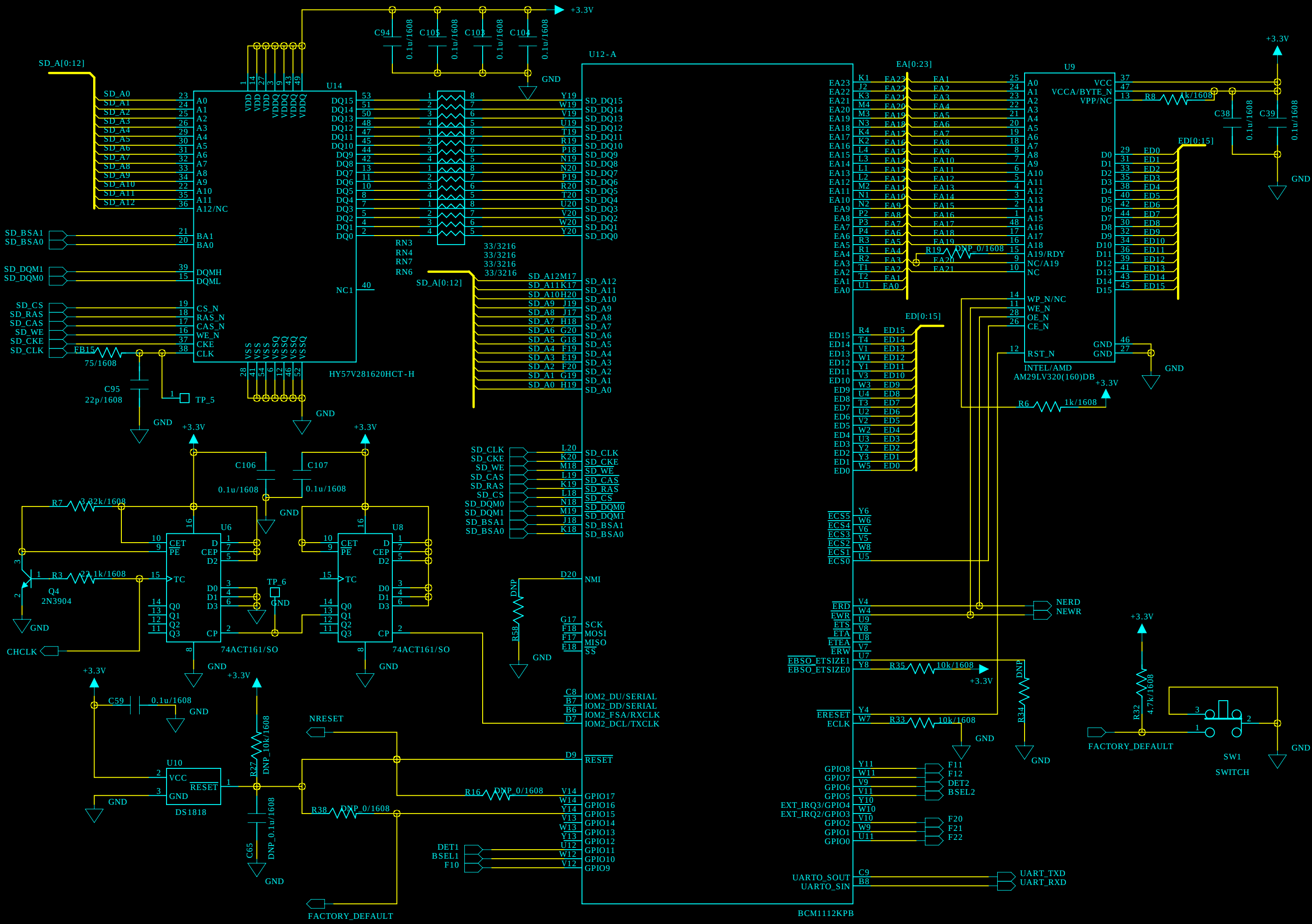
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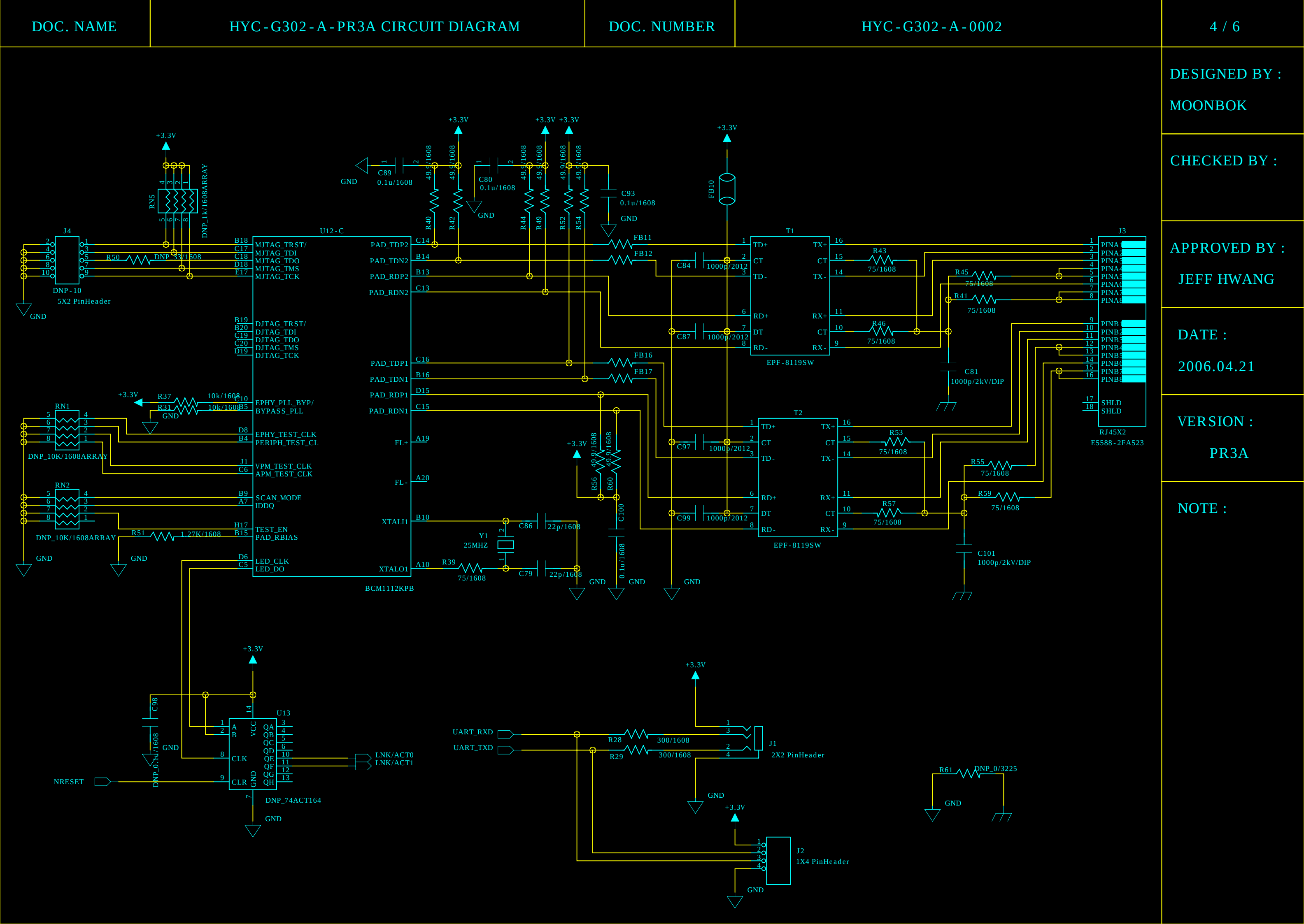
APPROVED BY :
JEFF HWANG

DATE :
2006.04.21

VERSION :
PR3A

NOTE :

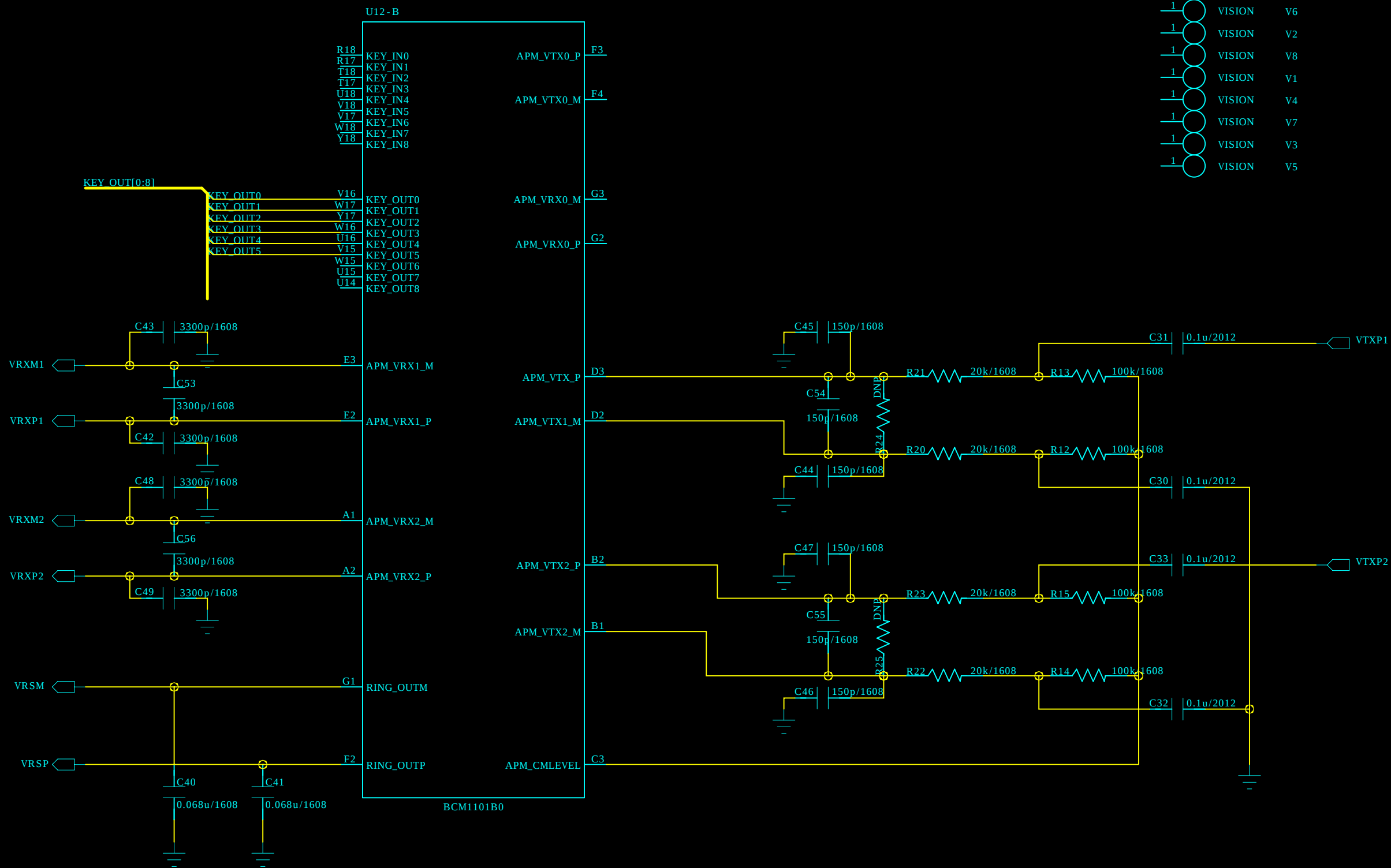


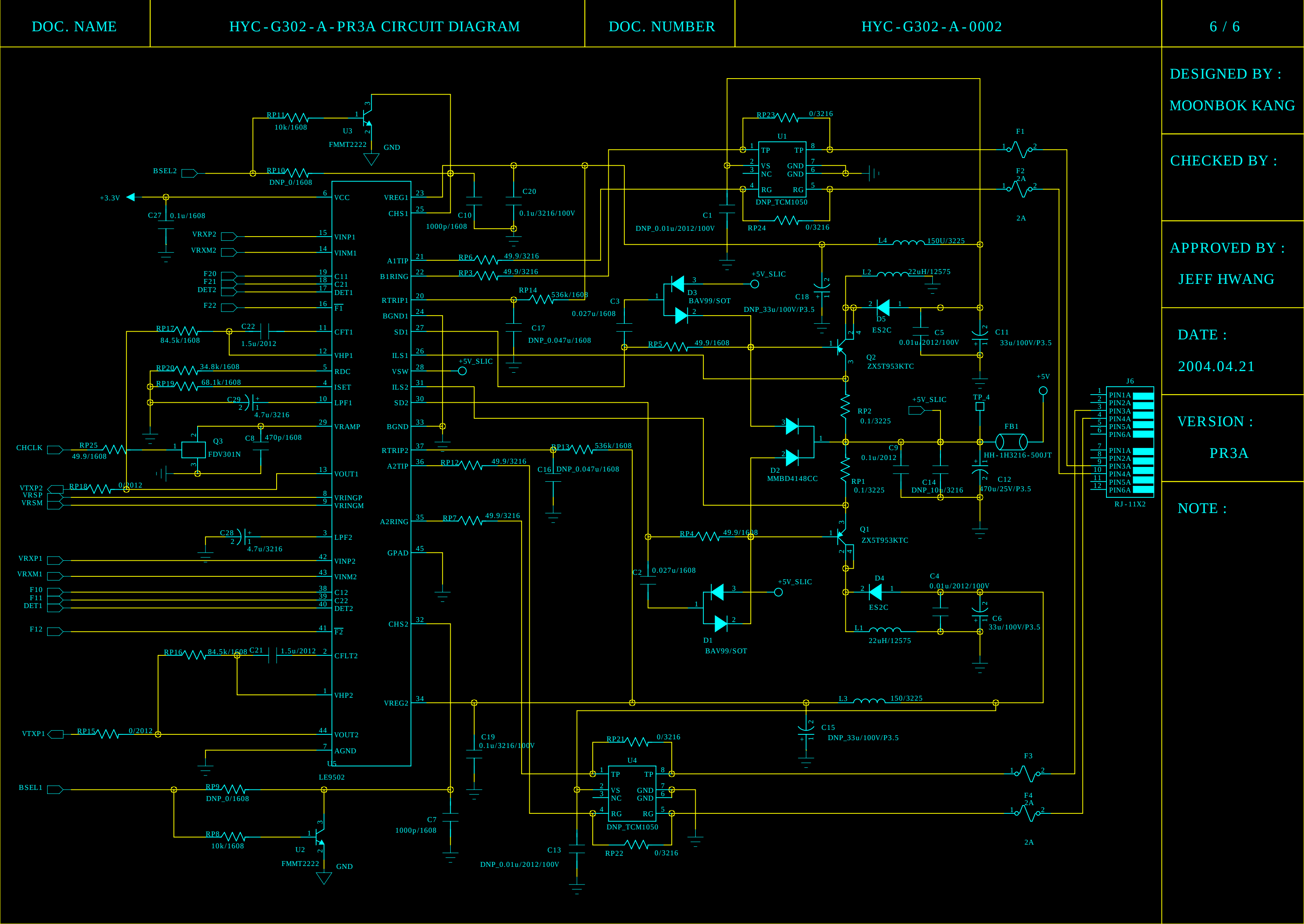


CHECKED BY :

DATE :
2006.04.21

NOTE :





Schematic

	NAME	SIGNATURE	DATE
DESIGNED BY	MOONBOK KANG		
CHECKED BY			
APPROVED BY	JEFF HWANG		

PROJECT	HYC - G302 - A			REVISION	PR3A
DOC. NUMBER	HYC - G302 - A - 0002	DATE	2006.04.21	TOTAL PAGES	6
DEVELOPMENT LEVEL	FEATURES	PL Rev.	PCB Rev.	S/W Rev.	ECN
DPP	PCB size : 130(L) x 130(W) [mm]	PR1A	PR1A		
DPP	PCB size : 130(L) x 120(W) [mm]	PR2A	PR2A		
DPP	PCB size : CHANGE LEFT SIDE ROUND	PR2B	PR2B		
DPP	PCB size : ADD A HALL	PR2C	PR2C		
DPP	5[V]/2[A] Input Power Solution	PR3A	PR3A		
EACH SHEET			REFERENCE		
P1 : POWER P2 : LED P3 : CPU/MEMORY P4 : WAN/LAN P5 : APM P6 : SLIC					