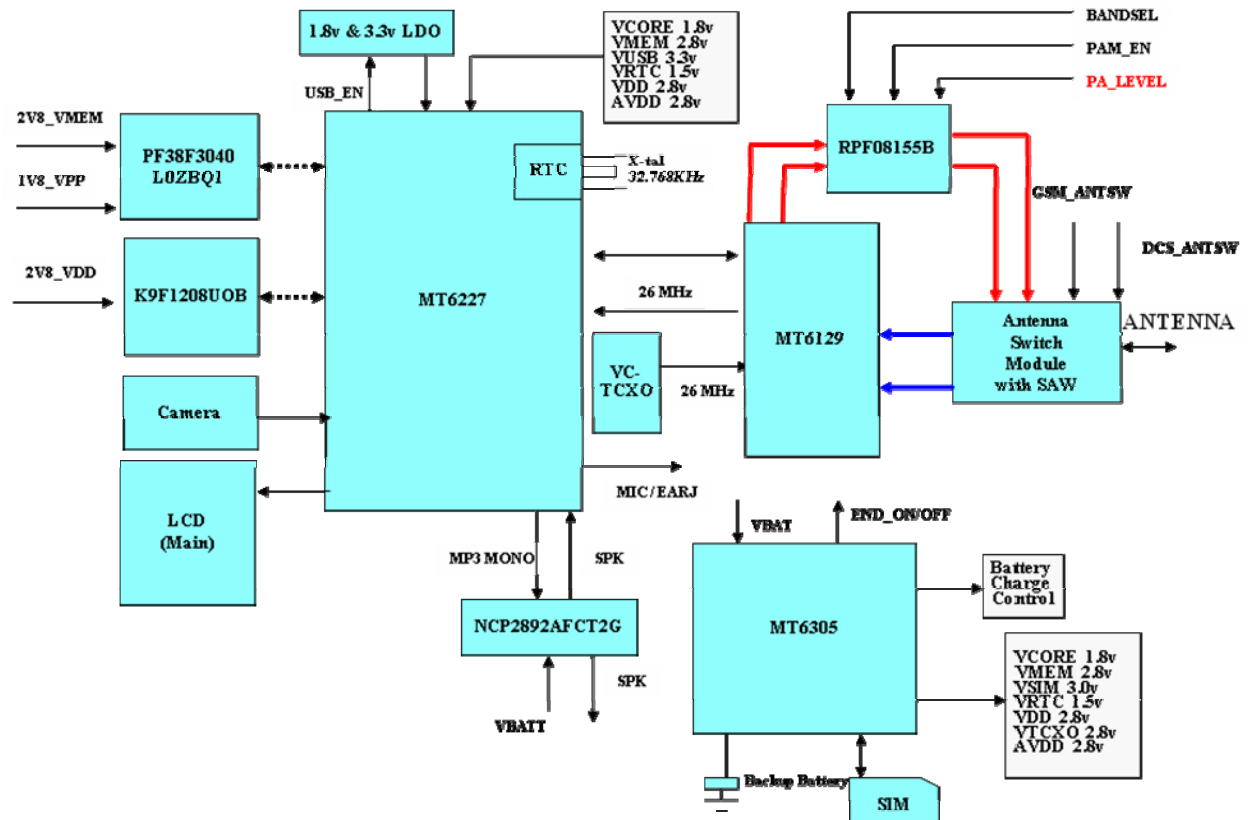


2. N120 BLOCK DIAGRAM



Introduction to Block Diagram

Logic part

MT6227

GSM GPRS Baseband Processor

MT6305

Handles all GSM Baseband Power Management

PF38F3040L0ZBQ1 [MCP]

128M 16bit Flash Memory and 32M 16bit Smart Combo RAM for Embedded System Control Processor

KT21-DCV28A-26.000MT [VC-TCXO]

Main Clock for Control Processor and RF Block, Subsystem.

SSP-T7 [32.768 KHz Sleep Crystal]

32.768KHz Clock for Sleep Mode Operation

TS414HIV01E [Back up Battery]

Back Up Battery to Storage Time Date Information on Power Off

IMT220G17G274C

Color LCD Module including 176 * 220 260K Color Main TFT LCD

NCP2892AFCT2G

1 Watt Audio Power Amplifier with Selectable Shutdown Logic Level

S-1711B1833-I6T1G [USB & Memory Dual Vcc]

High Speed Dual LDO Regulators

S-1711B1528-I6T1G [Camera Dual Vcc]

High Speed Dual LDO Regulators

RF PART

MT6129C [RF Transceiver IC]

Highly integrated RF Transceiver IC for multi-band GSM and GPRS.

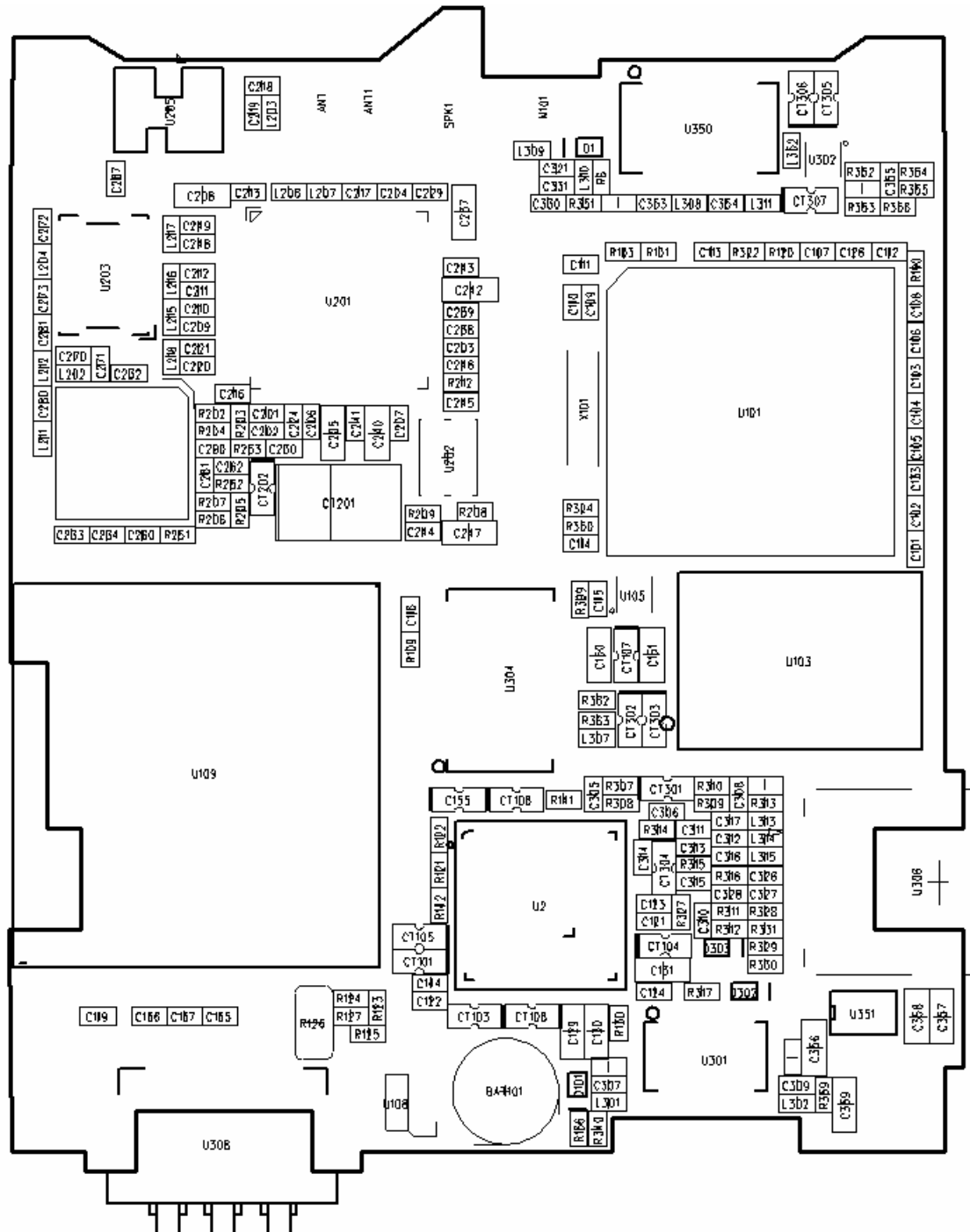
PF08155B [Power Amp Module]

Quad-Band GSM850 & EGSM 900 & DCS1800 & PCS1900 & GPRS RF Module

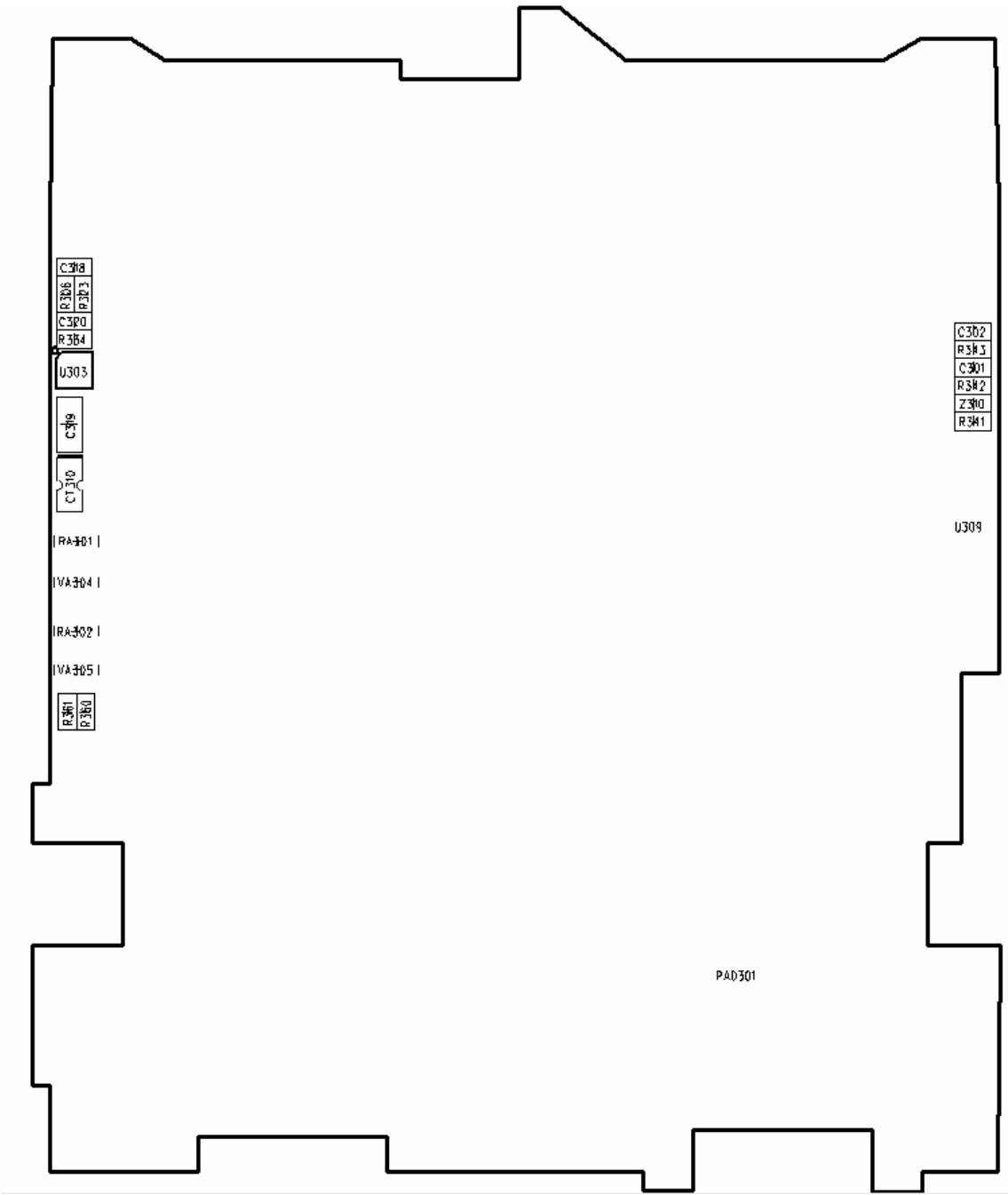
LMG004W-5155A [GSM850 & EGSM 900 & DCS1800 & PCS1900 Saw Filter]

GSM850 & EGSM 900 & DCS1800 & PCS1900 Quad-Band Front End Module

N120 Main PCB Top Side Layout



N120 Main Bottom Side Layout



N120 SUB PCB Top Side Layout

