

POWER CIRCUIT

The schematic diagram illustrates the power circuit for a microcontroller (U1, XC6401EE25MRN). The circuit includes a VCC supply, a reset button (R0402), and various capacitors (C1, C2, C3, C23, C24, C9, C32). It also shows test points (TP12, TP13, TP14, TP15) and power pads (P3, P4, P5, P6).

Components and Connections:

- Microcontroller (U1):** XC6401EE25MRN. Pins 1 (ENI), 2 (Vin), 3 (EN2), 4 (VOUT2), 5 (VSS), 6 (VOUT1) are shown.
- Resistor (R0402):** Connected between VCC and pin 2 (Vin).
- Capacitors:**
 - C1: 1uF/16V, connected between VCC and pin 6 (VOUT1).
 - C2: 470pF/50V, connected between VCC and pin 6 (VOUT1).
 - C3: 1uF/16V, connected between VCC and pin 1 (ENI).
 - C23: 22uF/10V, connected between VCC and pin 1 (ENI).
 - C24: 1uF/16V, connected between VCC and pin 4 (VOUT2).
 - C9: 470pF/50V, connected between VCC and pin 4 (VOUT2).
 - C32: 2.2uF, connected between VDDk and ground.
- Test Points:** TP12, TP13, TP14, TP15.
- Power Pads:** P3, P4, P5, P6.
- Other Labels:** VCC_GSP, VCCRF, VDDk.

Optional

VDDk VDD_RTC

R6 100R

C27 2.2uF

U6

NC2 OUT 3

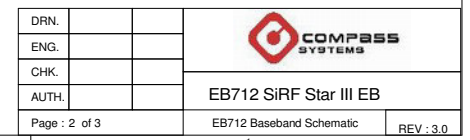
IN 2

NC1 GND 1

S817B15AMCCWH

R5 R0402 R7

BATTERY BATTERY



RF OPTION 2

FOR MODULE
PAD OUTPUT
USE

P1
PAD-TP1

P2
PAD-TP1

RF CIRCUIT

GSC3f RF Section

CLOCK CIRCUIT

DRN.			 COMPASS SYSTEMS	
ENG.				
CHK.				
AUTH.				
			EB712 SiRF Star III EB	
Page : 3 of 3			EB712 RF Schematic	REV : 3.0